Schedule

Team: Neural Network – Theta
Members: Thibaut Lanois, Wen Wen, Jieming Pan

Feb 6, 2017

	30-Jan	6-Feb	13-Feb	20-Feb	27-Feb	6-Mar	13-Mar	20-Mar
Software Development	Convolution layer in C		Testing					
	Pooling layer in C							
	Fully connecting layer in C							
	Training in Matlab	Weight extraction		Testing				
	Divide into batch	Controller		Testing				
		Image to matrix						
Hardware development		Convolution layer and Pooling layer on DSP Slice			Test bench			
		Sigmoid function in LUT & Fully connected			Router DSP register		Integration	Synthese + Place routes
	Research Router & Controller		Axi Bus		Router buffer – Block memory			
					FSM ++			