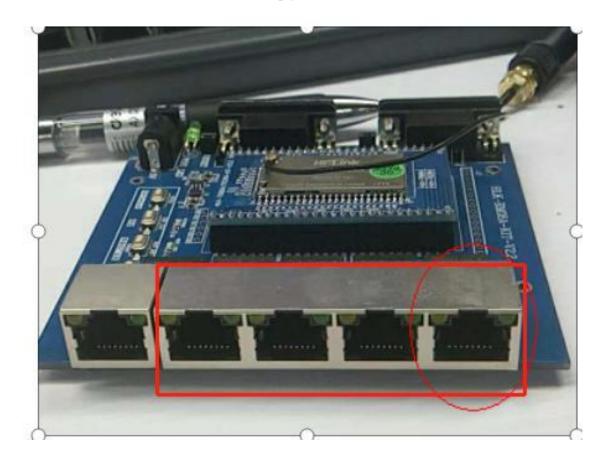
# Openwrt firmware module upgrade method

The modules of openwrt can use the following two firmware upgrade methods when the firmware is delivered from the factory

#### A. Upgrade firmware through network interface under uboot

Upgrade network interface: one of P1~P4 network interfaces needs to be used, and P0 interface cannot be upgraded



PC uses serial port to connect to HLK7688 debugging serial port Serial port setting as:

Basic Serial settings				1	
Serial port * COM10	(USB Serial Po	ort (COM10	)))	Speed (bps) * 5	7600 🗸
support MobaXterm by s	ubscribing to th	ne Professio	onal edition here: http://m	nhaxterm mohatek n	et
Advanced Serial settings	Termina		🜟 Bookmark settings		
(6)					
Serial engine: PuTTY	(allows manu	ual COM por	rt setting)	~	
Data bits 8	~)				
Ston bits 1	V		ed to transfer files (e.g. rout		
Parity None	~		ation file), you can use Mob led TFTP server	aXterm	
Flow control None	~	"Server	rs" window> TFTP :	server	,50
	S				
Reset default					

When powering on and starting, it will count down. At this time, press 2 on the keyboard to upgrade the firmware through TFTP

Prompt whether to erase Linux in Flash Type: y

```
2: System Load Linux Kernel then write to Flash via TFTP.

Warning!! Erase Linux in Flash then burn new one. Are you sure?(Y/N)
```

Specify parameters for TFTP:

Input device IP: device IP (board IP)

Input server IP: computer IP (set on PC network card)

Note: The two IPs must be in the same network segment, and the IPs cannot be the same

```
2: System Load Linux Kernel then write to Flash via TFTP.

Warning!! Erase Linux in Flash then burn new one. Are you sure?(Y/N)

Please Input new ones /or Ctrl-C to discard

Input device IP (10.10.10.123) ==:10.10.10.123

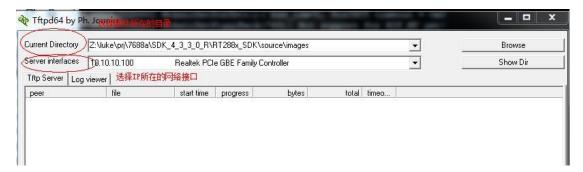
Input server IP (10.10.10.3) ==:10.10.10.3

Input Linux Kernel filename ==:firmware.bin
```

As shown in the figure above, change the IP address of the computer to 10.10.10.3

Fill in the firmware name to be upgraded: xxx.bin

Open TFTP on your computer



The default upgrade network ports for equipment shipment are P1~P4 ports. If Hailingke's backplane is used, there are five network ports in the backplane, and the separated network port is P0 port

Note: If the customer's board only leads out the P0 port, the default UBOOT cannot be upgraded through the network port

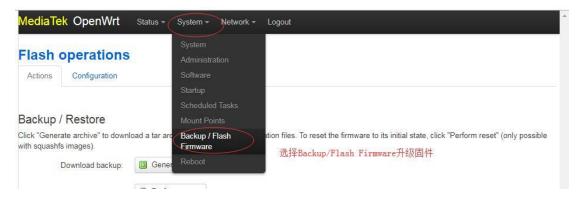


### If promoted

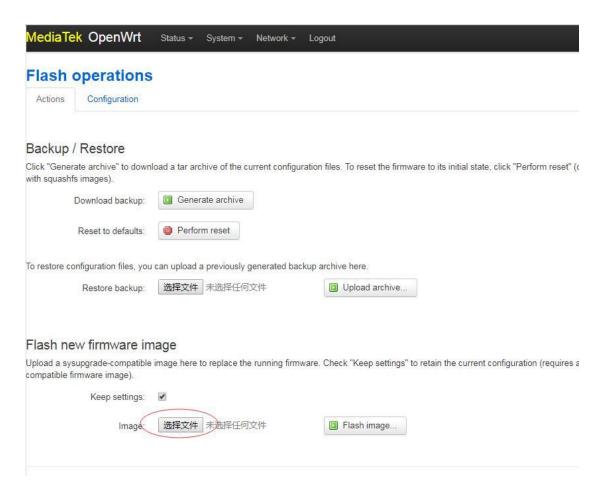
Please check the IP address of the PC and whether the network port is plugged correctly

#### B. Use web page upgrade

## Open Web Page:



Enter the firmware upg**rade** page: select the firmware, and click Flash Image



After checking the information, click Process to upgrade the firmware

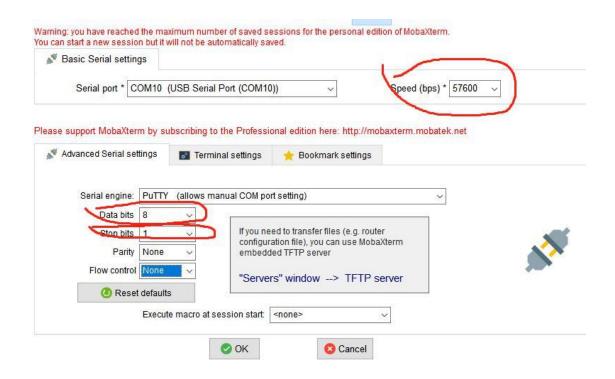
Flash Firmware - Verify
The flash image was uploaded. Below is the checksum and file size listed, compare them with the original file to ensure data integrity. Click "Proceed" below to start the flash procedure.
Checksum: 351bcbb260beeccc9665cfdac32779e7 Size: 5.25 MB (31.69 MB available) Configuration files will be kept.
Cancel Proceed
Powered by LuCl Trunk (unknown) OpenWrt Barrier Breaker 14.07

# Upgrade uboot

# **Uboot Upgrade uboot**

The PC needs to use the serial port to connect to the HLK7688 debugging serial port

Serial port setting:



When powering on and starting, it will count down. At this time, you can press 9 on the keyboard to upgrade uboot through TFTP

```
ASIC 7628 MP (PortS<->None)

DRAM component: 1024 Mbits DDR, width 16

DRAM bus: 16 bit

Total memory: 128 MBytes
Flash component: ST Flash
Date:Mar 4 2017 Time:16:37:55

loache: sets:512, ways:4, linesz:32 ,total:65536
doache: sets:256, ways:4, linesz:32 ,total:32768

#### The CPU freq = 580 MHZ ###
estimate memory size = 128 Mbytes
RESET MT7628 PHY!!!!!

Please choose the operation:

1: Load system code to SDRAM via TFTP.

3: Boot system code then write to Flash via TFTP.

3: Boot system code via Flash (default).

4: Entr boot command line interface.

7: Load Boot Loader code then write to Flash via TFTP.

2: Load system code to SDRAM via TFTP.

2: Load system code then write to Flash via TFTP.

3: Boot system code then write to Flash via TFTP.

4: Entr boot command line interface.

5: Load system code then write to Flash via TFTP.

2: Load System code then write to Flash via TFTP.

2: Load system code then write to Flash via TFTP.

2: Load system code then write to Flash via TFTP.

2: Load system code then write to Flash via TFTP.

2: Load System code then write to Flash via TFTP.

2: Load System code then write to Flash via TFTP.

3: Boot system code via Flash (default).

4: Entr boot command line interface.

5: Load System code then write to Flash via TFTP.

You chose 9

9: System Load Boot Loader then write to Flash via TFTP.

Warning!! Erase Boot Loader in Flash then burn new one. Are you sure?(Y/N)
```

Press Y to confirm the upgrade of uboot

```
9: System Load Boot Loader then write to Flash via TFTP.

Warning!! Erase Boot Loader in Flash then burn new one. Are you sure?(Y/N)

Please Input new ones /or Ctrl-C to discard

Input device IP (10.10.10.123) ==:10.10.10.123

Input server IP (10.10.10.3) ==:10.10.10.3

Input Uboot filename ==:uboot_7628N.bin
```

Input device IP module's own IP

Input server IP server IP (TFTP server) IP on PC

Input uboot filename File name of the uoot on the tftp server