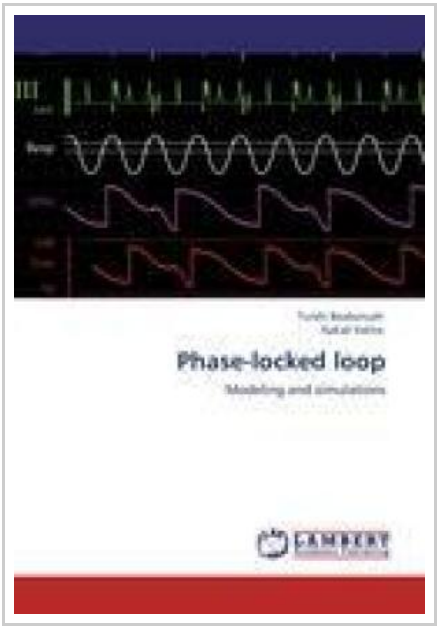

Phase-locked loop

By Tulshi Bezboruah

LAP Lambert Academic Publishing Jun 2012, 2012. Taschenbuch. Book Condition: Neu. 220x150x11 mm. This item is printed on demand - Print on Demand Neuware - The phase-locked loop (PLL) is a feedback control system, used to lock the output frequency and phase according to the frequency and phase of input signal. It can also be used as a frequency synthesizer for modulation and demodulation circuits. In general, PLL is used in both transmitting and receiving terminals of wireless communication systems. A nearly description of PLL appeared in the paper by Appleton in 1923. It was conceived by H. De Bellecize in 1932. Since then, it is widely used in applications involving automatic control of phase or frequency, such as applications in communications, control systems, measurements and instrumentation and in many other applications. The synchronization of horizontal and vertical scan in televisions produced the first widespread use of PLL. One of the first large-scale industrial applications of the PLL (back in the yearly 1950s) was the color sub-carrier recovery in color TV receivers. This work illustrates the modeling of high speed PLL applicable for very high frequency (VHF) communication systems. Details of theoretical estimation of the model and its simulation on...

Reviews

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