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(Dayne Johns)

ELECTRONIC TECHNOLOGY-BASED DIGITAL PART (FIFTH EDITION) PROBLEM PARSING



paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Paperback. Publisher: Xi an Jiaotong University Press Pub. Date :2010-09-03. Huazhong University of Science e-book technology courses pooled. edited by Professor Kang Huaguang E technology-based digital part (fifth edition) as reference materials. prepared in the course of the study guide books. Each chapter is divided into two parts; the first part of the basic concepts and analytical calculations based on is a textbook knowledge points clear and concise summary of each chapter to guide students to master the key and difficult; second part. Problem Resolution. After each chapter of the textbook exercises detailed analysis of the answers. students and scientific problem-solving way of thinking. Use the book as a teaching college students of the course resource materials; on the choice of teaching materials for students and other institutions related engineering and technical personnel. the book as a reference book to learn the course. for students who wish to pursue graduate This book is also available as PubMed counseling books. Contents: Preface Introduction 1.1 I basic concepts of digital logic and analytical calculations based on digital circuits and digital signal 1.1.1 1.1.2 1.1.3 binary number system arithmetic 1.1.4 1.1.5 binary logic binary code. variables and basic 1.1.6 Logical representation of logic functions and logic of algebra 2 1.2 Problem resolution and hardware description language-based analysis of 2.1 calculated on the basis of basic concepts and logic of algebra 2.1.1 2.1.2 Verilog HDL-based hardware description language 2.2 Problem Resolution 3 logic gates 3.1 Basic concepts and analytical calculations based on 3.1.1 MOS logic gates 3.1.2 TTL logic gate circuit 3.1.3 3.1.4 describes several problems in the logic gates used in several practical problems described by Verilog HDL 3.1.5 Problem parsing logic...



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