





Rust programming language in high-performance computing

Final Presentation Michal Sudwoj 25.08.2020

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Rust programming language in high-performance computing Final Presentation Michael Surfami

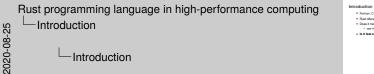
Introduction

- Fortran, C++ established languages for HPC
- Rust offers safety (borrow checker)
- Does it have (the features) that it needs?
 - see Introduction to Rust presentation

Rust Webinar 2

Is it fast enough?





-Introduction

 Rust offers safety (borrow checker) Does it have (the features) that it needs?

- see Introduction to Rust presentation . Is it fast enough?

Problem

• fourth-order numerical diffusion in xy-plane

$$\frac{\partial \phi}{\partial t} = -\alpha_4 \nabla^4_{xy} \phi = \underbrace{-\alpha_4 \Delta^2_{xy} \phi}_{\text{inline}} = \underbrace{-\alpha_4 \Delta_{xy} (\Delta_{xy} \phi)}_{\text{laplap}}$$

- on unit cube
- boundary conditions: $\partial_{\Omega} = 0$

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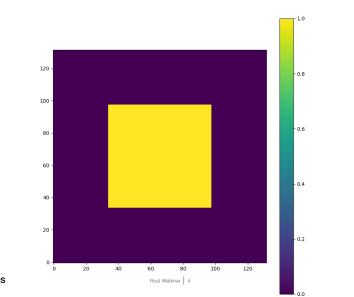
$$\begin{split} & \textbf{Problem} \\ & \bullet \text{ tourh-order numerical diffusion in } xy \text{ plane} \\ & \frac{\partial \phi}{\partial t} = -\alpha_0 \nabla_{xy}^4 \phi = \underbrace{-\alpha_4 \Delta_{xy}^2 \phi}_{\text{totals}} = \underbrace{-\alpha_4 \Delta_{xy} (\Delta_{xy} \phi)}_{\text{totals}} \\ & \bullet \text{ on with cabe} \\ & \bullet \text{ boundary conditions. } \partial_{\Omega} = 0 \end{split}$$

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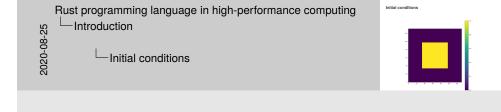
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Problem

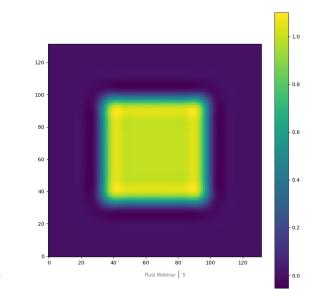
Initial conditions



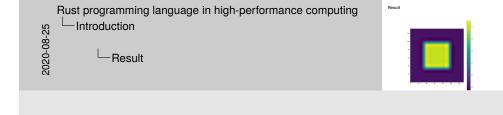
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Result



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Benchmark

- Fortran, C++, Rust
- Algorithms: laplap, inline
- Toolchains
 - Fortran, C++: GNU, Cray, Intel, PGI
 - Rust: rustc
- Sequential
- Parallel
 - Fortran, C++: OpenMP
 - Rust: Rayon
- GPU
 - Fortran, C++: OpenACC, OpenMP offloading, CUDA
 - Rust: Accel
- \implies 52 versions \times 4 grid sizes

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-Benchmark

-Introduction

Benchmark · Algorithms: laplap, inline Toolchains - Fortrain, C++: GNU, Cray, Intel, PGI - Rust rust c Sequential Parallel * Fortran, C++: OpenMP - Bust Bayon

* Fortran, C++: OpenACC, OpenMP officeding, CUDA. * Rust: Accel -- 52 versions × 4 orid sizes

1. MPI works, but not enough time to debug the partitioner



- arrays in column-major order
- -03 or equivalent
- no LTO
- optimization reports
- shared libraries
- C interface

```
void diffuse(
 float * in_field,
  float * out_field,
  size_t nx,
 size_t ny,
 size_t nz,
  size_t
         num_halo,
  float
         alpha,
  size_t num_iter
```

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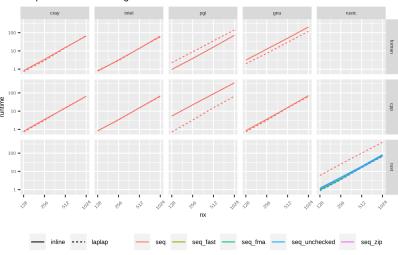
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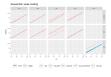
1. Show code on gitlab

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Results

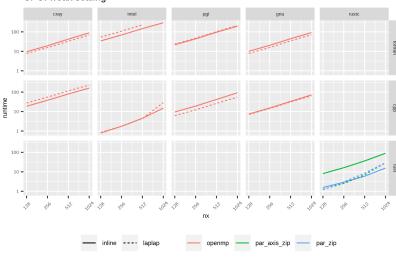






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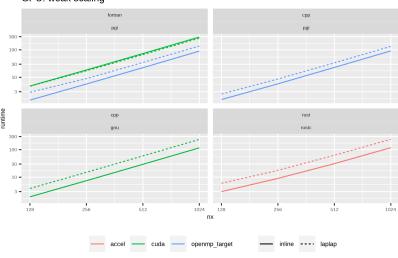
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Results



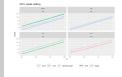








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Results









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Conclusions

- + Rust fast enough for scientific software
- + Safer code
- + Clearer error messages
- Support for certain features is lacking
- ~ All languages sometimes lead to lots of boilerplate

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Rust programming language in high-performance computing -Conclusions

-Conclusions

Conclusions

- Rust fast enough for scientific software Safer code
- Support for certain features is lacking All languages sometimes lead to lots of boilerplate

- 1. Rust compiler does good job of vectorizing code
- 2. Rust: clear what you get
- 3. Fortran/C++: different support, many bugs
- 4. GNU doesn't warn about lack of offloading
- 5. Cray sometimes silently generates invalid PTX code



Reccomendations

- Today
 - Rust for frontend/driver
- Tomorrow (some effort required)
 - GPU support
 - Evolve ndarray
 - ScaLAPACK bindings, ...
- ⇒ continue to pursue Rust in HPC
 - it has potential

See:

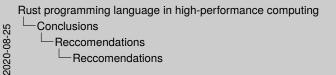
- https: //www.arewelearningyet.com/scientific-computing/
- https://www.arewelearningyet.com/gpu-computing/

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https://git.cscs.ch/msudwoj/rust-in-hpc









https://git.cscs.ch/msudwoj/rust-in-hp

* https://www.arewelearningvet.com/gpu-computing/

Reccomendations

1. With Cray and Intel moving to LLVM, cross-language LTO soon?

Start using Rust today!

```
> curl https://sh.rustup.rs -sSf | sh
> rustup toolchain install nightly
> rustup target add nvptx64-nvidia-cuda
> # On Piz Daint
> export
    CARGO_TARGET_X86_64_UNKNOWN_LINUX_GNU_RUSTFLAGS="
      -C target-cpu=haswell
      -C relocation-model=dynamic-no-pic
    CARGO_TARGET_NVPTX64_NVIDIA_CUDA_RUSTFLAGS="
      -C target-cpu=sm_60
      -C target-feature=+sm_60,+ptx60
      -C relocation-model=dynamic-no-pic
    MPICC=cc
> cargo install ptx-linker
                                                     ETH zürich
                       Rust Webinar 13
```

Rust programming language in high-performance computing Start using Rust today! -Conclusions Reccomendations Start using Rust today! > CARGO TARGET NVPTX64 NVIDIA CUDA RUSTFLAGS-> -C target-cpu-am_60 > -C target-feature=+sm 60.+ptx60 > -C relocation-model-dynamic-no-pic



> MPICC-cc > cargo install ptx-linker