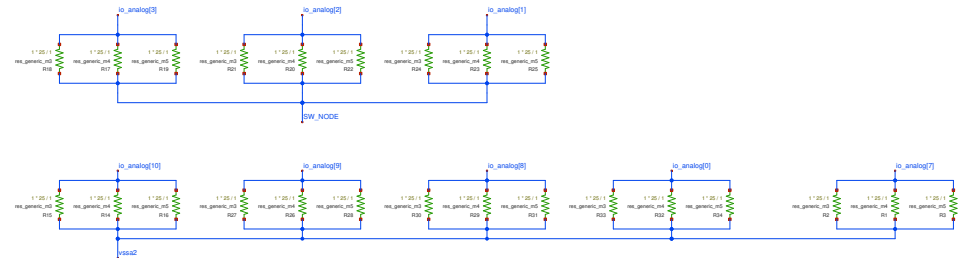
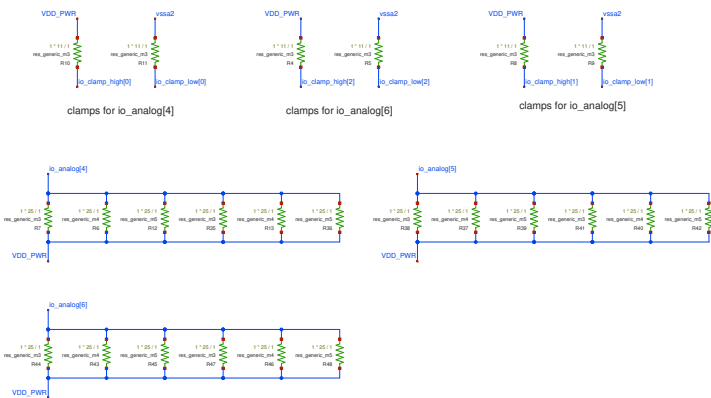
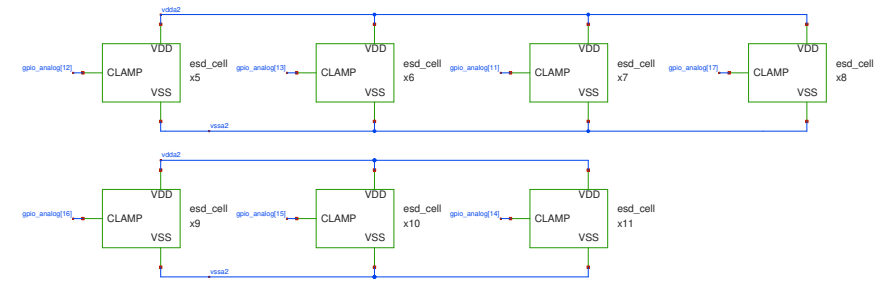
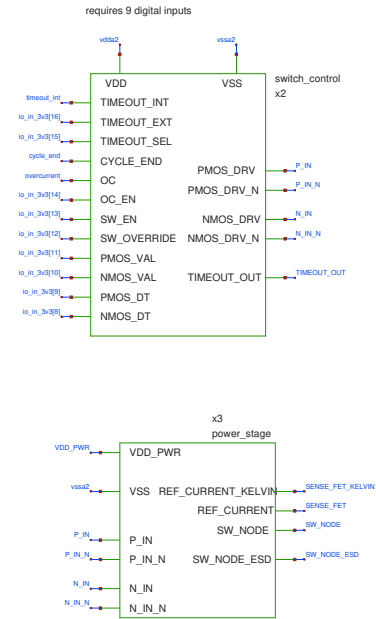
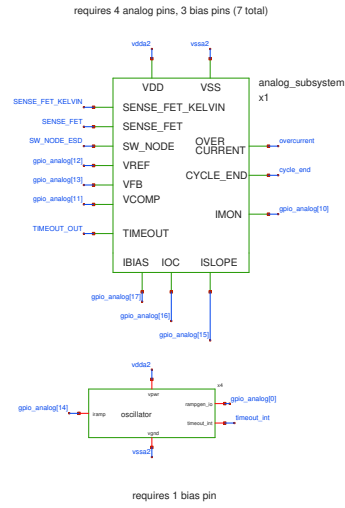


- vdda1
- vdda2
- vssa1
- vssa2
- vcc01
- vcc02
- vssd1
- vssd2
- wbs_ack_i
- wbs_rst_i
- wbs_sfb_i
- wbs_cyc_i
- wbs_we_i
- wbs_sst_i[3:0]
- wbs_dat_i[31:0]
- wbs_addr_i[31:0]
- wbs_ack_o
- wbs_dat_o[31:0]
- ia_data_in[127:0]
- ia_data_out[127:0]
- ia_oen[127:0]
- io_in[26:0]
- io_in_3v3[26:0]
- io_out[26:0]
- io_oen[26:0]
- gpio_analog[17:0]
- gpio_noesd[17:0]
- io_analog[10:0]
- io_clamp_high[2:0]
- io_clamp_low[2:0]
- user_clock2
- user_irq2[2:0]



metal resistors as net ties