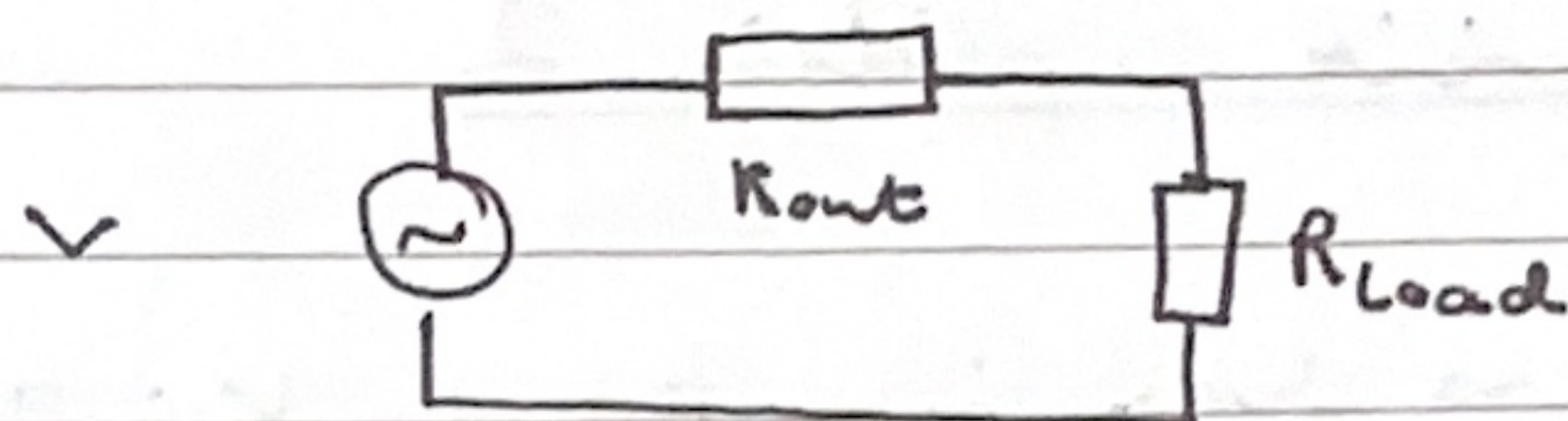


Weeks 1 & 2 ~ Experimental methods.

1 (a)



(i) Voltage across R_{Load}

Potential divider: $\frac{R_{Load}}{R_{Load} + R_{int}} \times V = V_{Load}$

if $R_{Load} \gg R_{int} \therefore R_{Load} + R_{int} \rightarrow R_{Load}$

hence $V_{Load} = V \times \frac{R_{Load}}{R_{Load}} = V \times 1 = V //$

> (ii) Max power dissipated in R_{Load}

$P_{load} = V_{load} I_{load}$

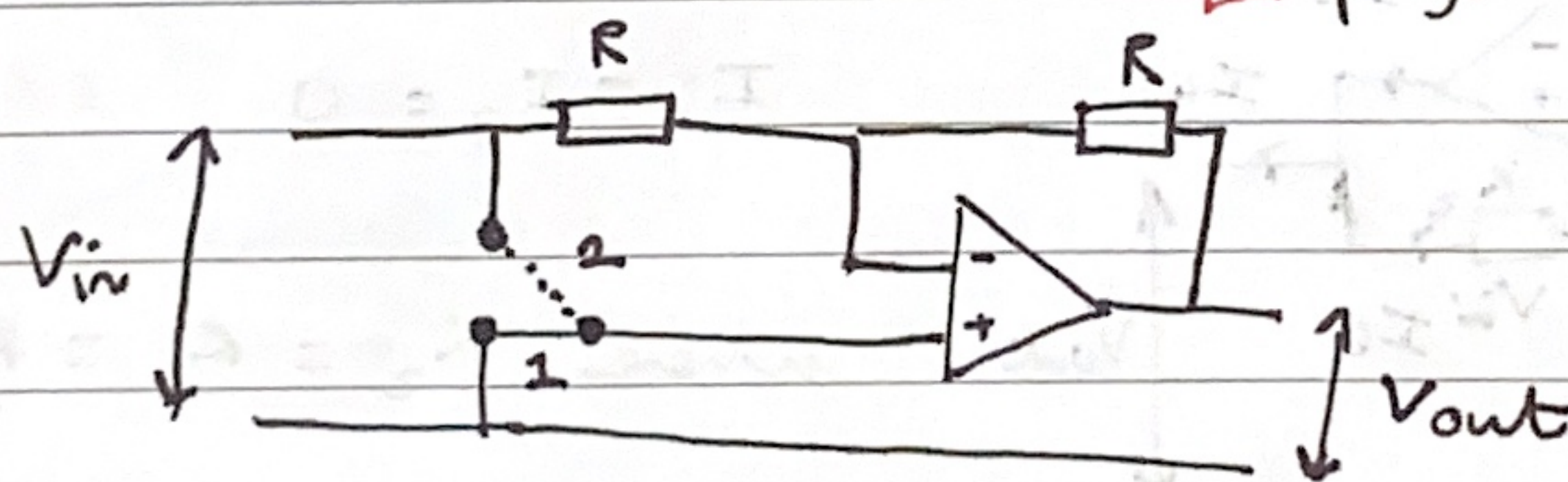
$V_{load} = \frac{V R_{Load}}{R_{int} + R_{Load}} = \frac{V}{2}$

$I = \frac{V}{R_{int} + R_{Load}} = \frac{V}{2 R_{Load}}$

hence $P_{load} = \frac{V}{2} \times \frac{V}{2 R_{Load}} = \frac{V^2}{4 R_{Load}} //$

physical significance:
when internal resistance
of generator is effectively
negligible (or constant
such as voltmeter
- with very high R - is
measuring device)

(b)



physical significance: if inputs
source has large resistance
(compatible with scopes)

Behaviour of circuit for position 1

Conventional op-amp set-up: hence $V_{out} = V_{in} \times -\frac{R}{R}$

$= -V_{in}$

the signal has therefore just been
reversed in phase.

Behaviour of vint in position 2

there will be no change in voltage: $V_{in} = V_{out}$

→ Impact of frequency & V_{in} magnitude on V_{out} .

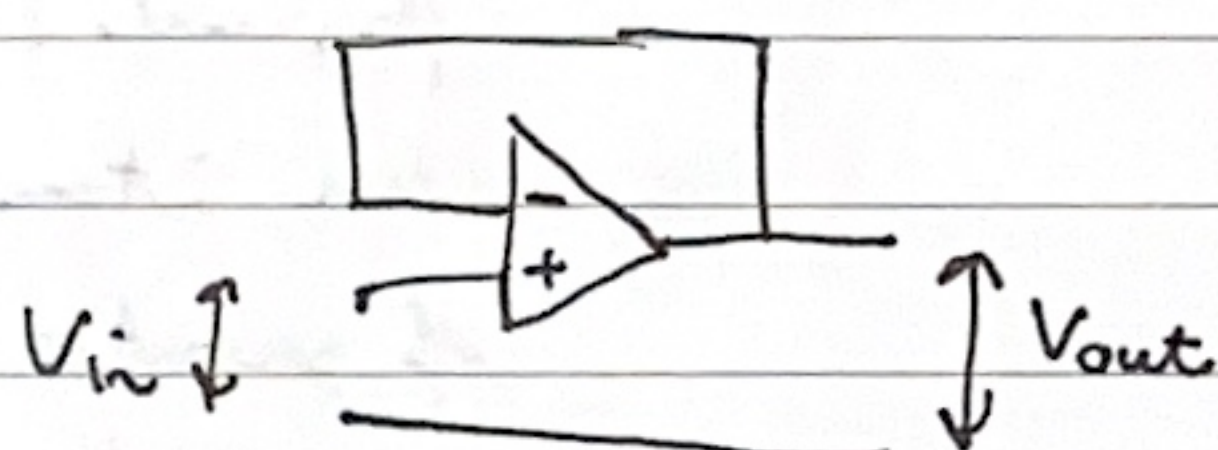
At high frequency there will be a delay in output ~~is~~?

(c) Buffer

~~Golden~~ GR1: inputs draw no current

GR2: + & - same.

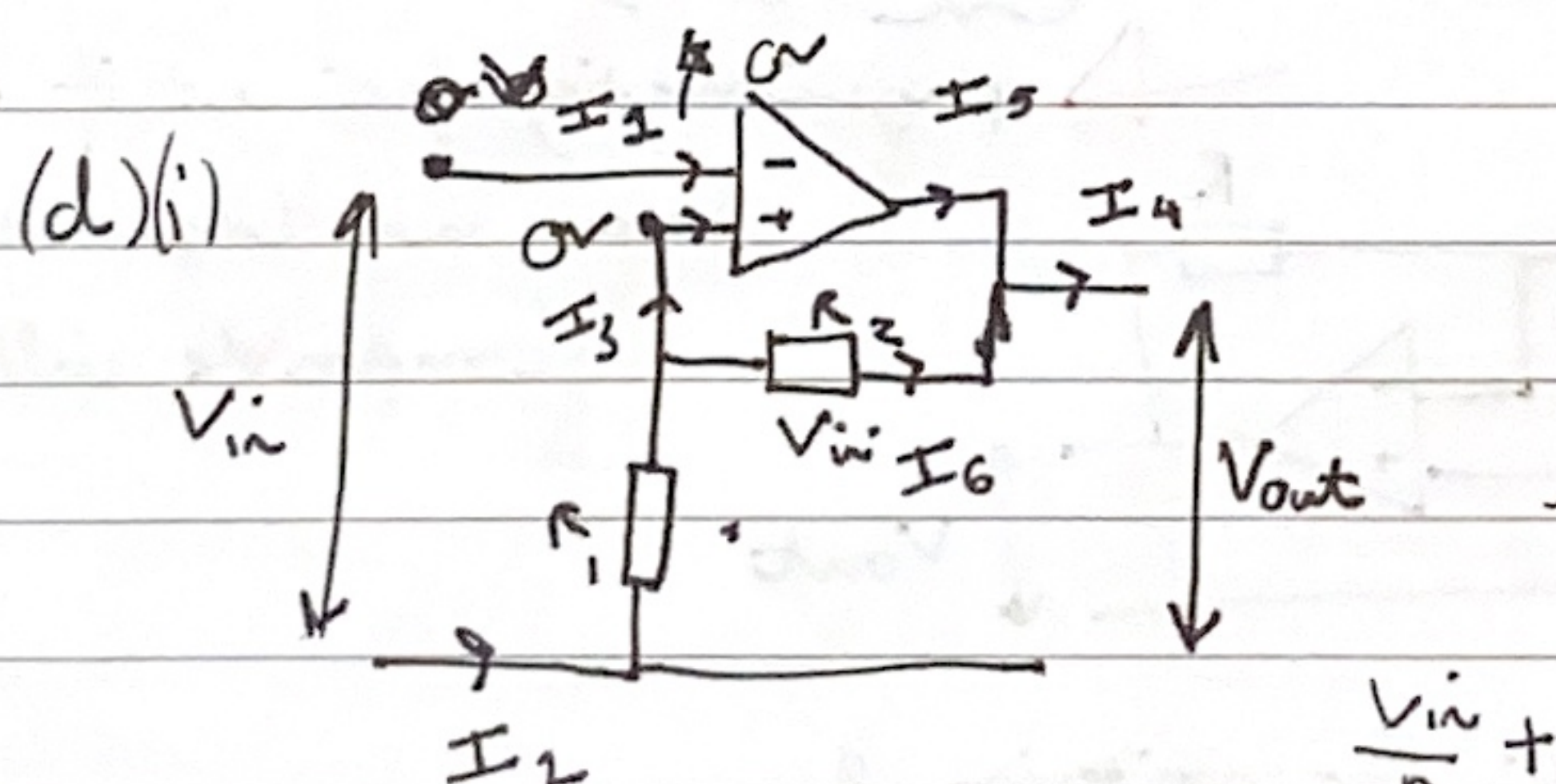
~~GR2~~:



GR2 means that if + input is a V_{in} , so must - input.

GR1 means that the upper wire (into - input) draws no current & so must be at equipotential & hence is V_{in} . $\frac{V_{out}}{V_{in}} = 1 \therefore$ unit gain. Must have infinite input impedance or short-circuited?

→ buffer used to connect circuits?



$$I_1 = I_3 = 0$$

where $R_2 = R_1 = R$

$$I_2 = I_C = \frac{V_{in}}{R}$$

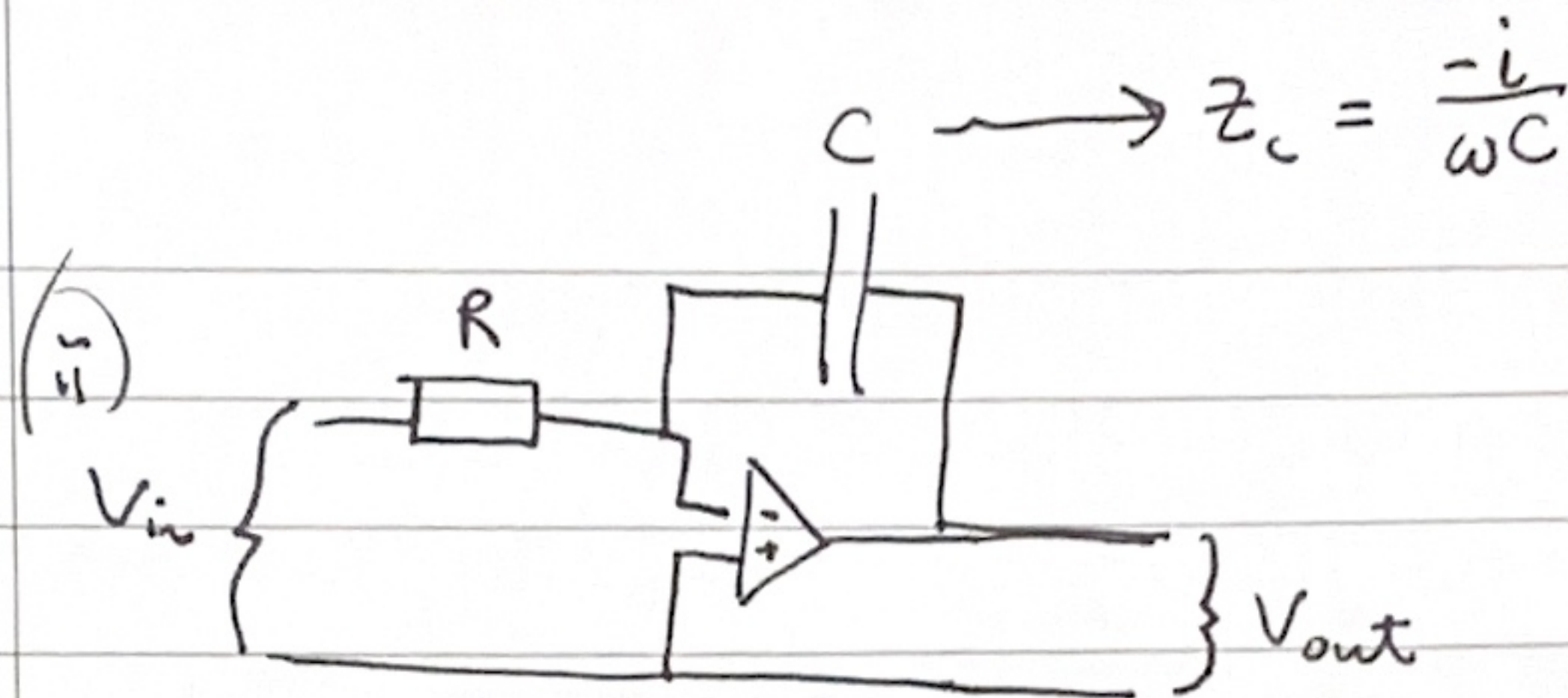
$$I_4 = I_5 + I_C$$

\therefore gain of -1

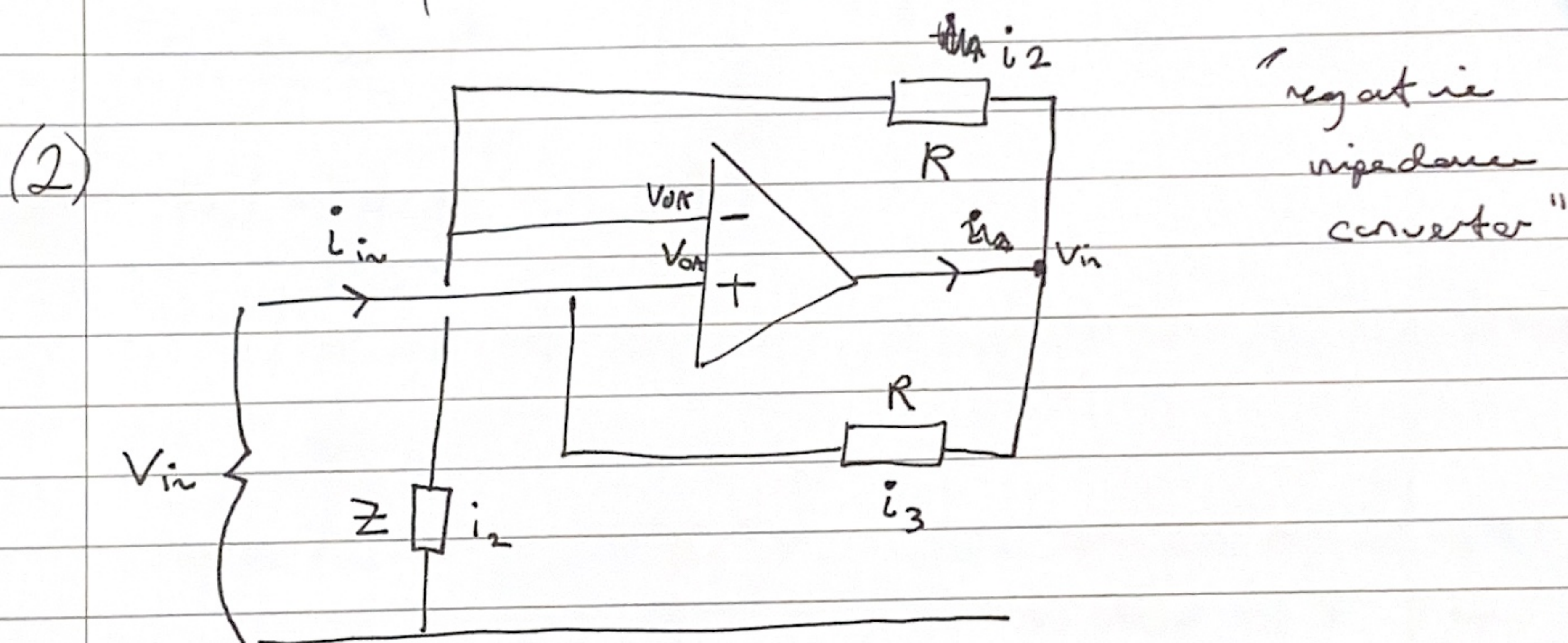
$$I_2 \times R = \frac{V_{in}}{R} \times R = -V_{in}$$

resistances

hence $V_{in} = -V_{out} \rightarrow$ by changing the ~~values~~ resistances so if they give different resistances will yield a voltage gain of $-\frac{R_2}{R_1}$



The impedance of the capacitor will $\rightarrow 0$ at high ω which would lead to a short circuit. Mitigate by putting resistor in parallel with capacitor?



+ Input $V = -$ Input V (as negative feedback present.)

$$i_3 = \frac{V_{in} - V_{out}}{R} \quad \left| \quad i_4 = \frac{V_{in}}{R} \quad \right| \quad i_2 = \frac{V_{out} - V_{in}}{R} = \frac{V_{in}}{Z}$$

$$i_2 = i_{in} - i_3$$

$$-i_3 Z = V_{in}$$

$$\frac{V_{in}}{i_3} = -Z$$

Q11)

$$\psi = \text{Re}[C \exp(i(\omega t - kz))] \Rightarrow \frac{\partial \psi}{\partial t} = \text{Re} \left[\overset{i\omega C \exp(i(\omega t - kz))}{\cancel{(\omega t - kz) C \exp(i(\omega t - kz))}} \right]$$

$$\begin{aligned} P(t) &= Z \left(\frac{\partial \psi}{\partial t} \right)^2 \\ &= \text{Re} \left[\omega^2 C^2 \exp(2i(\omega t - kz)) \right] \times Z \\ &= -\omega^2 |C|^2 \end{aligned}$$

$$\text{Re}[A] = \frac{1}{2} (A + A^*)$$

$$\therefore \text{Re}[i\omega C \exp(i(\omega t - kz))] = \frac{1}{2} (i\omega C \exp(i(\omega t - kz)) + \omega C^* \exp(-i(\omega t - kz)))$$

↓

square this $\rightarrow \frac{1}{4} (2\omega^2 |C|^2 + \dots \text{other terms with } \exp(i\omega t - kz) \text{ true wave } \rightarrow 0 \therefore \text{can be neglected.})$

$$\left(\frac{\partial \psi}{\partial t} \right)^2 = \frac{\omega^2 |C|^2}{2}$$

$$\therefore P(t) = \frac{1}{2} Z \omega^2 |C|^2$$

Complex Z \rightarrow this would occur to accommodate stiffness/elasticity in the spring?

$$\begin{aligned} \text{Re}[A] \text{Re}[B] &= \frac{1}{2} (A + A^*) \frac{1}{2} (B + B^*) \\ &= \frac{1}{4} (AB + \cancel{A^* B} + \cancel{B^* A} + A^* B^*) \\ &= \frac{1}{2} \text{Re}[AB + A^* B^*] \end{aligned}$$

$$\text{here } P = \frac{1}{2} \text{Re} [-\omega^2 C^2 \exp(2i(\omega t - kz))] Z = Z \omega^2 C^2 \exp(-2i(\omega t - kz))$$

$$= \frac{1}{2} \left(\right.$$

Notice how to get real part of this expression...