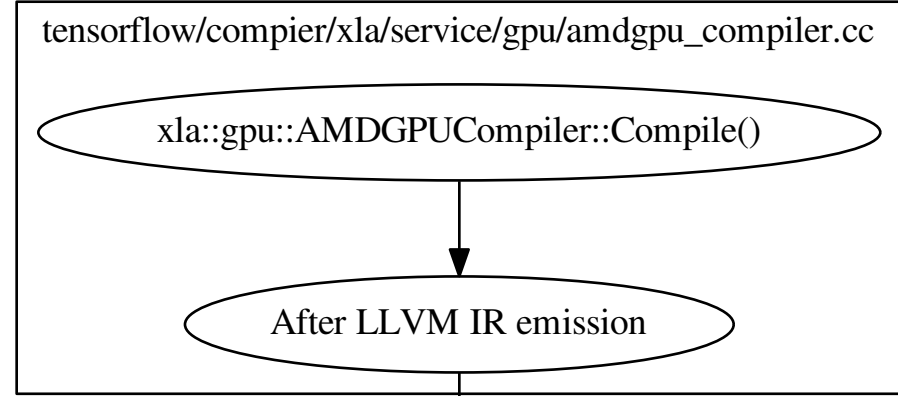
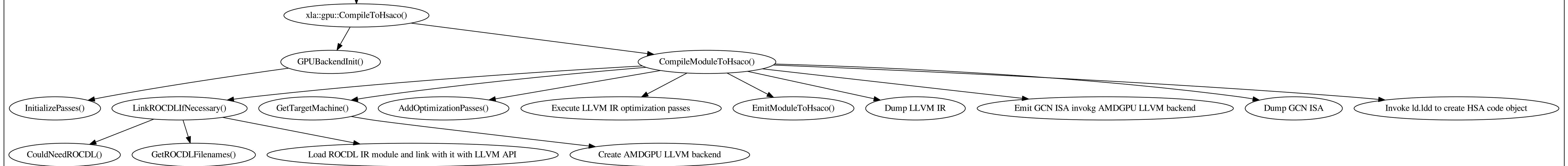


XLA



tensorflow/compiler/xla/service/gpu/llvm_gpu_backend/amdgpu_backend_lib.cc



AMDGPUCompiler::Compile() LLVM IR to HSA code object