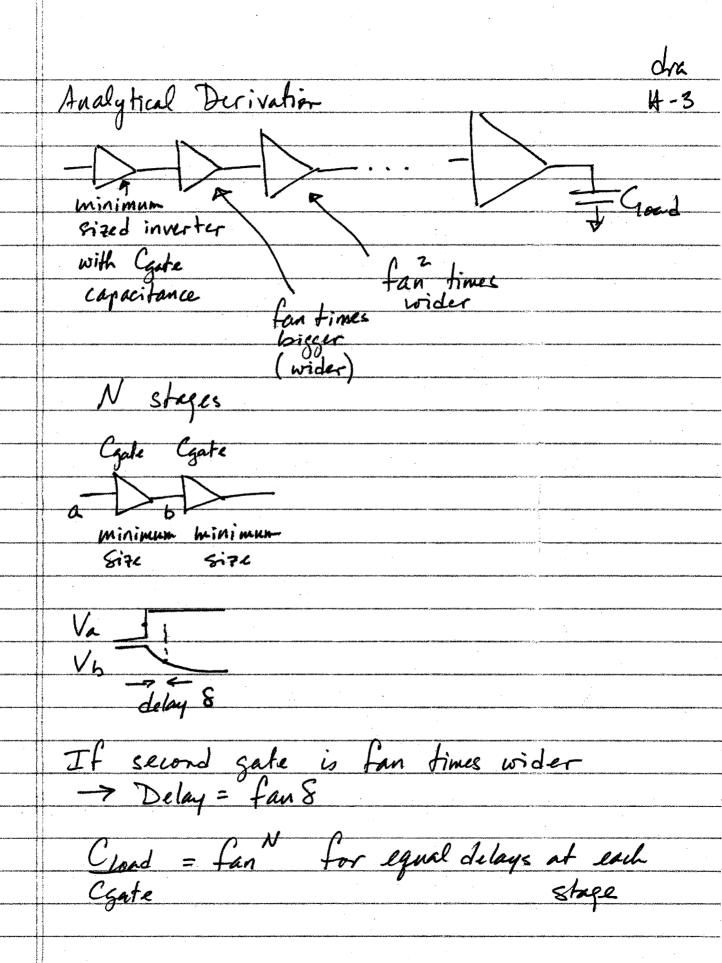
ASPICE notes Aspice manuel table of contacts p2-2(52) quick AC.sp 8-2(312) OPTIONS LIST NODE POST 9-6 (360) OP 11-4 (436) . AC dec 10 16 1Mes 7-4(250) # . print ac v(1) v(2) i(r2) i(c1) 5-6 (182) VI 1 0 10 ac 1 4-2 (120) D 1 2 1k rz zo 1k 4-4 (122) C1 20 .00/4 hspice quickAC.sp > genicleAC.lis quickAcaco binary for GUI output ica wital conditions . Stop status · lis output 2-7 (57) quick/NV.sp Juen 10-4 (412) · tran 200p 20N & statements 17-4 (730) m1 out in vcc vce pch L=10 W=200 VCC VCC 0 5 5-7 (153) Vin in 0 0 PULSE . I 4.8 IN IN SN ZON 3-29 (87) . model pch pmos /evel=1 18-1 (845)

Control of the Contro	dra
	InvChain.sp new statements 4-2
3-32 (90)	
• ;	.global vcc! gnd!
3-13 (4)	, subclet inv A 7
and the control of th	m1 7 A End! and! nmos W=1.4n 1=0.35h
maralinadaren erreta de seus en erreta esta esta esta esta esta esta esta e	m1 7 A gnd! gnd! nmos W=1.4m 1=0.35h mz Z A vcc! vcc! pmos W=(1.4u*alpha) 1=0.35h
1	. 1e
3-13 (71)	XINVI # 1 inv M=fan
5-4 (150)	Vin a god! OV PWL O ONS INS 3 ZONS 3
7-35 (28)) . Measure TRAN +phl-inv TRIG v(Xinv1.a)
	VAL = 1.5 RISE = 1 TARE V(Xinv5. 7)
& not in	VAL = 1.5 FALL=1 OPTION MEASFORM=3 CHE Somet
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Ignoring Parasitics total delay = D = N (fan.8) but fan = (Chad) \ R = Good

Cgate) ratio Cgate for optimum delay $\frac{\partial D}{\partial N} = 0$ $\frac{\partial}{\partial N} (N | 2^{N} \delta) = R^{N} \delta + 8N \frac{\partial}{\partial N} R^{N}$ $\frac{\partial}{\partial N} N$ 2 R'W = 2 e la R'W = 2 e tolork $= e^{\frac{1}{N} \ln R} \left(\frac{-1}{N^2} \right) = R^{\frac{1}{N} \ln R}$ 30 = 0 = R*8 = N8 R* lnR = R*8 (1 - lnR) lnR=1 lnR=N R=e^N $R^{N}=e=fan$ 4N=lnR

	ohr
But for practical circuits	H-5
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So we need to write a python surip to run hispice multiple times for	
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slightly different input files searching for an optimum N & fan for a given CMOS technology & Good.	

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