

```
1  library ieee;
2  use ieee.std_logic_1164.all;
3
4  entity contador255 is
5
6      port(   seletor, reset: in std_logic;
7              saida: out integer range 0 to 255          );
8  end contador255;
9
10 architecture arq of contador255 is
11
12     begin
13
14         process (seletor)
15             variable i : integer range 0 to 255 := 0;
16             begin
17
18                 if (reset='1' or i>255) then
19                     i := 0;
20                 elsif (seletor='1' and seletor'event) then
21                     i := i + 1;
22                 end if;
23                 saida <= i;
24             end process;
25
26     end arq;
```