

LEC-9 | Intro to Process.

- * what is program? compiled code, that is ready to execute.
- * what is process? program under execution.

CPP → compiler → compile → executable → program

program e.g. TIK Tok → OS → program
↓
Process

why process?
→ user → task → way
program → process.

- * **How OS creates a process?** Converting program into a process.

① Load the program & static data into memory.
→ used for initialization.

② Allocate runtime stack;

→ part of memory used for local variable, p.v argument & return value.

③ Heap memory allocation.

- part of memory used for dynamic allocation.

(4)

I/O tasks

- i/p — handle
- o/p — handle
- error — handle

(5)

OS hands off control to user ()

main ()

{

return 0; → successful execution hogyan
} to check.

exit ()

{

exit (-1)

}

→ ab return hogya
OS ko bataaki kuch
th gadbad h

Process.

main()

* Architecture of process:



local variable, function arguments & return value

Dynamically allocated variables

Global & static data.

Compiled code (loaded from disk)

Global & static
data

Stack & heap of

② stack overflow - Base case set

③ out of memory - de allocate unrecd obj.

* Attributes of process.

- Feature that allows identifying a process uniquely.

- Process table.

All processes are being tracked by OS using a table like data structure.

• Each entry in that table is process Control block (PCB).

- PCB: stores info of attributes of a process.

• Data structure used for each process, that stores info of a process such as process id,

Program counter, process state, priority etc.

Process table	
1	P ₁
2	P ₂
3	P ₃

• PCB Structure:

Unique identifier
Next instruction address of the program

Storage process state
Based on priority a process gets CPU time
Save reg of CPU

Process ID

Program Counter (PC)

Process state

Priority

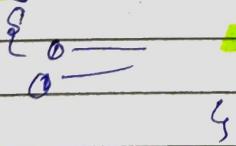
Registers

List of open files

List of open devices

In CPU
SP reg, BP reg
or control reg

• Program Counter (PC)



Compile converted into instructions

1.2 APP

program → Coll' of instructions

CPU want only instruction & it will execute if

in

OS know which process be selected ex program counter associate with

12 Add E

~~Fetch the instruction R
from address in PC~~

PC++

execute

$\begin{cases} 50 \\ 50 \\ 50 \\ \{1\} \\ 5 \end{cases}$

Registers in the PCB, if it is a data structure, when a process is running & it's time slice expires, the current value of process specific registers could be stored in the PCB & process would be swapped out, when the process is scheduled to be run, the register values is read from the PCB & written to CPU registers. This is the main purpose of registers in PCB.

LEC - 10 | Diff process states in OS.

X Process states: As process executes, it changes state.
Each process may be in one of the following state

① New

as it about to pick the program & converted it into process is being created.

② Run

Instructions are being executed; CPU is allocated

③ Ready

Process is trying to move to next in queue

- is in ready queue.

④ Run

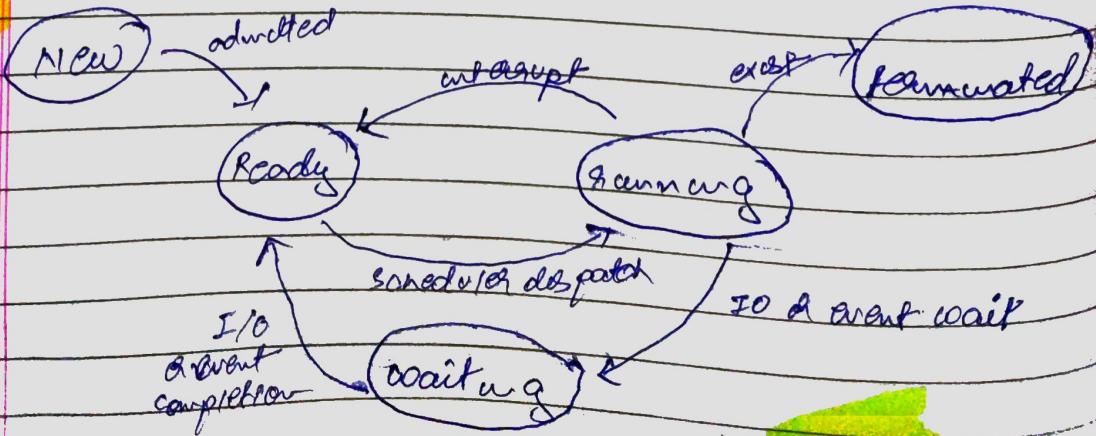
Instructions are being executed; CPU is allocated

⑤ Waiting

waiting for I/O

⑥ Terminated

process has finished execution. PCB entry removed from process table.



* PROCESS QUEUES

(a) Job Queue

i. processes in new state

ii. present in secondary memory

iii. Job scheduler (Long term scheduler) LTS

Picks process from the pool and loads them onto memory for execution.

→ 1000 (Req)
ideal time is high

| P₁ | P₂ | P₃ |

i.e. (on new state)

i. Chosen in

④ ready queue
CHECK kafal h koi job ready queue
mawal dalmia.

(b) Ready Queue

i. processes in Ready State

ii. present in main memory

iii. CPU scheduler (short term scheduler) picks process from ready queue & dispatch it to CPU.
→ work on high freq → P₃ → I/O th fall se P₁, P₂
ideal time is 1000
Ko running state mawal dega

(c) Waiting Queue

i. process in wait state.

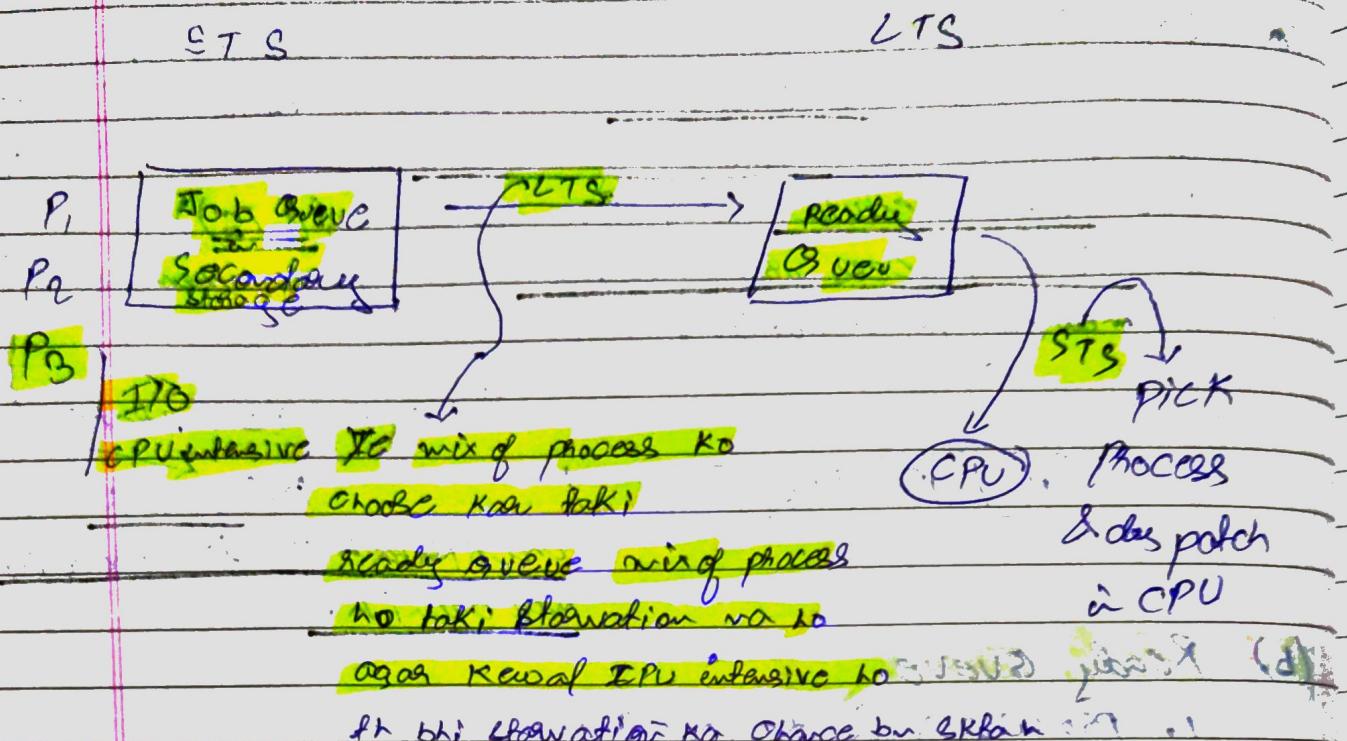
* Degree of multi programming is the no. of process in the memory.

→ LTS controls degree of multi programming.
↳ yahi memory se kaha ke wait state mawal dega

* Dispatcher: The module of OS that gives control of CPU to a process selected by LTS.

CECA - 11 | Context switching | Medium term sched.

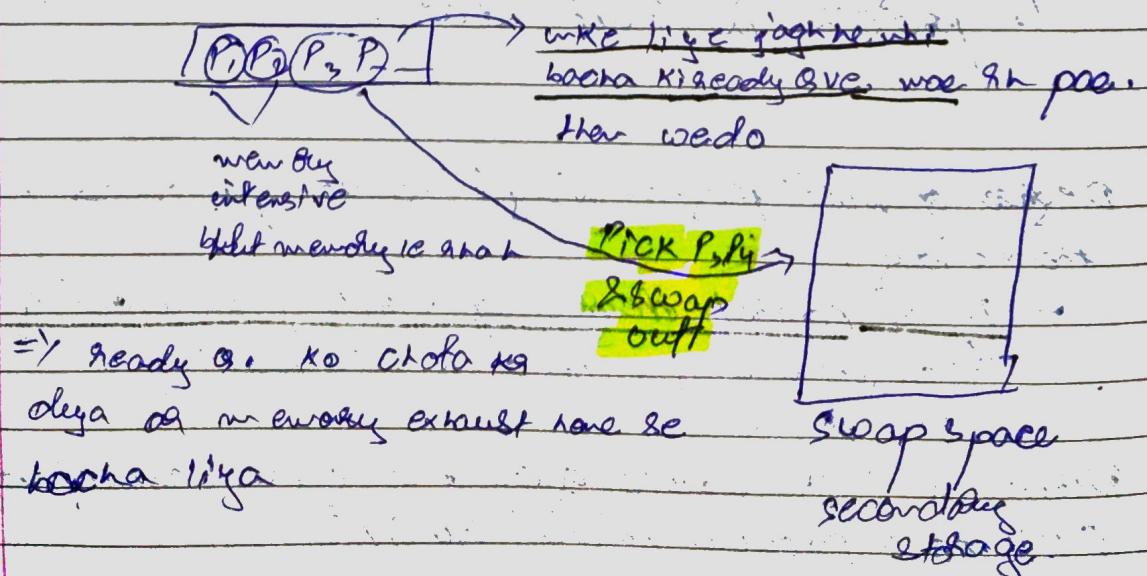
A plan/zombie process



MTS — Medium term Scheduler

- latest
- degree of multi prog. ↑ by LTS

Ready Q ve.



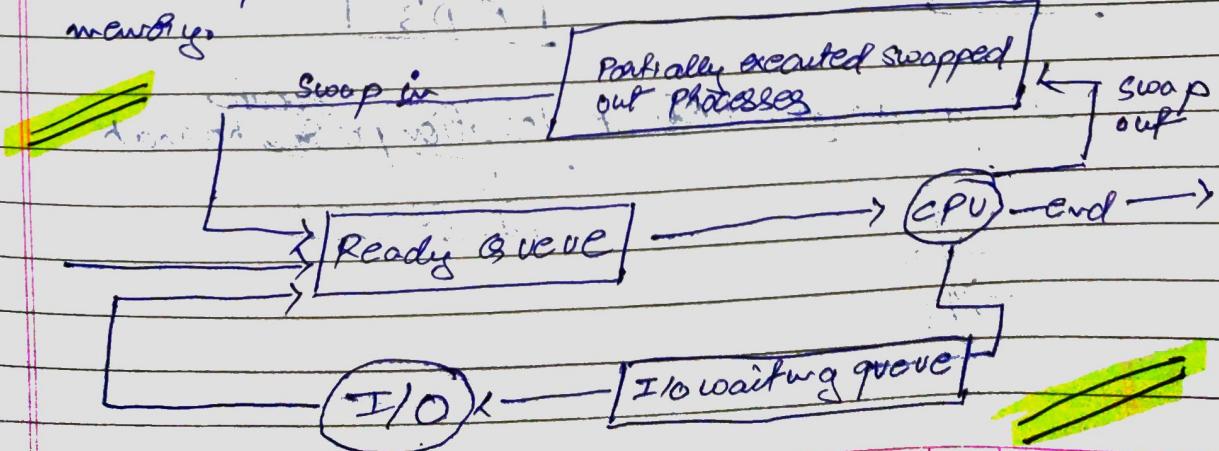
Now, let $K_1 P_1 \rightarrow$ form wait hogai.

P_1 jo K_1 memory int ansive thi th ab

P_3 or P_4 jo swapped out hogai; thi cuko whare wapas ready & mal be exec i.e. swapped in.

7. Swapping

- Fair sharing system may have medium term scheduler (MTS)
- Remove processes from memory to reduce degree of multiprogramming.
- This removed process can be reintroduced into memory & its execution can be continued where it left off.
This is called swapping.
- Swap out & swap in done by MTS.
- Swapping is necessary to improve process mix b/c a change in memory requirements has over committed available memory, requiring memory to be freed up.
- Swapping is a mechanism in which a process can be swapped temporarily out of main memory (or move) to secondary storage (disk) make that memory available to other processes. At some later time, the system swaps back the process from the secondary storage to main memory.



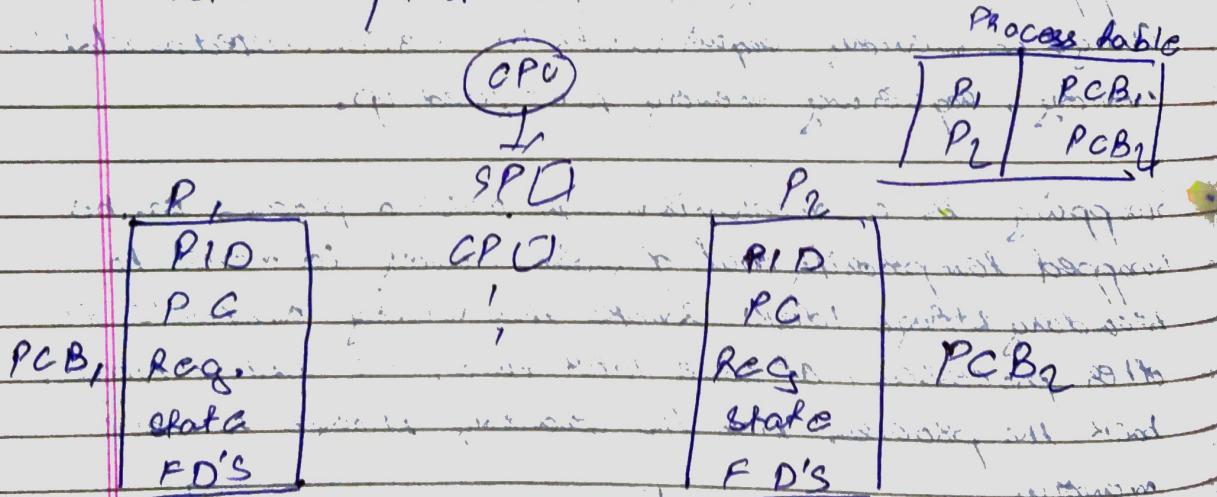
Context Switching

Switching the CPU to another process by performing a state save of the current process & a state restore of a diff process.

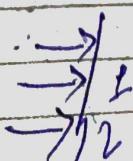
- when this occurs the kernel saves the context of the old process in the PCB & loads the saved context of the new process scheduled to run.

- It is process overhead, b/c the system does no useful work while switching.

- Speed varies from machine to machine, depending on the memory speed, the no. of registers that must be copied etc.



obj: ex program tell ex location per execute
↳ ref address L

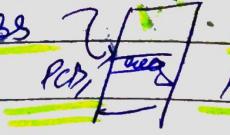


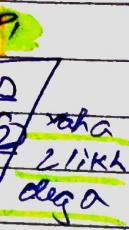
ab CPU krega abhi koi context save nahi

Save kare ke liye wo next instruction (2) ko

or jise bhi CPU ke aeg hoga

utna ke jo abhi current value wa

(3)  reg. value entry was save PCB, PC, R12



Ab usko P2 schedul kar diya;

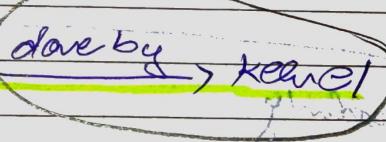
P2 bhi koi save nahi hogi

th P2 ke corresponding reg. X value utna ke CPU
ke liye wo copy kar dega

or P2 program counter ko blagya sunko yahan se
execute karva h.

ab P1 ke context ko astore ke liye P1 ke
context ko save kar diya.

Context switching

done by  kernel

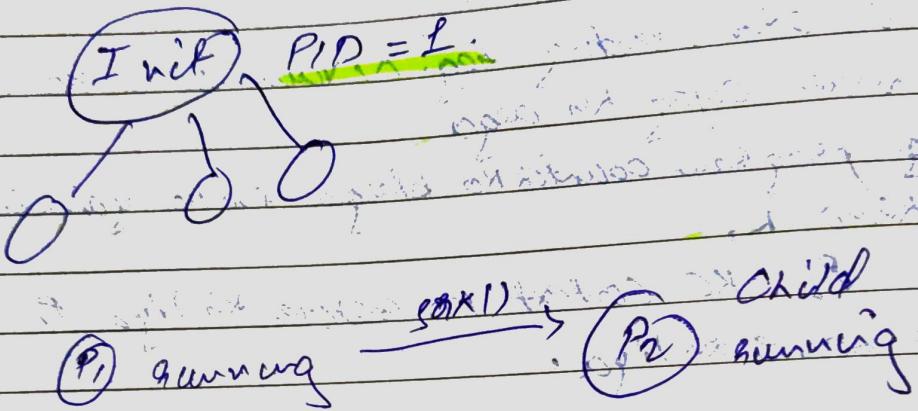
it is

Processor overhead

i.e user defined processes in ready no one is
working, only context switching is happening

Orphan process

- The process whose parent process has been terminated and it is still running
- Orphan processes are adopted by Init process
- Init is the parent process of OS.



Exception → terminate

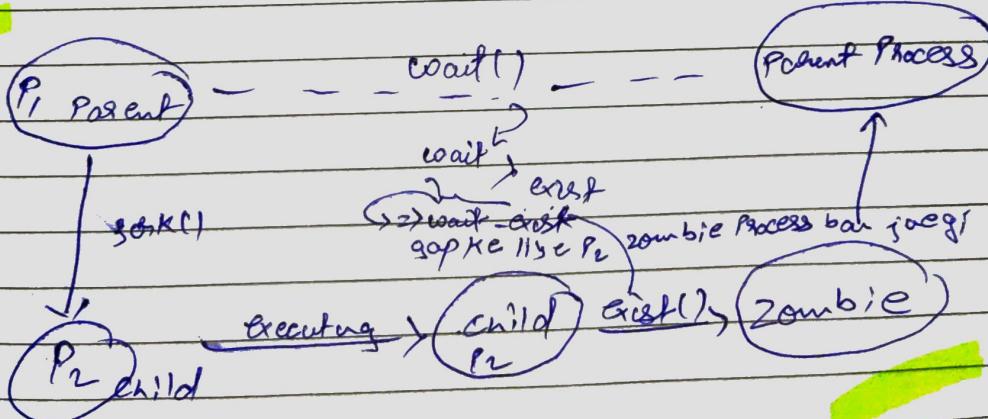
ab (P_2) ka kya hogा?

due to OS
jaga.

(P_2) ka parent Init ho

* Zombie process / Defunct process

- A Zombie process is a process whose execution is completed but it still has an entry in the process table.
- Zombie processes usually occur for child processes, or the parent process still needs to read the child's exit status. Once this is done using the wait system call, the Zombie process is eliminated from process table. This is known as reaping the Zombie process.
- If the parent process may call `wait()` on child process for a longer time duration & child process get terminated much earlier.
- As entry in the process table can only be removed, after the parent process reads the exit status of child process. Hence, the child process remains a Zombie till it is removed from process table.



if wait call nahi kiya Parent ne

Process table exhaust na jaegi @ Zombie Process ki Pid ab
 koi old process use nahi
 kaise kota

LE12 | Process scheduling.

#

Process scheduling

- Basics of Multi-programming OS.
- By switching the CPU among processes the OS can make the computer more productive.
- Many processes are kept in memory at a time. When a process must wait or time quantum expires, the OS takes the CPU away from that process & gives the CPU to another process & this pattern continues.

#

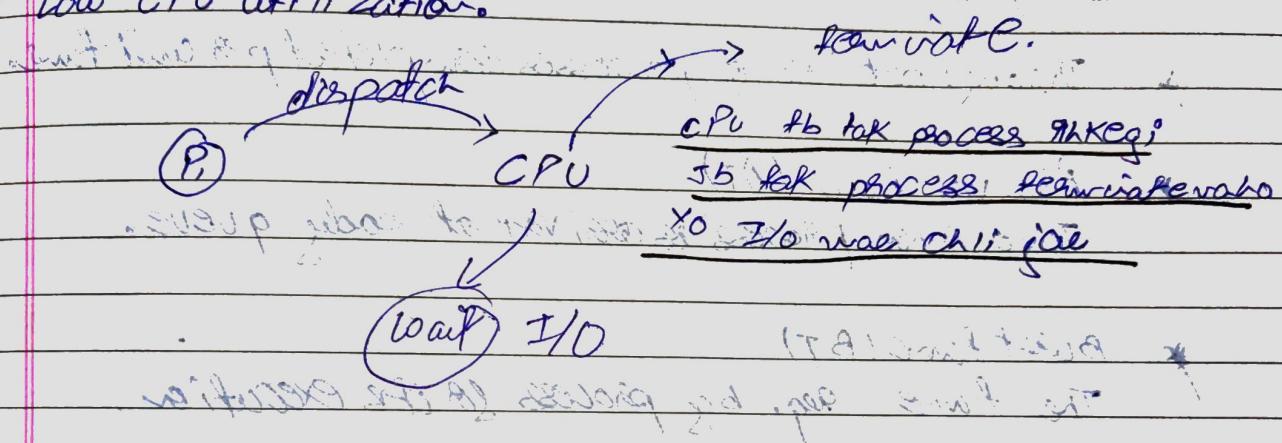
CPU Scheduling

- whenever the CPU becomes idle, OS must select one process from the ready queue to be executed.
done by STS.

20a

* Non-preemptive Scheduling

- once CPU has been allocated to a process keeps the CPU until it releases CPU either by terminating or by switching to wait state.
- starvation as a process with long burst time may starve less burst time process.
- less CPU utilization.



* Pre-emptive scheduling

- CPU is taken away from a process after time quantum expires along with terminating or switching to wait state.
- less starvation & high CPU utilization.

if time quantum expire hota tb hti

CPU cr8 diff

* Goals of CPU Scheduling

Maximum CPU utilization

Minimum turnaround time (TAT) $\rightarrow P_i$ ka ready time

Min wait time

Min response time

Max throughput of system.

\hookrightarrow P_i ka ready que. nae come se \rightarrow CPU

* Imp. Terms \rightarrow waiting time

* Throughput: No. of processes completed per unit time.

* Arrival Time (AT)

Time when process is arrived at ready queue.

* Burst time (BT)

The time req. by process for its execution.

* Turnaround Time (TAT)

Time taken from first time process enters ready statif it terminates. ($C_T = AT + WT$)

* Wait time (WT):

Time process spends waiting for CPU.

$$WT = TAT - BT$$

* Response Time: Time duration b/w process getting into ready que. & process getting CPU for first time

* Completion time (CT)

Time taken till process gets terminated.

FCFS (First Come, first serve)

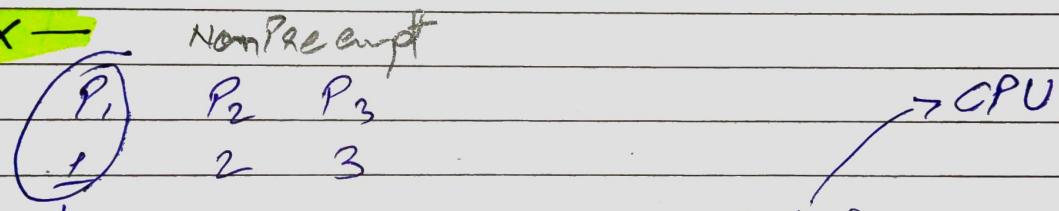
(a) whichever process comes first in the ready que. will be given CPU first.

(b) In this, if one process has longer BT. It will have major effect on avg. WT of diff processes. Called convoy effect

(C) Convoy effect is a situation where many processes who need to use a resource for a short time, are blocked by one process holding that resource for a long time.

- This cause poor resource management

Ex -

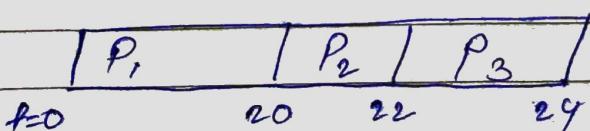


→ Selected first

CT - AT TAT - BT

P ₁ O	AT	BT	CT	TAT	WT
1	0	20	20	20	0
2	1	2	22	21	19
3	2	2	24	22	20
		33/3 → avg WT		13	

Gantt Chart



Page No. _____
Date _____

CT AT TAT-BT

Now.

Proc AT BT CT TAT WT

P₂ 0 2 2 2 0

P₃ 1 2 4 3 1

P₁ 2 20 24 22 2

and Durt:

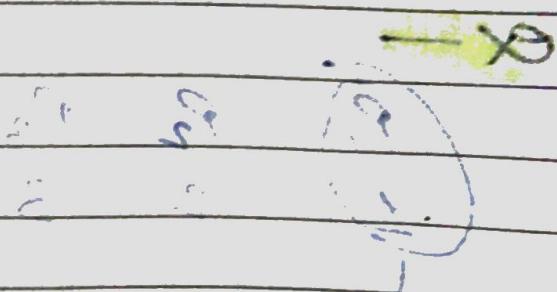
	P ₂	P ₃	P ₁
t=0	2	4	24

For ex: memory process file creation at both process

KO ~~but~~ but can't run parallel

Ex: program execution both serial & parallel

Ques.



LECTURE 13 | CPU scheduling | 3. SJF / Priority / RR.

Shortest job first

(@ Non-preemptive)

- Process with lowest BT will be dispatched to CPU first.
- Must do estimation for BT for each process in ready que.
- If beachard, exact estimation of BT is an impossible task ideally.
- Run lowest time process for all time than, choose job having lowest BT at that instance.
- This will suffer convoy effect as if the very first process which comes in Ready state is having a large BT.

• Process starvation might happen

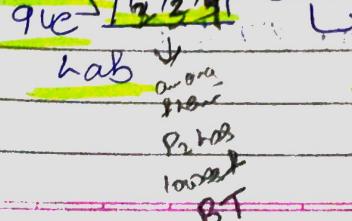
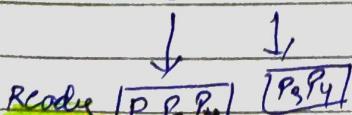
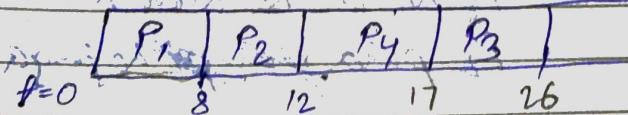
- Criteria for SJF algo, AT + BT.

AT + BT → ~~isochoreic~~ estimation by OS

Ex -

Proc AT BT CT JT T WT

Proc	AT	BT	CT	JT	T	WT
P1	0	8	8	8	0	0
P2	1	4	12	11	7	6
P3	12	9	26	24	15	13
P4	17	5	22	14	9	13
					avg	7.75



Wait
Arrive
Run
Finish
Leave
BT

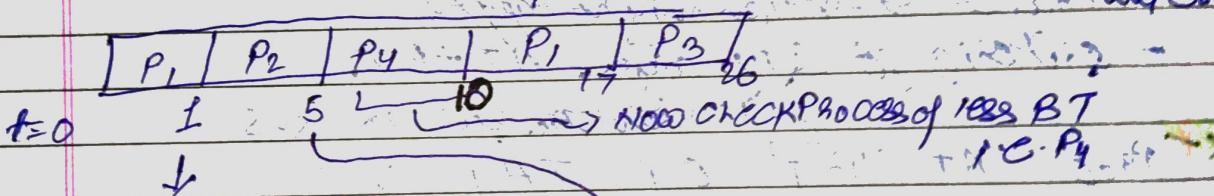
(b) SJF (Preemptive) AT + BT & Preemption

- Less starvation, no convoy effect

- Gives avg WTT for a given set of processes as scheduling short job before a long one decreases the WT of short job more than it increases the WT of the long process.

Process	AT	BT	CT	TAT	WT
P ₁	0	8	17	17	9
P ₂	1	4	5	4	6
P ₃	2	9	26	24	15
P ₄	3	5	10	7	2

$$\rightarrow P_1 \text{ BT} < P_3 \text{ BT} \rightarrow \text{avg} \rightarrow 6.5$$



P₁ → 1st prius (P₂) ojaega
then P₂ BT < P₁ BT
then P₂ preempted

Now P₁ BT → 8

Agar P₂ apna us ekta 1st
process ready gye ga
jaise but, P₃ & P₄, or BT
P₂ kya ho jaega

Note:

Really impossible kota h Kya KI BT kya
h and pta ke ni nahi ho skta.

Priority scheduling

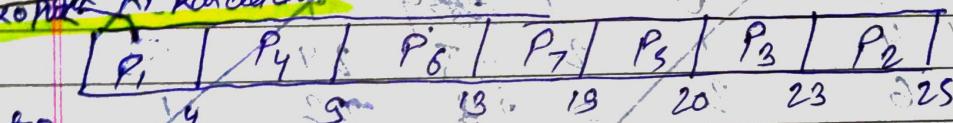
(a) Non preemptive

- priority is assigned to a process when it is created.
- SJF is a special case of general priority scheduling with priority inversely proportional to BT.

Ex:-

Process	Priority	AT	BT	CT	TAT	WT
P ₁	2	0	4	4	4	0
P ₂	4	1	2	25	22	22
P ₃	6	2	3	23	22	19
P ₄	10	3	5	9	6	1
P ₅	8	4	1	20	16	15
P ₆	12	5	4	13	8	4
P ₇	9	6	6	19	13	7

No P₁ waiting = 0 waiting processes
 No P₂ waiting = 1 waiting process



t=0

at t=4 ready

P₂ ready
 P₅

Now check high priority among
 remaining processes & schedule them.

i.e. P₆

at t=4 ready

[act proc]

check high pri

process

Now check high priority among
 rest of processes & schedule them.

(b) priority scheduling (Preemptive)

- current RUN state job will be preempted if next job has higher priority.
- May cause undesirable waiting (starvation) for lower priority jobs. (possibility is they won't get executed ever); (TRUE for both preemptive & non-preemptive version)

- Solution Awaiting is solo.
 - Gradually increase priority of process that wait so long. E.g. increase priority by 1 every 15 minutes.

EX -

	Process Priority	AT	BT	CT
1	2	0	43	25
2	4	1	21	22
3	6	2	252	21
4	10	3	53	12
5	8	4	1	19
6	12	5	4	9
7	9	6	6	18

avg WT $\rightarrow 11.4$

→ highest
PMP & Pre-scheduled logic

P ₁	P ₂	P ₃	P ₄	P ₅	P ₆	P ₇	P ₈	P ₉	P ₁₀	P ₁₁	P ₁₂
2	3	8	9	12	18	19	21	22	2	1	5

Pre
curr
th
at=0
idle
process
KO

Ready Q

Check

HIGH
Priority

i.e P₂

Process
Re AT

TK

& other
checks

its
Priority.

avg WT Non pre

for priority

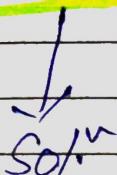
avg WT pre

for priority

Convoy effect \rightarrow highest pri job aati jaegi or
min BT high jata ja tha to ~~high~~ job ~~lowest~~
priority job phir wao niche jati ja gai.
ak KO mautka udi mil gada.

→ Drawbacks for pre & non pre priority Sched

— Indefinite waiting & Extreme starvation



Aging

Gradually giving priority of lowest priority job

for 15 min/sec → lowest priority job

lakhi priority ko (+)

kala jaunga th lowest priority job & lakh priority dhore dhore short jaungi.



Ex-1

Ex-1
→ 3 jobs ready
J1: 10 sec
J2: 10 sec
J3: 10 sec

Priority: J3 > J2 > J1
Time quantum = 5 sec

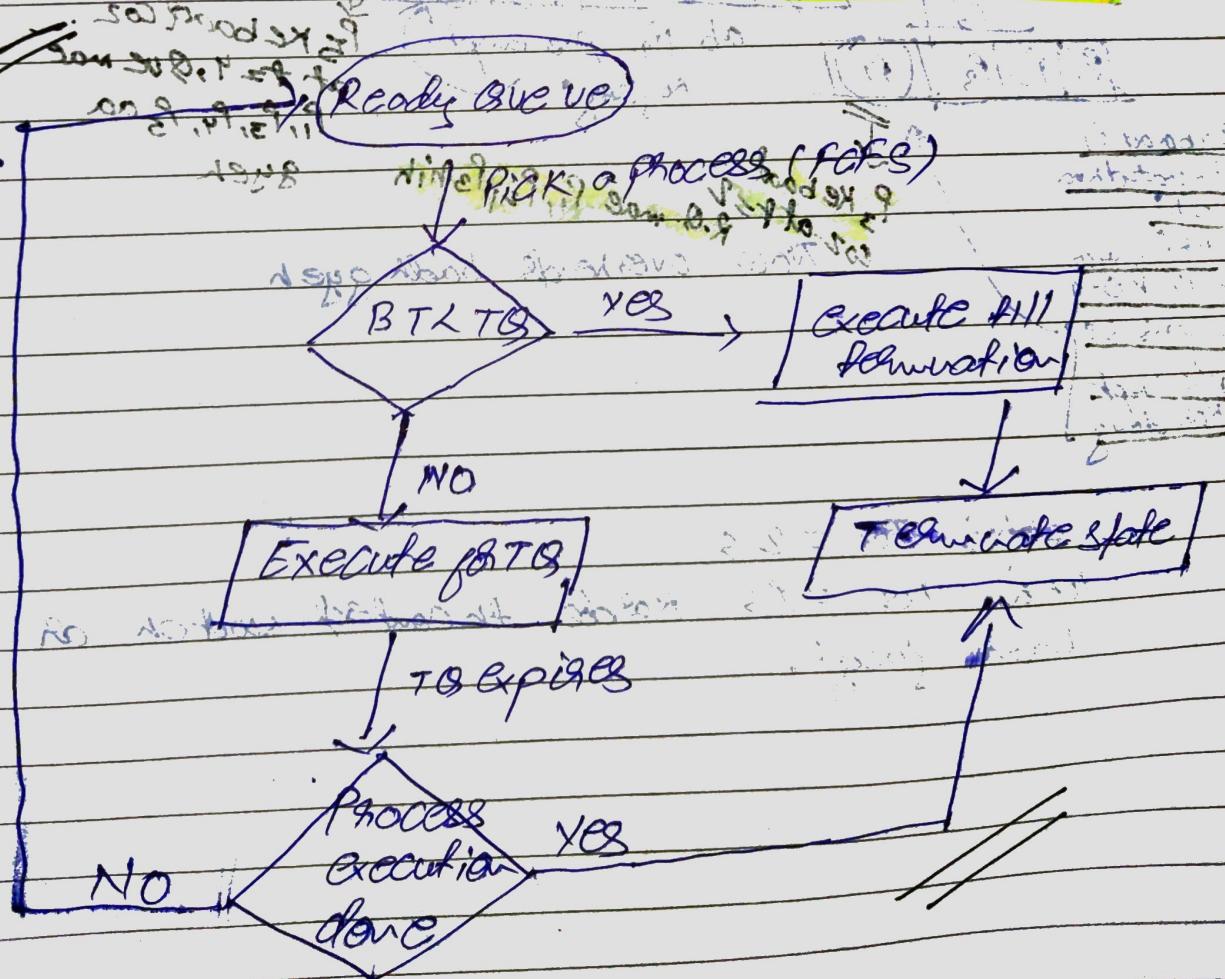
F Round robin scheduling (RR)

- most popular.
- like FCFS but preemptive.
- designed for time sharing systems.
- criteria: AT + time quantum (TQ), doesn't depend on BT.

- no process is going to wait forever, hence low starvation \Rightarrow no convoy effect.

- easy to implement

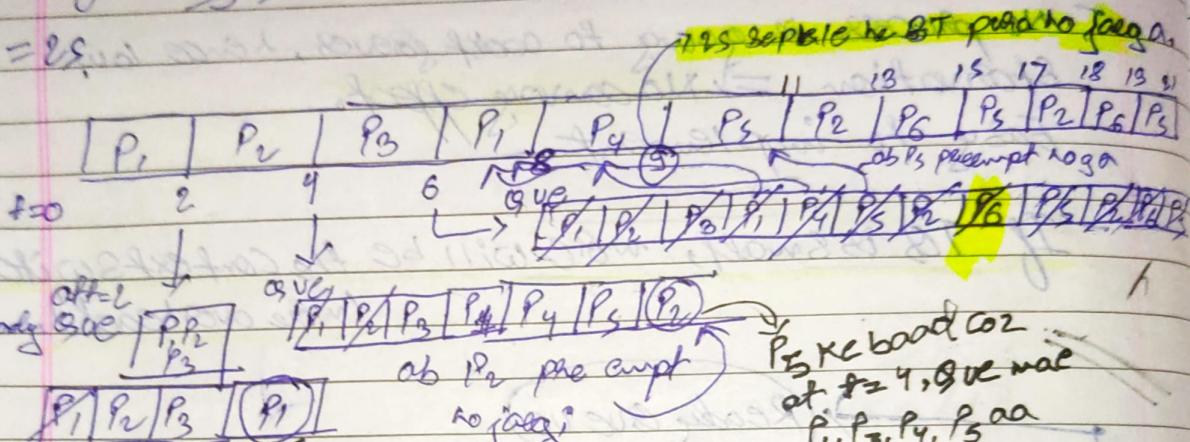
- If TQ is small, more will be the context switch (more overhead).



EX -

Process	AT	BT
1	0	4x0
2	1	5x0
3	2	2x0
4	3	1x0
5	4	6x0
6	6	3x0

$$TS = 2S$$



estab P1
terminated now
→ whi
whi gayo
th P1, KO
wasas be
pre crupt
KO doing

$$ab h; TS = 2S$$

1ekin TS = 1S kore, th confit switch or
badh jaege.

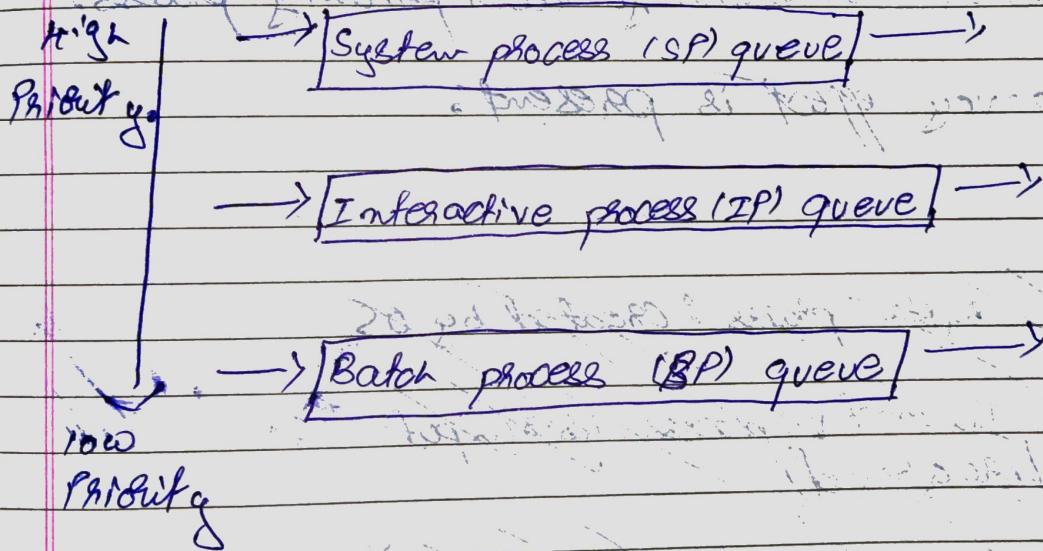
LEC-14 | MLQ | MLFQ.

Multi-level queue scheduling (MLQ).

- Ready queue is divided into multiple queues depending upon priority.
- A process is permanently assigned to one of the queues (unflexible) based on some property of process, memory, size, process priority or process type.

Co. Each queue has its own scheduling algorithms.

E.g. SP \rightarrow RR, IP \rightarrow RR & BP \rightarrow FCFS.



a. System process: Created by OS (highest priority)

b. Interactive process (foreground process): Needs user input (I/O)

c. Batch process (background process): Runs silently no user input req.

c. Scheduling among diff sub queries is implemented as fixed priority preemptive scheduling. E.g. foreground queue has absolute priority over background queue.

f. If an interactive process comes & batch process is currently executing then, batch process will be preempted.

g. problem: only after completion of all the processes from the top level ready queue, the further level ready queues will be scheduled. This can be starvation for lower priority process.

h. Convoy effect is present.

SP QBC }
 Q-PR IP QBC }
 BP QBC } CC scheduling

Q-FIFO
 In process mo BC ek mae chha
 jaega.

(P₁)

SP > IP > BP

P₁ → ko inao process aya & P₂ → already scheduled to CPU
 ↓
 IP process batch
 Process

⇒ abhi P₂ jo ki (BP) h usko preempt
 koi lunga and jo new process a P₁-IP
 usko wai schedule koi lunga

Jab tak top phr SP ki process terminate nahi
 ho jati kaki nexte wali process IP or SP ko
 waikar nahi milega.

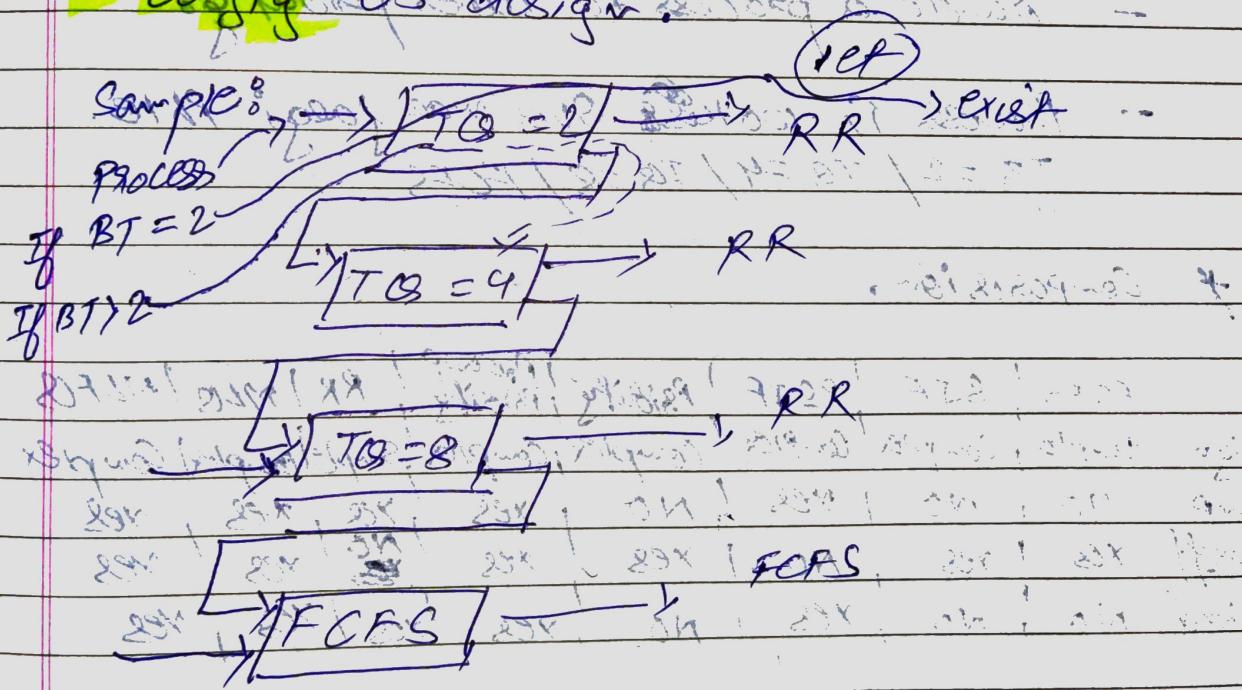
⇒ starvation hogi

Multilevel feedback queue scheduling (MLFQ)

- (a) Multiple sub queues are present.
- (b) Allows the process to move b/w queues. The idea is to sep. processes according to the characteristics of their BT. If a process uses too much CPU time it will be moved to lower priority queue. This scheme leaves I/O bound & interactive processes in the higher priority queue.
In addⁿ, a process that waits too much in a lower priority queue may be moved to a higher priority queue. This form of ageing prevent starvation.
- c. Less starvation than MLQ & more efficient & it is flexible
- d. Can be configured to meet a specific system requirement sample MLQ design diagram.

- Multiple sub queues
- Inter queue movement is allowed.
- Sched. process based on BT.
- ~~BT ↑ => 1000s queues.~~
- I/O bound & interactive process
 \Rightarrow higher priority
- ageing method: lower priority process until priority increases. Koala going to avoid convoy effect.

- Config OS design



~~ye koalne se lower BT processes wo store nahi ho skte~~

Agar koi process $TQ = 2 \rightarrow TQ = 4 \rightarrow TQ = 8$

issle ~~ISSL~~ FCFS
Shaweto ~~SWTO~~
ERDA L

Design of MLFQ.

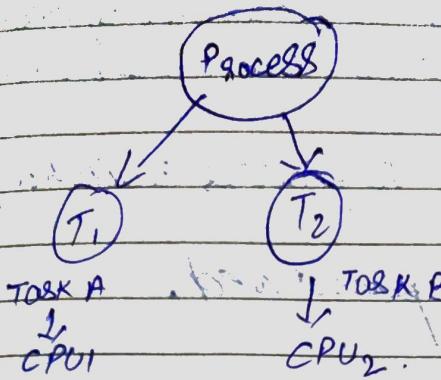
- No. of Queues
- Scheduling Algo in each Queue
- Method to upgrade a process to higher Queue.
- demote a process to lower priority
- Process P_A works in que was facing pte.
 $TQ = 2 / TQ = 4 / TQ = 8 / FCFS$

Comparison.

	FCFS	SJF	PSJF	Priority	Pri. exp	RR	MLO	MLFQ
Design	Simple	Complex	Complex	Complex	Complex	Simple	Complex	Complex
Pri. exp	No	No	Yes	No	Yes	Yes	Yes	Yes
Convoy off	Yes	Yes	No	Yes	Yes	No	Yes	Yes
overhead	No	No	Yes	No	Yes	Yes	Yes	Yes

LEC-15 / Concurrency / Threads.

- * Concurrency is the execution of multiple instruction sequence at the same time. It happens in the OS when there are several process threads running in I/O.

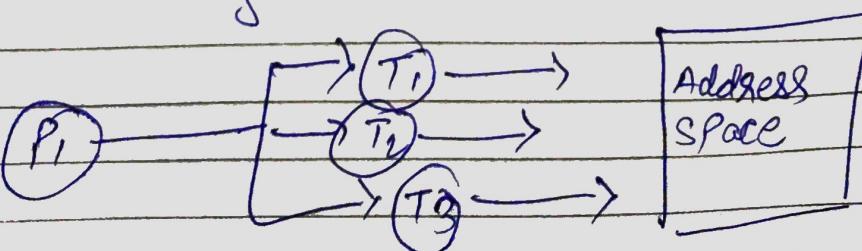


Forms of concurrency (short notes)

- * Thread threads are going with the process.
- Single seq stream within a process.
- An independent path of execution in a process.
- Light weight process.
- used to achieve parallelism by dividing a process's task which are independent path of execution.
- E.g.- Multiple tabs in a browser, text editor.

* Thread scheduling

Threads are scheduled for execution based on their priority. Even though threads are executing within the runtime, all threads are assigned to processor time slices by the OS.

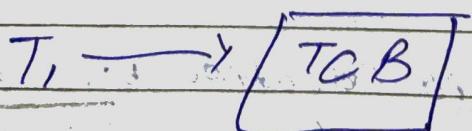


* Thread context switching

- OS saves current state of thread & switches to another thread of same process.
- Doesn't involves switching of memory address space.
(But program counter, reg offset & stack are included)
- Fast switching as compared to process switching
- CPU's cache state is preserved.

* How each thread get access to CPU?

- Each thread has its own program counter.
- Depending upon the thread scheduling algo., OS schedule these threads depending upon the Thread Scheduling algo.
- OS will fetch instructions corresponding to PC of that thread & execute instruction.



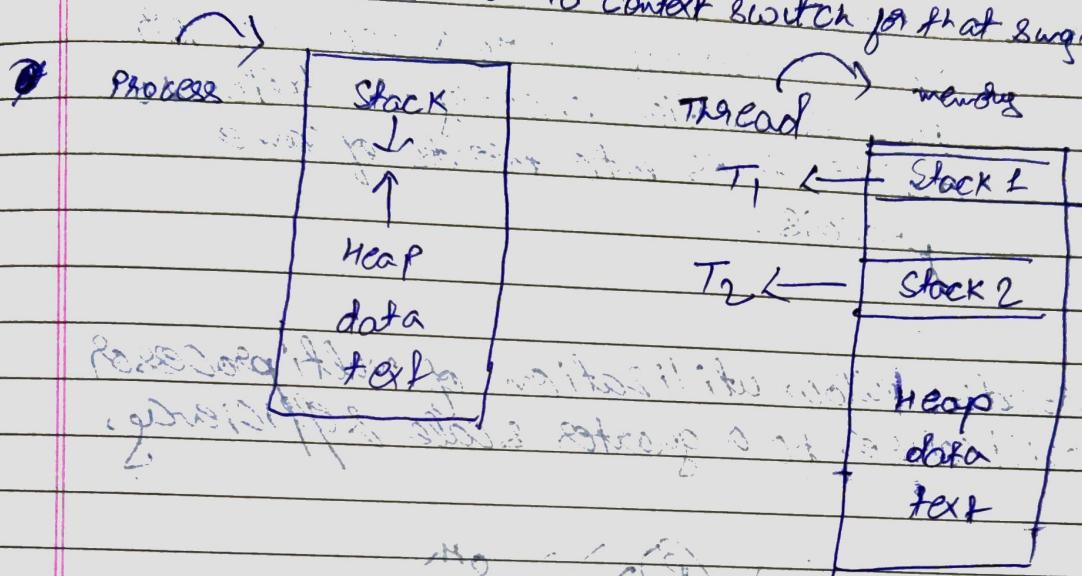
Thread
Control
block

I/O or TQ, based context switching is done here is well.

- we have TCB like PCB for state storage management while performing context switching.

Will Single CPU system could gain by multi threading techniques?

- Never
- As two threads have to context switch for that single CPU.



Benefits of Multithreading

① Responsiveness.

e.g. interactive app → taking input from user

→ I/O Bhi ho aka a

→ Internet se fetching bhi ho raha

yaha independent bkt same path chke
user se input per ka path

download kane ka diff path

back up kane ka diff path

it mai inn sbko threads wae divide ke data hu

⇒ Agar koi koi thread I/O wae block hoga ta

baki threads formating jaise task-3 ki rate the

in wo particular process block nahi hoga

② Resource sharing

address space of same process threads are same so allow resource sharing.

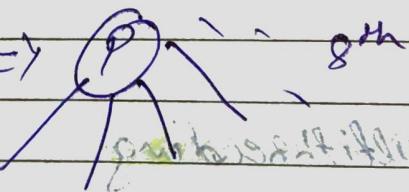
③ Economical

- ~~process context~~

- It is more economical to create & context switch threads.

⇒ Also, allocating memory & resources for process creation is costly, so better to divide tasks into threads of same process.

④ Threads allow utilization of multi processor architectures to a greater scale & efficiency,

Ex- 8 core ⇒  8th

Can be divided into 8 independent tasks.

Can be provided to 8 independent CPUs.