

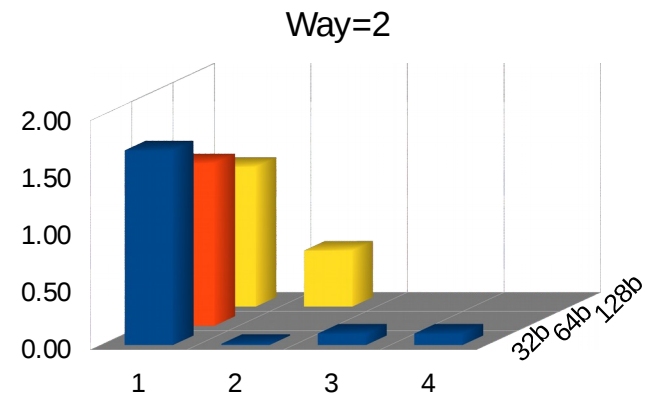
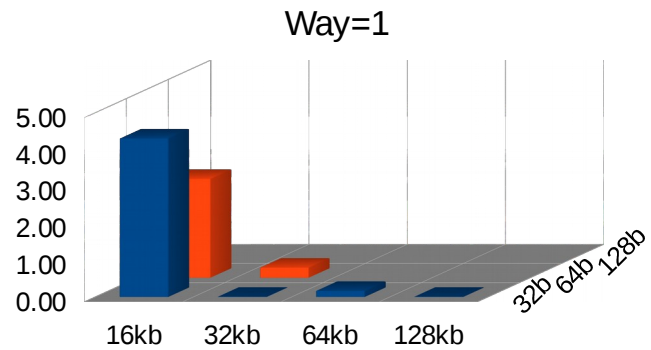
icache Cache miss %, less is better

Way=1	Total size	16kb	32kb	64kb	128kb
Line size					
32b		4.31	0.01	0.17	0.01
64b		2.69	0.28	0.00	0.00
128b		0.00	0.00	0.00	0.00

Way=2	Total size	16kb	32kb	64kb	128kb
Line size					
32b		1.70	0.01	0.10	0.10
64b		1.42	0.00	0.00	0.00
128b		1.22	0.49	0.00	0.00

Way=4	Total size	16kb	32kb	64kb	128kb
Line size					
32b		0.01	0.01	0.01	0.01
64b		0.00	0.00	0.00	0.00
128b		0.00	0.00	0.00	0.00

Way=8	Total size	16kb	32kb	64kb	128kb
Line size					
32b		0.01	0.01	0.01	0.01
64b		0.00	0.00	0.00	0.00
128b		0.00	0.00	0.00	0.00

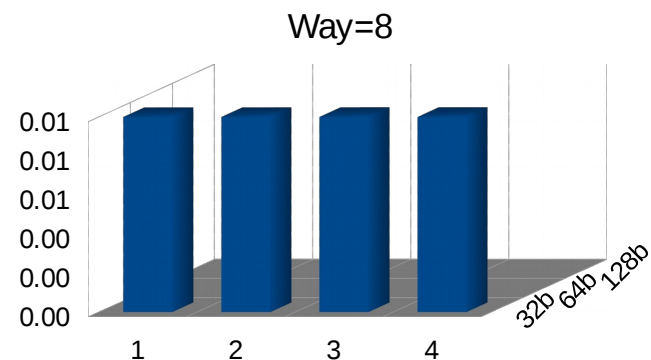
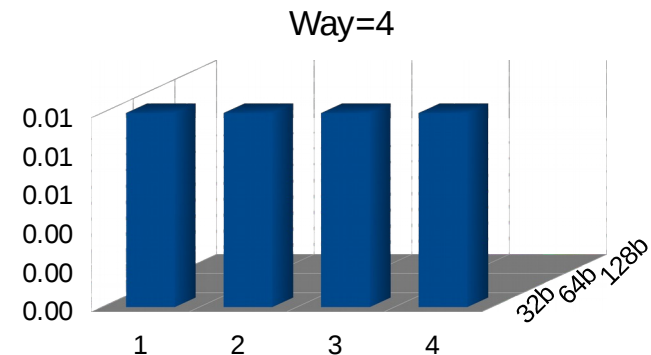


dcache Load+store miss %, less is better

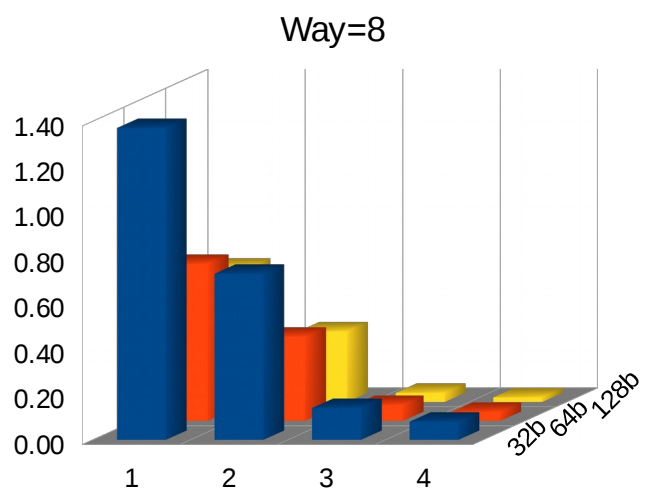
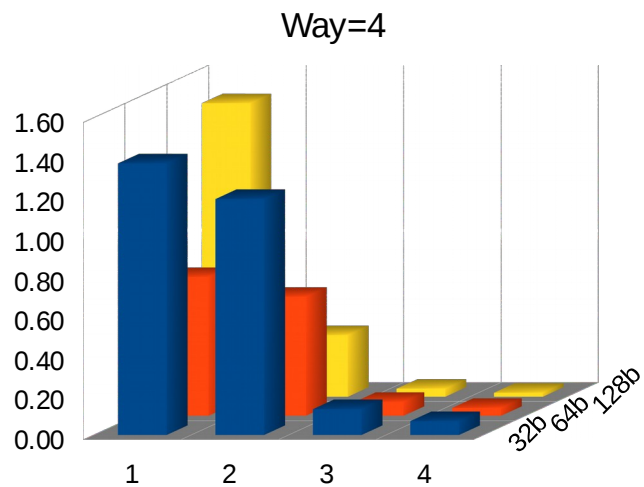
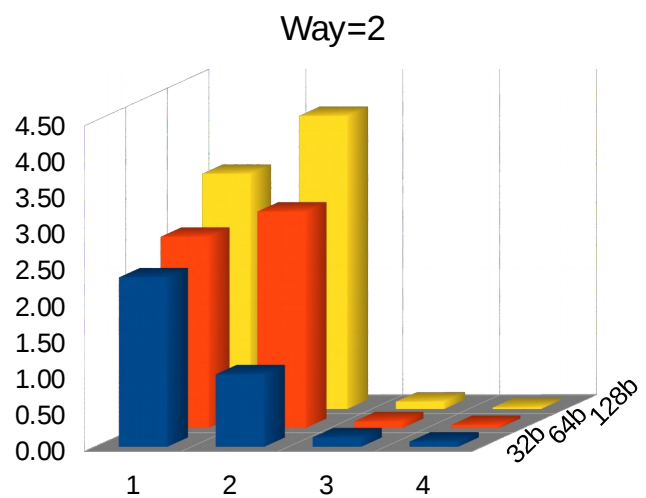
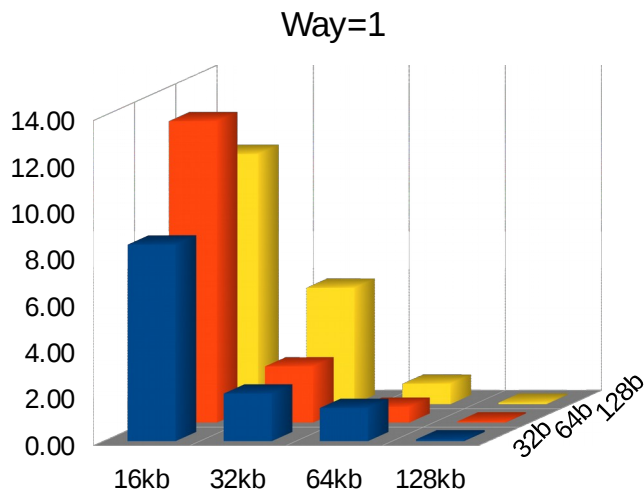
Way=1	Total size	16kb	32kb	64kb	128kb
Line size					
32b		8.47	2.05	1.43	0.08
64b		12.98	2.42	0.68	0.05
128b		10.77	5.01	0.88	0.06

Way=2	Total size	16kb	32kb	64kb	128kb
Line size					
32b		2.34	1.00	0.13	0.07
64b		2.64	2.99	0.09	0.04
128b		3.25	4.05	0.10	0.02

Way=4	Total size	16kb	32kb	64kb	128kb
Line size					
32b		1.37	1.19	0.13	0.07
64b		0.70	0.60	0.07	0.04
128b		1.48	0.31	0.04	0.02



Way=8	Total size	16kb	32kb	64kb	128kb
Line size					
32b		1.37	0.73	0.14	0.08
64b		0.69	0.37	0.07	0.04
128b		0.59	0.31	0.04	0.02



Increasing ways seems to increase performance in smaller cache, but doesn't have much change in larger cache size. Increasing cache size increase performance in linearly manner.