Date:				cCARD F	Pinout:	F2800137				
p2021										
	HS	EC pin	MCU pin	MCU Usage for Std	HSEC cCARD standard	HSEC cCARD standard	MCU Usage for Std	MCU pin	HSEC pin	
		1 3	- TMS		JTAG-EMU1 JTAG-TMS	JTAG-EMU0 JTAG-TRSTn			2 4	
		5	тск		JTAG-TCK	JTAG-TDO		GPIO-37/TDO *	6 8	
		7 9	VSS A0/C15/DACA	Analog, DACA	GND ADC1 (and/or DACA)	JTAG-TDI GND		GPIO-35/TDI * VSS	10	
		11	A1	Analog	ADC1 (and/or DACB) GND	ADC2	Analog	C0/A11	12	
		13 15	A2/C9/GPIO224	Analog	ADC1 (and/or CMPIN+)	GND	Analog	C1/A12	14 16	
			A3/C5/GPIO242	Analog	ADC1	ADC2	Analog	C4/A14	18	
-		19 21	VSS A4/C14	Analog	GND ADC1 (and/or CMPIN+)	ADC2 GND	Analog	C6/GPIO226 VSS	20 22	-
Analog	2	23 25	A5/C2 A6/GPIO228	Analog Analog	ADC1 ADC (and/or CMPIN+)	ADC2	Analog Analog	C7/A15 C8/A9/GPIO227	24 26	go
	5	27	A7/C3	Analog	ADC (and/or CMPINT)	ADC	Analog	C10/A10/GPIO227	28	Analog
		29 31			GND ADC	ADC Rsv	Analog	C11/A8 **	30 32	u\
_ Q	١	33			ADC	ADC	Analog	C11/A8 **	34	4
		35 37	VSS		GND ADC	ADC GND		- VSS	36 38	
		39			ADC	ADC		• • • • • • • • • • • • • • • • • • • •	40	
		41			Rsv VREFLO	ADC Rsv			42 44	-
		45	VREFHI	VREFHI	VREFHI	GND		vss	46	
		47 49	VSS GPIO-00	PWM1A	GND PWM1A	5V0 PWM3A	PWM3A	GPIO-04	48 50	
		51	GPIO-01	PWM1B	PWM1B	PWM3B	PWM3B	GPIO-05	52	
-		53 55	GPIO-02 GPIO-03	PWM2A PWM2B	PWM2A PWM2B	PWMA PWMB	PWM4A PWM4B	GPIO-06 GPIO-07	54 56	
-		57 59	GPIO-08 GPIO-09	PWM5A PWM5B	PWMA PWMB	PWMA or TZ1 PWMB or TZ2	PWM6A PWM6B	GPIO-18/X2 GPIO-19/X1	58 60	
		61	GPIO-12/A20/C20	PWM7A	PWMA	PWMA or TZ3	r vvivios	GPIO-40	62	
-1		63 65	GPIO-13/A19/C19	PWM7B	PWMB GND	PWMB or TZ4 Rsv		GPIO-41	64 66	
		67	GPIO-16	SPISIMOA	SPISIMO	QEPA or McBSP-MDX	QEP1A	GPIO-20/A17/C17	68	
-		69 71	GPIO-17 GPIO-10	SPISOMIA SPICLKA	SPISOMI SPICLK	QEPB or McBSP-MDR QEPS or McBSP-MCLKX	QEP1B QEP1S	GPIO-21/A18/C18 GPIO-22	70 72	
		73	GPIO-11	SPISTEA	SPISTE	QEPI or McBSP-MFSX	QEP1I	GPIO-23	74	
		75 77	GPIO-24 # GPIO-39		eCAP or SPISIMO eCAP or SPISOMI	SCIRX/UARTRX SCITX/UARTTX or ERRORSTS	SCIRXDA SCITXDA	GPIO-28/A16/C16 @ GPIO-29 @	76 78	
		79	-		eCAP or SPICLK	CANRX	CANRXA	GPIO-35/TDI *	80	
		81 83	- VSS		eCAP or SPISTE GND	CANTX	CANTXA	GPIO-37/TDO *	82	
		85	GPIO-32 #	SDAA	12CSDA	GPIO			86	
		87 89	GPIO-33 GPIO-40 ***	SCLA	GPIO GPIO	GPIO GPIO			88 90	
		91	GPIO-41 ***		GPIO	GPIO			92	
		93 95			GPIO GPIO	GPIO GPIO			94 96	
		97	VSS		GND				98	
		99 101	-		SD-D SD-C	QEPA QEPB			100 102	
		103			SD-D	QEPS			104	
		105 107			SD-C SD-D	QEPI GPIO or McBSP-MCLKR			106 108	
_		109	-		SD-C	GPIO or McBSP-MFSR			110	Te
- 5	3	111 113	VSS -		GND Rsv	5V0 Rsv			112 114	ij
Digital	۵	115			Rsv	Rsv			116	Digital
- Ö	2	117 119	VDDIO		RSV	Device Reset (Active low)	XRSn	VDD XRSn	118 120	
		113	10010		1000	Device reset (vetre low)	жы	Aligh	11.0	
		121			GPIO	GPIO			122	
		123 125			GPIO	GPIO			124	
		127			GPIO GPIO	GPIO GPIO			126 128	
-		129 131			GPIO GPIO	GPIO GPIO			130 132	
1		133			GPIO	GPIO			134	
-		135 137			GND GPIO	Rsv GPIO			136 138	
1		139			GPIO	GPIO			140	
-		141 143			GPIO	GPIO GPIO			142 144	
		145			GPIO GPIO	GPIO GPIO			146	
-		147			GPIO GPIO	GPIO GPIO			148 150	
1		149 151			GPIO	GPIO			152	
1		153 155			GPIO GPIO	GPIO GPIO			154 156	
1		155 157			GPIO GND	5V0			158	
		159			GPIO	GPIO			160	
1		161 163			GPIO GPIO	GPIO GPIO			162 164	
		165			GPIO	GPIO			166	
		167 169			GPIO GPIO	GPIO GPIO			168 170	
-		171			Rsv	Rsv			172	
-		173 175			Rsv Rsv	Rsv Rsv			174 176	
<u> </u>		177			Rsv	ERRORSTS			178	
-		179			GND	5V0			180	
				-						
-	* T	his set o	f signals can be routed to one of two	o HSEC pins via switch (HSEC	pins 80 & 82 by default)					
	**	This sign	al can be routed to one of two HSE	C pins via switch (HSEC pin 3	by default)					
		* Signal of Boot mod	an be routed to this HSEC pin if unp	oopulated resistor is populate	ed					
			- p1							
			onnected to PC terminal via switch							