

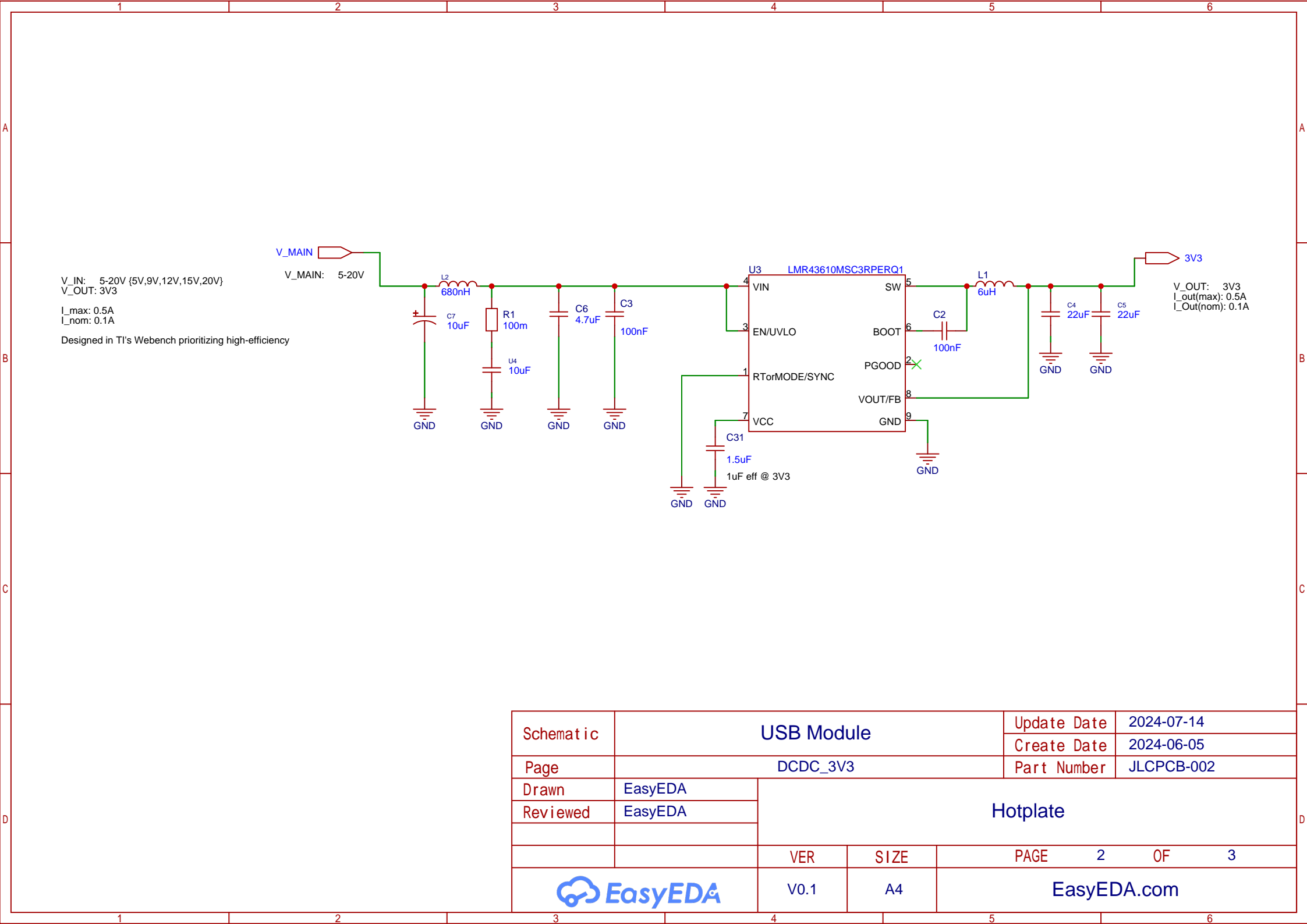
V_MAIN
Vout = 5V...20V
{5V,9V,12V,15V,20V}
Imax = 5A

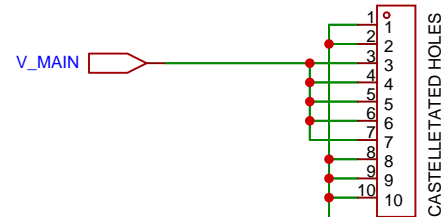
When USB PD source capability is
below Sink request set by ADCINx
Highest Source voltage/current will be provided
CAP_MIS flag will be low

ADCINx Voltage Dividers
[Datasheet]
Decoding: Table 8-1
ADCIN1: Table 8-4
ADCIN2: Table 8-5
ADCIN3/4: Table 8-6

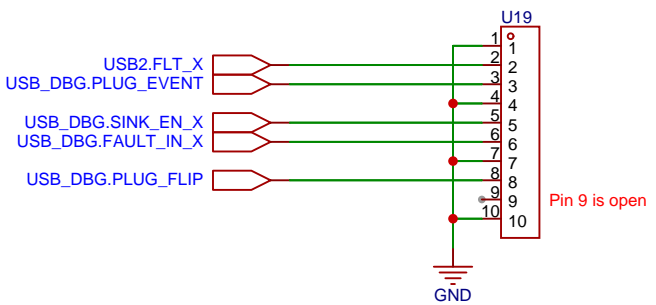
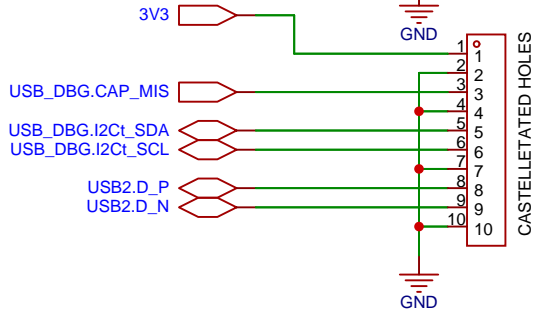
Pin	Min Voltage	Max Voltage	Current (Operating)	Current (Max)
ADCIN1	20V	20V	5A	5A
ADCIN2	20V	20V	5A	5A
ADCIN3	20V	20V	5A	5A
ADCIN4	20V	20V	5A	5A


Schematic	USB Module		Update Date	2024-07-14
			Create Date	2024-06-05
Page	USB		Part Number	JLPCB-002
Drawn	EasyEDA	Hotplate		
Reviewed	EasyEDA			
		VER	SIZE	PAGE 1 OF 3
		V0.1	A4	EasyEDA.com





1.27mm Pins have current capacity of 1.20A
I_max = 5A -> 5 pins each for V_MAIN and GND



Schematic	USB Module			Update Date	2024-07-14
				Create Date	2024-07-03
Page	Module Interface			Part Number	JLCPCB-002
Drawn	EasyEDA	Hotplate			
Reviewed	EasyEDA				
		VER	SIZE	PAGE 3	OF 3
		V0.1	A4	EasyEDA.com	