

**CMSI 387-01**  
**OPERATING SYSTEMS**  
Spring 2014

**Assignment 0422 Feedback**

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**Dining Philosophers**

*2d* — The philosophers thank you for feeding them :) (+)

*4a* — You've gotten the core solution right—but you need to make it clear to the user that this is the case. Thus, suggestions given inline are primarily refinements to output format so that the user can perceive what's happening more easily, particularly with respect to errors. Speaking of errors, you have a nice, loud one at exactly the right place, between the mutex lock and changing the chopstick state. For symmetry, you should have a similar one right before you mark a chopstick as being down again (with corresponding failure if the chopstick is already down at that point). (|)

*4b* — Code is decently structured with logically separated/sequenced sections. (+)

*4c* — I was able to read your code pretty easily, except for...TAAAAABBBBBBSSSS! Few enough to be considered an oversight, but still, the way editors are today, there's not really a reason to have even a stray tab in source code anymore, right? (/)

*4d* — Your work demonstrates good use of the information available to you. (+)

*4e* — Commit frequency and messages are appropriate to the work done. (+)

*4f* — Largely submitted on time, with enhanced error messages one day later. (+)

**Paged Memory Address Translation**

*2d* — You have successfully implemented paged memory address translation from the ground up. (+)

*4a* — No issues with your code outside of that unnecessary bitwise-&. Otherwise works as spec'ed. (+)

*4b* — One little separation of concerns tweak would be unhardcoding 256. But overall n.b.d. (+)

*4c* — Clean code, no big issues. (+)

*4d* — You successfully utilized available information to implement paged memory address translation. (+)

*4e* — Commit frequency and messages are appropriate to the work done. (+)

*4f* — Submitted on time. (+)