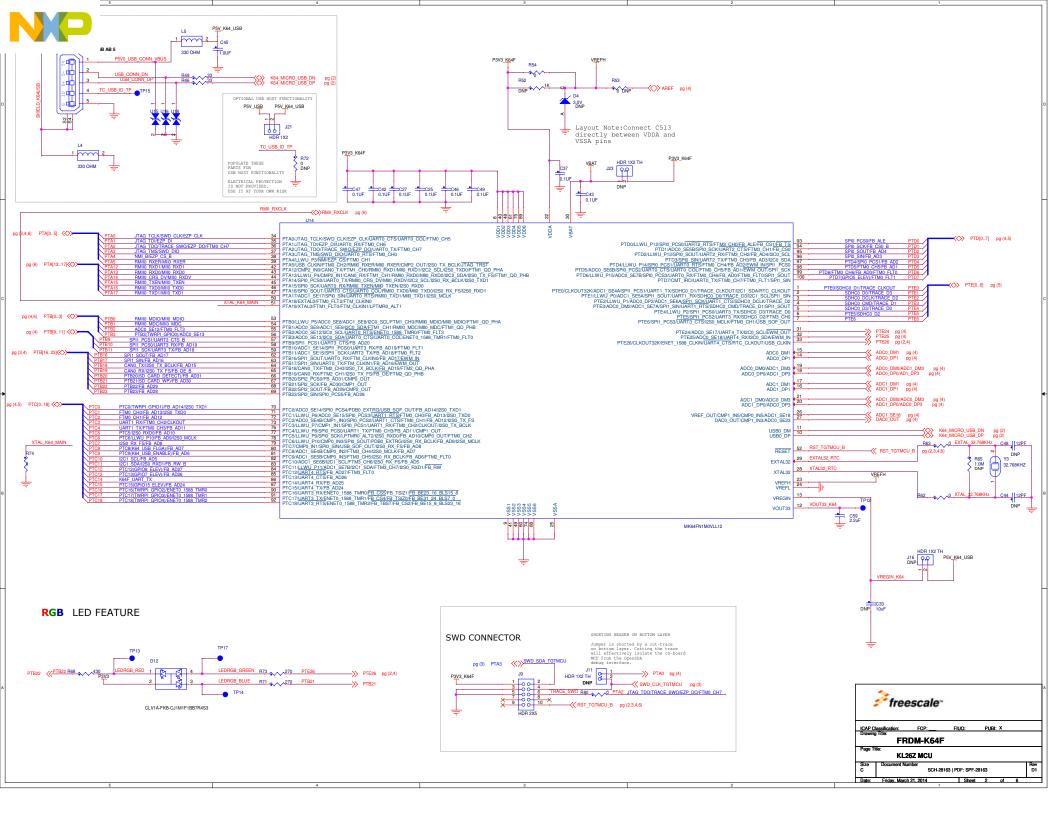
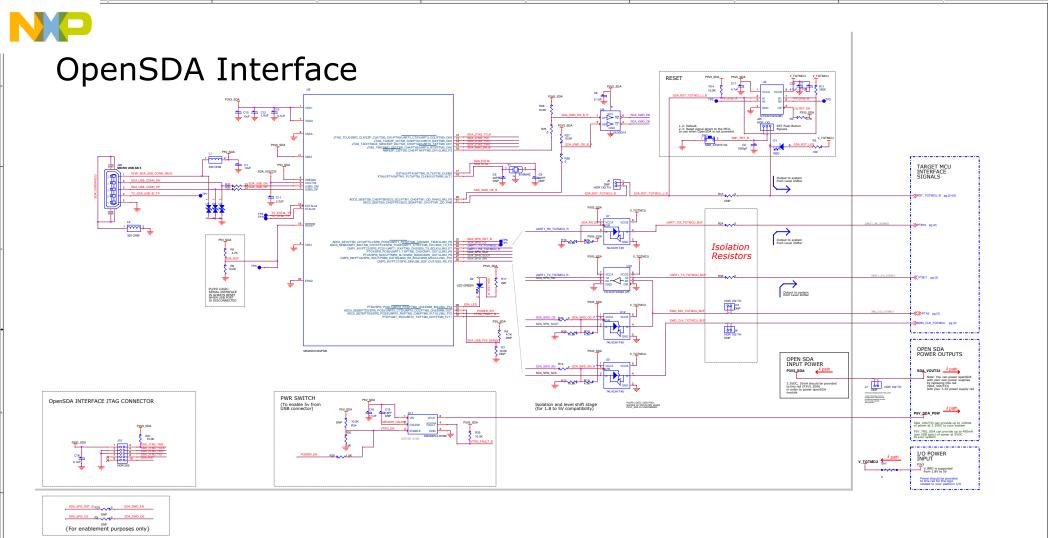
R		Contents	
_	, 0,000000 10010	RFACE	
5	I/O Headers a	and Power Suppl	У

FREEDOM K64F

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	Revisions & Chang	ge Log		ı
Rev	Description	Date	Approved	ı
X1	Initial Draft	Oct 18	Chung	ı
X2	Feedbacks Implemented	Oct 22	Chung	ı
хз	Feedbacks from Chung Implemented	Oct 23	Chung	ı
X4	Components changed as per the feedbacks	Oct 24	Chung	ı
X5	RF and Bluetooth headers added	Oct 28	Chung	D
X6	Arduino connections added	Nov 1	Chung	ı
Х7	Net review and fix	Nov 3	Chung	ı
A	Release to production, prototype build	Nov 8	Chung	ı
A1	openSDA reset capacitor fix	Nov 22	del Rey	ı
В	Release to production, production build	Jan 20	Chung	ı
С	Release to production, production build	Feb 06	Chung	Н
D	Fixing I2C swap to headers	Feb 26	Chung	ı
D1	Enhancing USB PWR input filter for robustness	Mar 18	Chung	ı

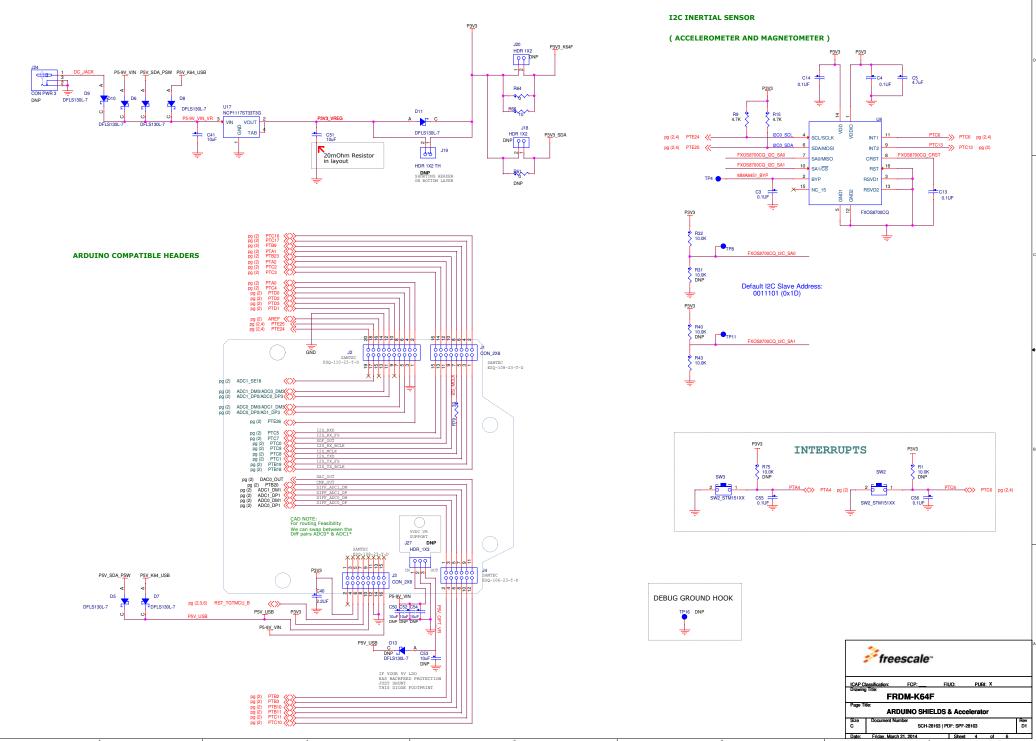
freescale		Automotive, Industrial & Multi- Market Solutions Group 6501 William Cannon Drive West Austin, TX 78735-8598							
Designer: Rafael del Rey	Drawin	Title:	Classification:	FCP:		FIUO:		PUBI:	x
Drawn by: Sudhasha(LnT)	Page T		TLE PAGE						
Approved: Chung	Size C	Document	ent Number SCH-28163 PDF: SPF-28163			Rev D1			
	Date:	Friday, M.	arch 21, 2014		Sheet	1	of	6	



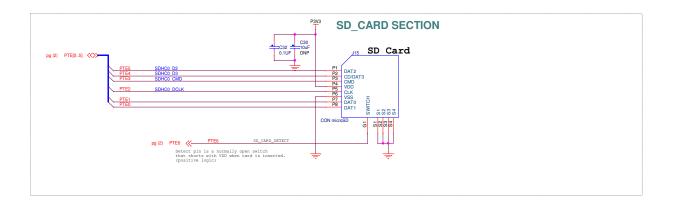


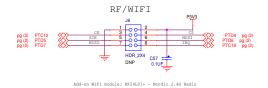




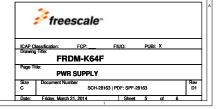




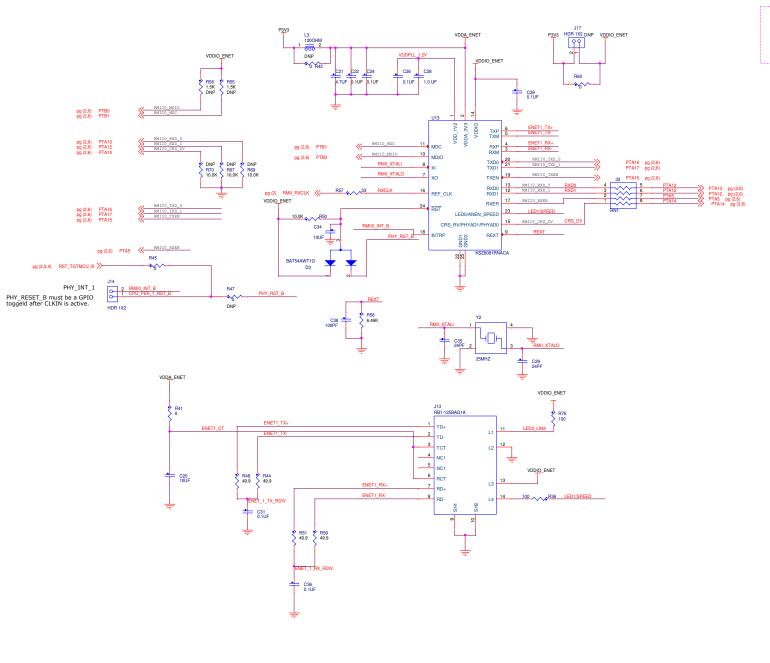












LAYOUT NOTES:

1. The TRO+/TD-pairs should be routed with a 1000hm of the TRO+/TD-pairs and a 500hm single ended (characteristic) impedance.

2. The trace lengths within a TRD+/TRD- differential pair should be matched.

3. The distance between each TRD+/TRD- differential pair should be 50mils or more.

