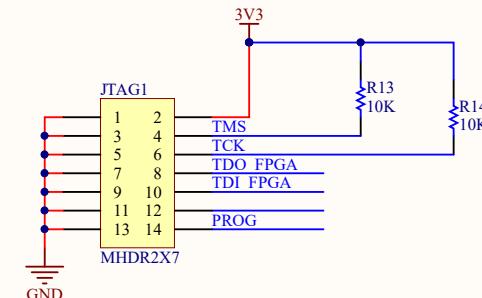
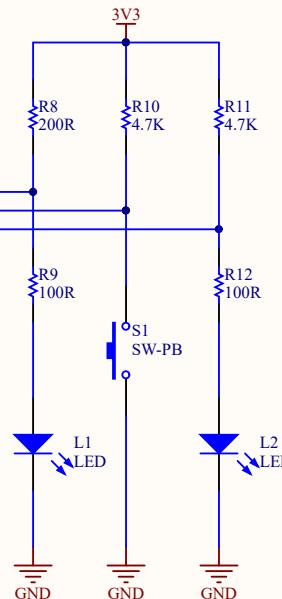
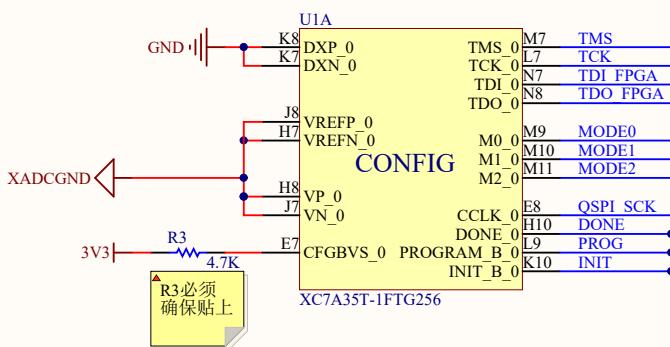


FPGA Power

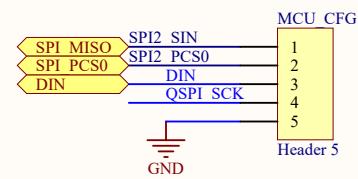


Title		
Size	Number	Revision
A4		
Date: 2018/6/17	Sheet of	
File: G:\通用平台\PCB\FP0G3A\catalog.SchDoc	Drawn By:	

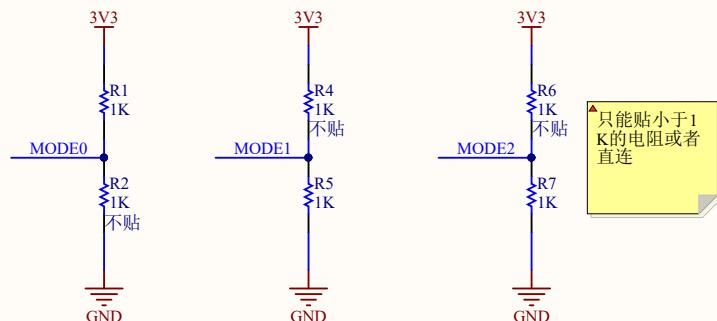
A



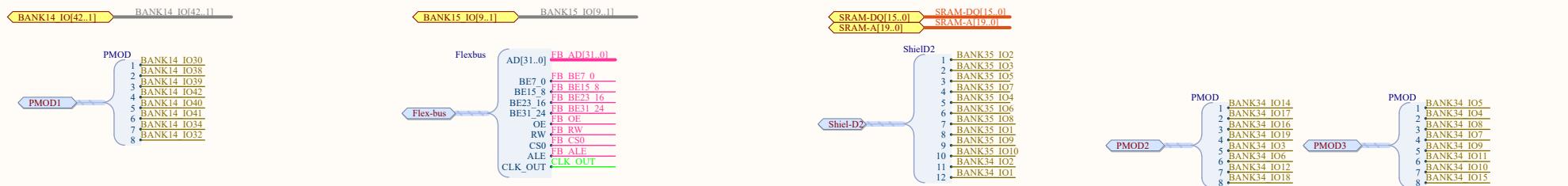
B



C

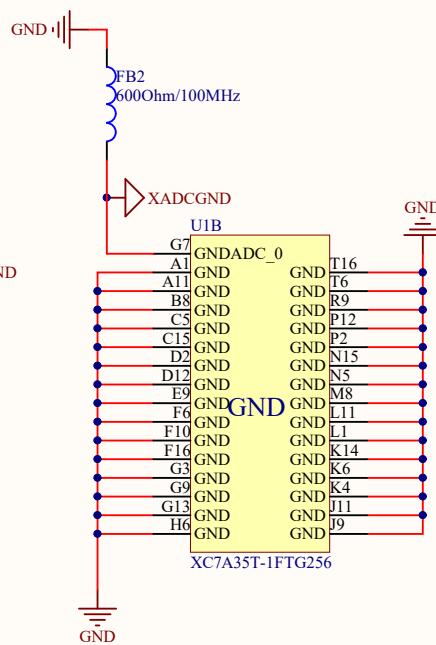
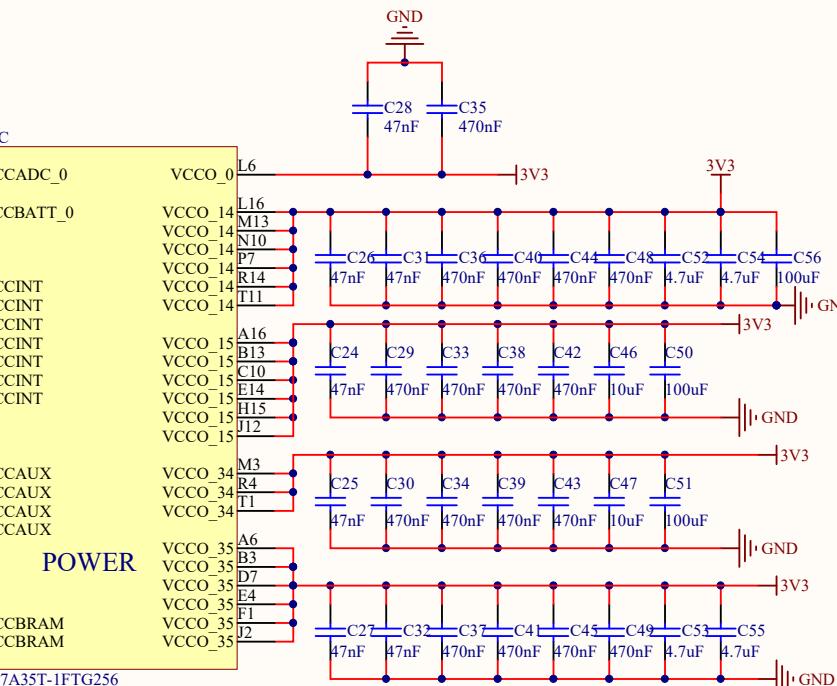
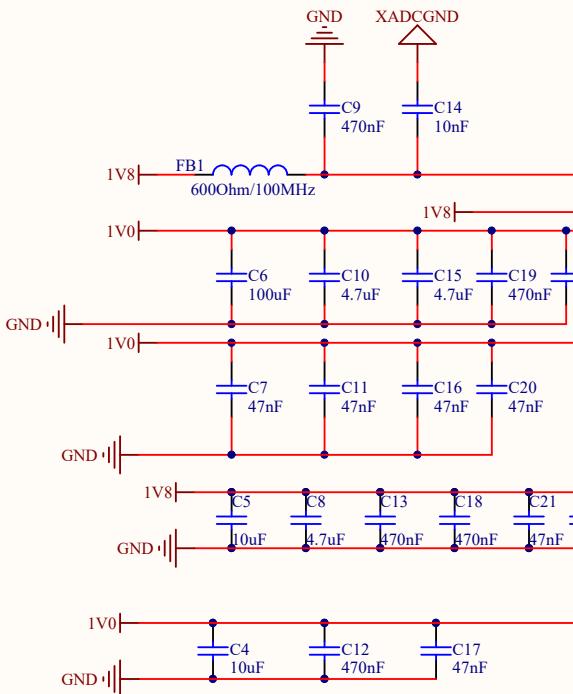


Title		
Size	Number	Revision
A4		
Date: 2018/6/17	Sheet of	
File: G:\通用平台\PCB\FP0G3A\FPGA_CONDIG.schDoc	DIGI SchDoc	



	UID	UIE	UIG	UIF		
BANK14	<p>IO_0_14 K12 BANK14 IO1 IO_LIP_T0_D00_MOSI_DIN J13 QSPI_DQ0 IO_LIN_T0_D01_DIN J14 QSPI_DQ1 IO_L2P_T0_D02_DIN J15 QSPI_DQ2 IO_L2N_T0_D03_DIN J16 QSPI_DQ3 IO_L3P_T0_DQS_PUDC_B M15 R20_W1K IO_L3N_T0_DQS_EMGCLKL J14 BANK14 IO2 IO_L4P_T0_D04_DIN M14 BANK14 IO3 IO_L5P_T0_D05_DIN K13 BANK14 IO4 IO_L5N_T0_D07_DIN J13 BANK14 IO5 IO_L6P_T0_FCS_B J12 QSPI_CS IO_L6N_T0_D08_VREF M12 BANK14 IO6 IO_L7P_T1_D04_DIN M16 BANK14 IO7 IO_L7N_T1_D10_DIN P15 BANK14 IO8 IO_L8N_T1_D12_DIN P16 BANK14 IO9 IO_L9P_T1_D05_DOS J14 BANK14 IO10 IO_L9N_T1_D05_D13_D14 J14 BANK14 IO11 IO_L10P_T1_D14_D15 J14 BANK14 IO12 IO_L10N_T1_D15_D14 N13 BANK14 IO13 IO_L11P_T1_SRCC_B J13 BANK14 IO14 IO_L11N_T1_SRCC_B J13 BANK14 IO15 IO_L12P_T1_MRCC_B J14 BANK14 IO16 IO_L12N_T1_MRCC_B J14 BANK14 IO17 IO_L13P_T2_MRCC_B J14 BANK14 IO18 IO_L13N_T2_MRCC_B J14 BANK14 IO19 IO_L14P_T2_SRCC_B P10 BANK14 IO20 IO_L14N_T2_SRCC_B P11 BANK14 IO21 IO_L15P_T2_SRCC_B J14 BANK14 IO22 IO_L15N_T2_DQS_DOUT_CSB_B J14 BANK14 IO23 IO_L16P_T2_DQS_DOUT_CSB_B J14 BANK14 IO24 IO_L16N_T2_DQS_DOUT_CSB_B J14 BANK14 IO25 IO_L17P_T2_A15_D31_R14 R10 BANK14 IO26 IO_L17N_T2_A14_D30_R14 R11 BANK14 IO27 IO_L18P_T2_A13_D29_R14 N9 BANK14 IO28 IO_L18P_T2_A12_D28_R14 P9 BANK14 IO29 IO_L18N_T2_A11_D27_R14 M6 BANK14 IO30 IO_L19P_T2_A10_D26_R14 N6 BANK14 IO31 IO_L19N_T2_A09_D25_VREF_R14 P8 BANK14 IO32 IO_L20P_T2_A08_D24_R14 R8 BANK14 IO33 IO_L21P_T2_A07_D23_R14 T7 BANK14 IO34 IO_L22P_T2_A06_D22_R14 T8 BANK14 IO35 IO_L21N_T3_DQS_A06_D22_R14 T8 BANK14 IO36 IO_L22P_T3_A04_D20_R14 T10 BANK14 IO37 IO_L22N_T3_A04_D20_R14 T10 BANK14 IO38 IO_L23P_T3_A03_D19_R14 R5 BANK14 IO39 IO_L23N_T3_A02_D18_R14 T5 BANK14 IO40 IO_L24P_T3_A01_D17_R14 R6 BANK14 IO41 IO_L24N_T3_A00_D16_R14 R7 BANK14 IO41 IO_L25_14 IO25_14</p>	<p>IO_0_14 K12 BANK14 IO1 IO_LIP_T0_D00_MOSI_DIN J13 QSPI_DQ0 IO_LIN_T0_D01_DIN J14 QSPI_DQ1 IO_L2P_T0_D02_DIN J15 QSPI_DQ2 IO_L2N_T0_D03_DIN J16 QSPI_DQ3 IO_L3P_T0_DQS_PUDC_B M15 R20_W1K IO_L3N_T0_DQS_EMGCLKL J14 BANK14 IO2 IO_L4P_T0_D04_DIN M14 BANK14 IO3 IO_L5P_T0_D05_DIN K13 BANK14 IO4 IO_L5N_T0_D07_DIN J13 BANK14 IO5 IO_L6P_T0_FCS_B J12 QSPI_CS IO_L6N_T0_D08_VREF M12 BANK14 IO6 IO_L7P_T1_D04_DIN M16 BANK14 IO7 IO_L7N_T1_D10_DIN P15 BANK14 IO8 IO_L8N_T1_D12_DIN P16 BANK14 IO9 IO_L9P_T1_D05_DOS J14 BANK14 IO10 IO_L9N_T1_D05_D13_D14 J14 BANK14 IO11 IO_L10P_T1_D14_D15 J14 BANK14 IO12 IO_L10N_T1_D15_D14 N13 BANK14 IO13 IO_L11P_T1_SRCC_B J13 BANK14 IO14 IO_L11N_T1_SRCC_B J13 BANK14 IO15 IO_L12P_T1_MRCC_B J14 BANK14 IO16 IO_L12N_T1_MRCC_B J14 BANK14 IO17 IO_L13P_T2_MRCC_B J14 BANK14 IO18 IO_L13N_T2_MRCC_B J14 BANK14 IO19 IO_L14P_T2_SRCC_B P10 BANK14 IO20 IO_L14N_T2_SRCC_B P11 BANK14 IO21 IO_L15P_T2_SRCC_B J14 BANK14 IO22 IO_L15N_T2_DQS_DOUT_CSB_B J14 BANK14 IO23 IO_L16P_T2_DQS_DOUT_CSB_B J14 BANK14 IO24 IO_L16N_T2_DQS_DOUT_CSB_B J14 BANK14 IO25 IO_L17P_T2_A15_D31_R14 R10 BANK14 IO26 IO_L17N_T2_A14_D30_R14 R11 BANK14 IO27 IO_L18P_T2_A13_D29_R14 N9 BANK14 IO28 IO_L18P_T2_A12_D28_R14 P9 BANK14 IO29 IO_L18N_T2_A11_D27_R14 M6 BANK14 IO30 IO_L19P_T2_A10_D26_R14 N6 BANK14 IO31 IO_L19N_T2_A09_D25_VREF_R14 P8 BANK14 IO32 IO_L20P_T2_A08_D24_R14 R8 BANK14 IO33 IO_L21P_T2_A07_D23_R14 T7 BANK14 IO34 IO_L22P_T2_A06_D22_R14 T8 BANK14 IO35 IO_L21N_T3_DQS_A06_D22_R14 T8 BANK14 IO36 IO_L22P_T3_A04_D20_R14 T10 BANK14 IO37 IO_L22N_T3_A04_D20_R14 T10 BANK14 IO38 IO_L23P_T3_A03_D19_R14 R5 BANK14 IO39 IO_L23N_T3_A02_D18_R14 T5 BANK14 IO40 IO_L24P_T3_A01_D17_R14 R6 BANK14 IO41 IO_L24N_T3_A00_D16_R14 R7 BANK14 IO41 IO_L25_14 IO25_14</p>	<p>IO_0_14 K12 BANK14 IO1 IO_LIP_T0_D00_MOSI_DIN J13 QSPI_DQ0 IO_LIN_T0_D01_DIN J14 QSPI_DQ1 IO_L2P_T0_D02_DIN J15 QSPI_DQ2 IO_L2N_T0_D03_DIN J16 QSPI_DQ3 IO_L3P_T0_DQS_PUDC_B M15 R20_W1K IO_L3N_T0_DQS_EMGCLKL J14 BANK14 IO2 IO_L4P_T0_D04_DIN M14 BANK14 IO3 IO_L5P_T0_D05_DIN K13 BANK14 IO4 IO_L5N_T0_D07_DIN J13 BANK14 IO5 IO_L6P_T0_FCS_B J12 QSPI_CS IO_L6N_T0_D08_VREF M12 BANK14 IO6 IO_L7P_T1_D04_DIN M16 BANK14 IO7 IO_L7N_T1_D10_DIN P15 BANK14 IO8 IO_L8N_T1_D12_DIN P16 BANK14 IO9 IO_L9P_T1_D05_DOS J14 BANK14 IO10 IO_L9N_T1_D05_D13_D14 J14 BANK14 IO11 IO_L10P_T1_D14_D15 J14 BANK14 IO12 IO_L10N_T1_D15_D14 N13 BANK14 IO13 IO_L11P_T1_SRCC_B J13 BANK14 IO14 IO_L11N_T1_SRCC_B J13 BANK14 IO15 IO_L12P_T1_MRCC_B J14 BANK14 IO16 IO_L12N_T1_MRCC_B J14 BANK14 IO17 IO_L13P_T2_MRCC_B J14 BANK14 IO18 IO_L13N_T2_MRCC_B J14 BANK14 IO19 IO_L14P_T2_SRCC_B P10 BANK14 IO20 IO_L14N_T2_SRCC_B P11 BANK14 IO21 IO_L15P_T2_SRCC_B J14 BANK14 IO22 IO_L15N_T2_DQS_DOUT_CSB_B J14 BANK14 IO23 IO_L16P_T2_DQS_DOUT_CSB_B J14 BANK14 IO24 IO_L16N_T2_DQS_DOUT_CSB_B J14 BANK14 IO25 IO_L17P_T2_A15_D31_R14 R10 BANK14 IO26 IO_L17N_T2_A14_D30_R14 R11 BANK14 IO27 IO_L18P_T2_A13_D29_R14 N9 BANK14 IO28 IO_L18P_T2_A12_D28_R14 P9 BANK14 IO29 IO_L18N_T2_A11_D27_R14 M6 BANK14 IO30 IO_L19P_T2_A10_D26_R14 N6 BANK14 IO31 IO_L19N_T2_A09_D25_VREF_R14 P8 BANK14 IO32 IO_L20P_T2_A08_D24_R14 R8 BANK14 IO33 IO_L21P_T2_A07_D23_R14 T7 BANK14 IO34 IO_L22P_T2_A06_D22_R14 T8 BANK14 IO35 IO_L21N_T3_DQS_A06_D22_R14 T8 BANK14 IO36 IO_L22P_T3_A04_D20_R14 T10 BANK14 IO37 IO_L22N_T3_A04_D20_R14 T10 BANK14 IO38 IO_L23P_T3_A03_D19_R14 R5 BANK14 IO39 IO_L23N_T3_A02_D18_R14 T5 BANK14 IO40 IO_L24P_T3_A01_D17_R14 R6 BANK14 IO41 IO_L24N_T3_A00_D16_R14 R7 BANK14 IO41 IO_L25_14 IO25_14</p>	<p>IO_0_14 K12 BANK14 IO1 IO_LIP_T0_D00_MOSI_DIN J13 QSPI_DQ0 IO_LIN_T0_D01_DIN J14 QSPI_DQ1 IO_L2P_T0_D02_DIN J15 QSPI_DQ2 IO_L2N_T0_D03_DIN J16 QSPI_DQ3 IO_L3P_T0_DQS_PUDC_B M15 R20_W1K IO_L3N_T0_DQS_EMGCLKL J14 BANK14 IO2 IO_L4P_T0_D04_DIN M14 BANK14 IO3 IO_L5P_T0_D05_DIN K13 BANK14 IO4 IO_L5N_T0_D07_DIN J13 BANK14 IO5 IO_L6P_T0_FCS_B J12 QSPI_CS IO_L6N_T0_D08_VREF M12 BANK14 IO6 IO_L7P_T1_D04_DIN M16 BANK14 IO7 IO_L7N_T1_D10_DIN P15 BANK14 IO8 IO_L8N_T1_D12_DIN P16 BANK14 IO9 IO_L9P_T1_D05_DOS J14 BANK14 IO10 IO_L9N_T1_D05_D13_D14 J14 BANK14 IO11 IO_L10P_T1_D14_D15 J14 BANK14 IO12 IO_L10N_T1_D15_D14 N13 BANK14 IO13 IO_L11P_T1_SRCC_B J13 BANK14 IO14 IO_L11N_T1_SRCC_B J13 BANK14 IO15 IO_L12P_T1_MRCC_B J14 BANK14 IO16 IO_L12N_T1_MRCC_B J14 BANK14 IO17 IO_L13P_T2_MRCC_B J14 BANK14 IO18 IO_L13N_T2_MRCC_B J14 BANK14 IO19 IO_L14P_T2_SRCC_B P10 BANK14 IO20 IO_L14N_T2_SRCC_B P11 BANK14 IO21 IO_L15P_T2_SRCC_B J14 BANK14 IO22 IO_L15N_T2_DQS_DOUT_CSB_B J14 BANK14 IO23 IO_L16P_T2_DQS_DOUT_CSB_B J14 BANK14 IO24 IO_L16N_T2_DQS_DOUT_CSB_B J14 BANK14 IO25 IO_L17P_T2_A15_D31_R14 R10 BANK14 IO26 IO_L17N_T2_A14_D30_R14 R11 BANK14 IO27 IO_L18P_T2_A13_D29_R14 N9 BANK14 IO28 IO_L18P_T2_A12_D28_R14 P9 BANK14 IO29 IO_L18N_T2_A11_D27_R14 M6 BANK14 IO30 IO_L19P_T2_A10_D26_R14 N6 BANK14 IO31 IO_L19N_T2_A09_D25_VREF_R14 P8 BANK14 IO32 IO_L20P_T2_A08_D24_R14 R8 BANK14 IO33 IO_L21P_T2_A07_D23_R14 T7 BANK14 IO34 IO_L22P_T2_A06_D22_R14 T8 BANK14 IO35 IO_L21N_T3_DQS_A06_D22_R14 T8 BANK14 IO36 IO_L22P_T3_A04_D20_R14 T10 BANK14 IO37 IO_L22N_T3_A04_D20_R14 T10 BANK14 IO38 IO_L23P_T3_A03_D19_R14 R5 BANK14 IO39 IO_L23N_T3_A02_D18_R14 T5 BANK14 IO40 IO_L24P_T3_A01_D17_R14 R6 BANK14 IO41 IO_L24N_T3_A00_D16_R14 R7 BANK14 IO41 IO_L25_14 IO25_14</p>	<p>IO_0_14 K12 BANK14 IO1 IO_LIP_T0_D00_MOSI_DIN J13 QSPI_DQ0 IO_LIN_T0_D01_DIN J14 QSPI_DQ1 IO_L2P_T0_D02_DIN J15 QSPI_DQ2 IO_L2N_T0_D03_DIN J16 QSPI_DQ3 IO_L3P_T0_DQS_PUDC_B M15 R20_W1K IO_L3N_T0_DQS_EMGCLKL J14 BANK14 IO2 IO_L4P_T0_D04_DIN M14 BANK14 IO3 IO_L5P_T0_D05_DIN K13 BANK14 IO4 IO_L5N_T0_D07_DIN J13 BANK14 IO5 IO_L6P_T0_FCS_B J12 QSPI_CS IO_L6N_T0_D08_VREF M12 BANK14 IO6 IO_L7P_T1_D04_DIN M16 BANK14 IO7 IO_L7N_T1_D10_DIN P15 BANK14 IO8 IO_L8N_T1_D12_DIN P16 BANK14 IO9 IO_L9P_T1_D05_DOS J14 BANK14 IO10 IO_L9N_T1_D05_D13_D14 J14 BANK14 IO11 IO_L10P_T1_D14_D15 J14 BANK14 IO12 IO_L10N_T1_D15_D14 N13 BANK14 IO13 IO_L11P_T1_SRCC_B J13 BANK14 IO14 IO_L11N_T1_SRCC_B J13 BANK14 IO15 IO_L12P_T1_MRCC_B J14 BANK14 IO16 IO_L12N_T1_MRCC_B J14 BANK14 IO17 IO_L13P_T2_MRCC_B J14 BANK14 IO18 IO_L13N_T2_MRCC_B J14 BANK14 IO19 IO_L14P_T2_SRCC_B P10 BANK14 IO20 IO_L14N_T2_SRCC_B P11 BANK14 IO21 IO_L15P_T2_SRCC_B J14 BANK14 IO22 IO_L15N_T2_DQS_DOUT_CSB_B J14 BANK14 IO23 IO_L16P_T2_DQS_DOUT_CSB_B J14 BANK14 IO24 IO_L16N_T2_DQS_DOUT_CSB_B J14 BANK14 IO25 IO_L17P_T2_A15_D31_R14 R10 BANK14 IO26 IO_L17N_T2_A14_D30_R14 R11 BANK14 IO27 IO_L18P_T2_A13_D29_R14 N9 BANK14 IO28 IO_L18P_T2_A12_D28_R14 P9 BANK14 IO29 IO_L18N_T2_A11_D27_R14 M6 BANK14 IO30 IO_L19P_T2_A10_D26_R14 N6 BANK14 IO31 IO_L19N_T2_A09_D25_VREF_R14 P8 BANK14 IO32 IO_L20P_T2_A08_D24_R14 R8 BANK14 IO33 IO_L21P_T2_A07_D23_R14 T7 BANK14 IO34 IO_L22P_T2_A06_D22_R14 T8 BANK14 IO35 IO_L21N_T3_DQS_A06_D22_R14 T8 BANK14 IO36 IO_L22P_T3_A04_D20_R14 T10 BANK14 IO37 IO_L22N_T3_A04_D20_R14 T10 BANK14 IO38 IO_L23P_T3_A03_D19_R14 R5 BANK14 IO39 IO_L23N_T3_A02_D18_R14 T5 BANK14 IO40 IO_L24P_T3_A01_D17_R14 R6 BANK14 IO41 IO_L24N_T3_A00_D16_R14 R7 BANK14 IO41 IO_L25_14 IO25_14</p>	<p>IO_0_14 K12 BANK14 IO1 IO_LIP_T0_D00_MOSI_DIN J13 QSPI_DQ0 IO_LIN_T0_D01_DIN J14 QSPI_DQ1 IO_L2P_T0_D02_DIN J15 QSPI_DQ2 IO_L2N_T0_D03_DIN J16 QSPI_DQ3 IO_L3P_T0_DQS_PUDC_B M15 R20_W1K IO_L3N_T0_DQS_EMGCLKL J14 BANK14 IO2 IO_L4P_T0_D04_DIN M14 BANK14 IO3 IO_L5P_T0_D05_DIN K13 BANK14 IO4 IO_L5N_T0_D07_DIN J13 BANK14 IO5 IO_L6P_T0_FCS_B J12 QSPI_CS IO_L6N_T0_D08_VREF M12 BANK14 IO6 IO_L7P_T1_D04_DIN M16 BANK14 IO7 IO_L7N_T1_D10_DIN P15 BANK14 IO8 IO_L8N_T1_D12_DIN P16 BANK14 IO9 IO_L9P_T1_D05_DOS J14 BANK14 IO10 IO_L9N_T1_D05_D13_D14 J14 BANK14 IO11 IO_L10P_T1_D14_D15 J14 BANK14 IO12 IO_L10N_T1_D15_D14 N13 BANK14 IO13 IO_L11P_T1_SRCC_B J13 BANK14 IO14 IO_L11N_T1_SRCC_B J13 BANK14 IO15 IO_L12P_T1_MRCC_B J14 BANK14 IO16 IO_L12N_T1_MRCC_B J14 BANK14 IO17 IO_L13P_T2_MRCC_B J14 BANK14 IO18 IO_L13N_T2_MRCC_B J14 BANK14 IO19 IO_L14P_T2_SRCC_B P10 BANK14 IO20 IO_L14N_T2_SRCC_B P11 BANK14 IO21 IO_L15P_T2_SRCC_B J14 BANK14 IO22 IO_L15N_T2_DQS_DOUT_CSB_B J14 BANK14 IO23 IO_L16P_T2_DQS_DOUT_CSB_B J14 BANK14 IO24 IO_L16N_T2_DQS_DOUT_CSB_B J14 BANK14 IO25 IO_L17P_T2_A15_D31_R14 R10 BANK14 IO26 IO_L17N_T2_A14_D30_R14 R11 BANK14 IO27 IO_L18P_T2_A13_D29_R14 N9 BANK14 IO28 IO_L18P_T2_A12_D28_R14 P9 BANK14 IO29 IO_L18N_T2_A11_D27_R14 M6 BANK14 IO30 IO_L19P_T2_A10_D26_R14 N6 BANK14 IO31 IO_L19N_T2_A09_D25_VREF_R14 P8 BANK14 IO32 IO_L20P_T2_A08_D24_R14 R8 BANK14 IO33 IO_L21P_T2_A07_D23_R14 T7 BANK14 IO34 IO_L22P_T2_A06_D22_R14 T8 BANK14 IO35 IO_L21N_T3_DQS_A06_D22_R14 T8 BANK14 IO36 IO_L22P_T3_A04_D20_R14 T10 BANK14 IO37 IO_L22N_T3_A04_D20_R14 T10 BANK14 IO38 IO_L23P_T3_A03_D19_R14 R5 BANK14 IO39 IO_L23N_T3_A02_D18_R14 T5 BANK14 IO40 IO_L24P_T3_A01_D17_R14 R6 BANK14 IO41 IO_L24N_T3_A00_D16_R14 R7 BANK14 IO41 IO_L25_14 IO25_14</p>
D	<p>This diagram shows the connection of the N25Q032A SPI device (U2) and its associated components:</p> <ul style="list-style-type: none"> Power: 3V3 is connected to VCC and GND. Control: CS, SDO/DQ1/HOLD/DQ0, /WPD/QD2, SCK, SDI/DQ0, VSS, and GND are connected to U2. Feedback: R18 (1.8kΩ) connects to the CS pin and ground. OSC: The oscillator circuit consists of R21 (1.8kΩ), C1 (100nF), and C2 (100nF). The output is connected to the SCK pin of U2. Timing: R22 (100Ω) connects to the SDO/DQ1 pin and ground. Output: QSPI_SCK is connected to the SDI/DQ0 pin of U2. Ground: GND is connected to the GND pins of U2 and the oscillator circuit. 	<p>Y1 OSC GND 2 1 GND 3 4 VDD NC 100MHZ</p> <p>Y2 OSC GND 2 1 GND 3 4 VDD NC XMHZ</p>	<p>PL_SW1 PL_SW2 XC7A35T-1FTG256</p>	<p>Title Size A3 Number Revision Date: 2018/6/17 Sheet of File: G:\通用平台\PCB\FP0G3A\FPGA IO_14\14.htm By:</p>		

A



VCCINT 1.0V 95mA+120mA
VCCO 3.3V 1mA+40mA * 4
VCCAUX 1.8V 22mA+40mA
VCCBRAM 1.0V 2mA + 60mA

Title		
Size	Number	Revision
A4		
Date: 2018/6/17		Sheet of
File: G:\通用平台\PCB\FP0G3A\FPGA POWD\schDoc		

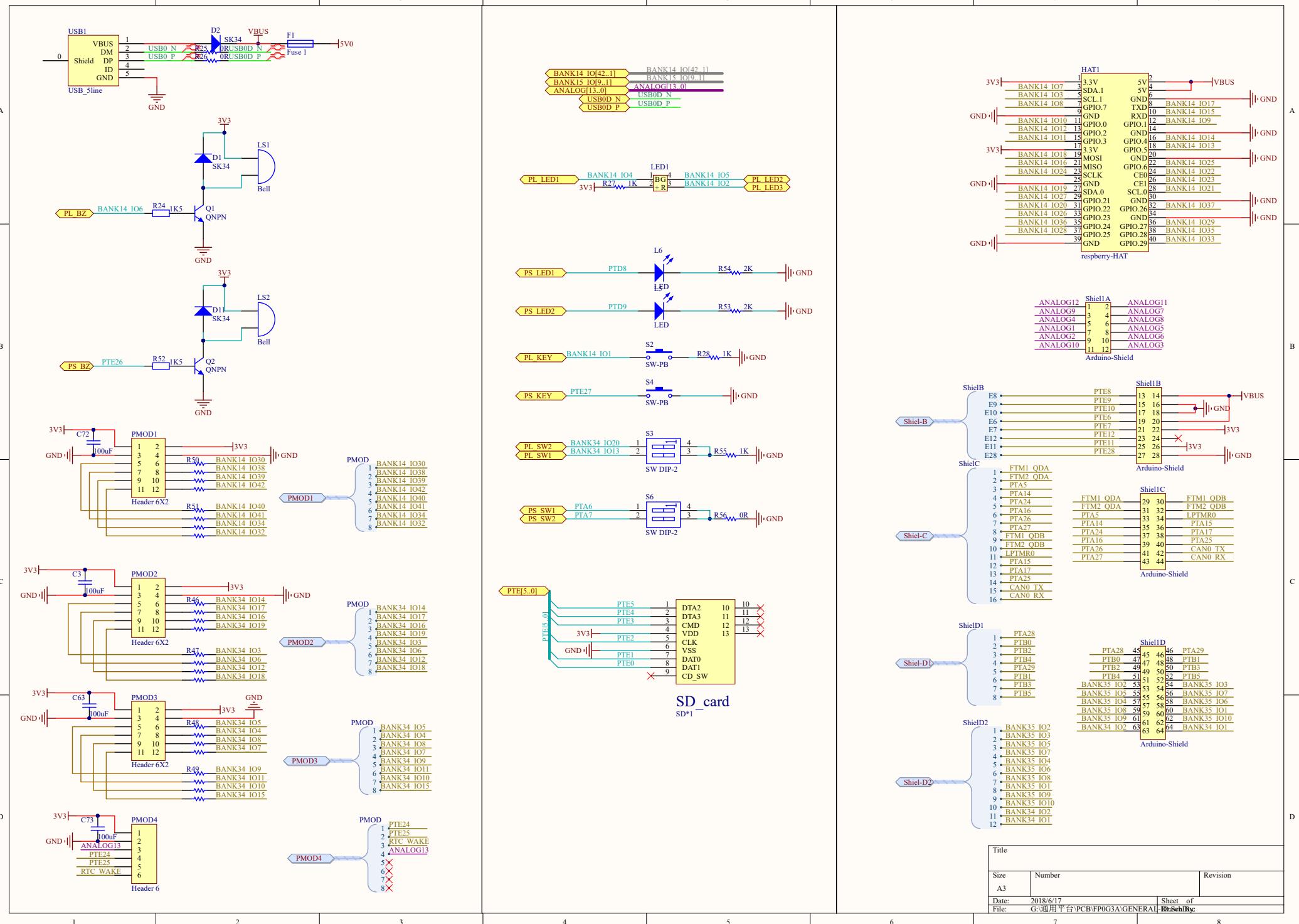
D

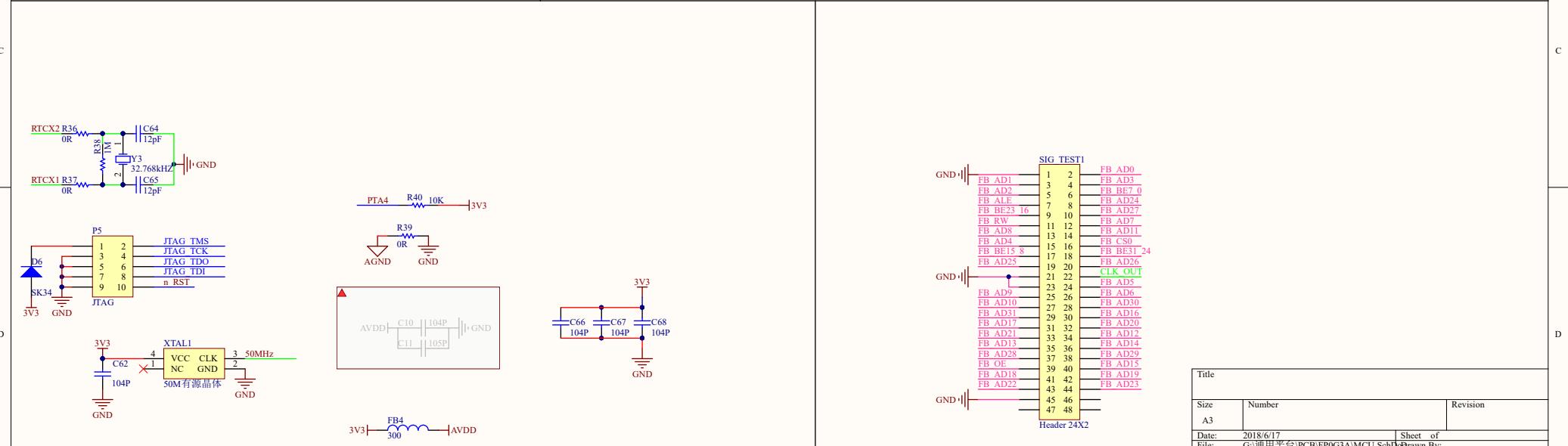
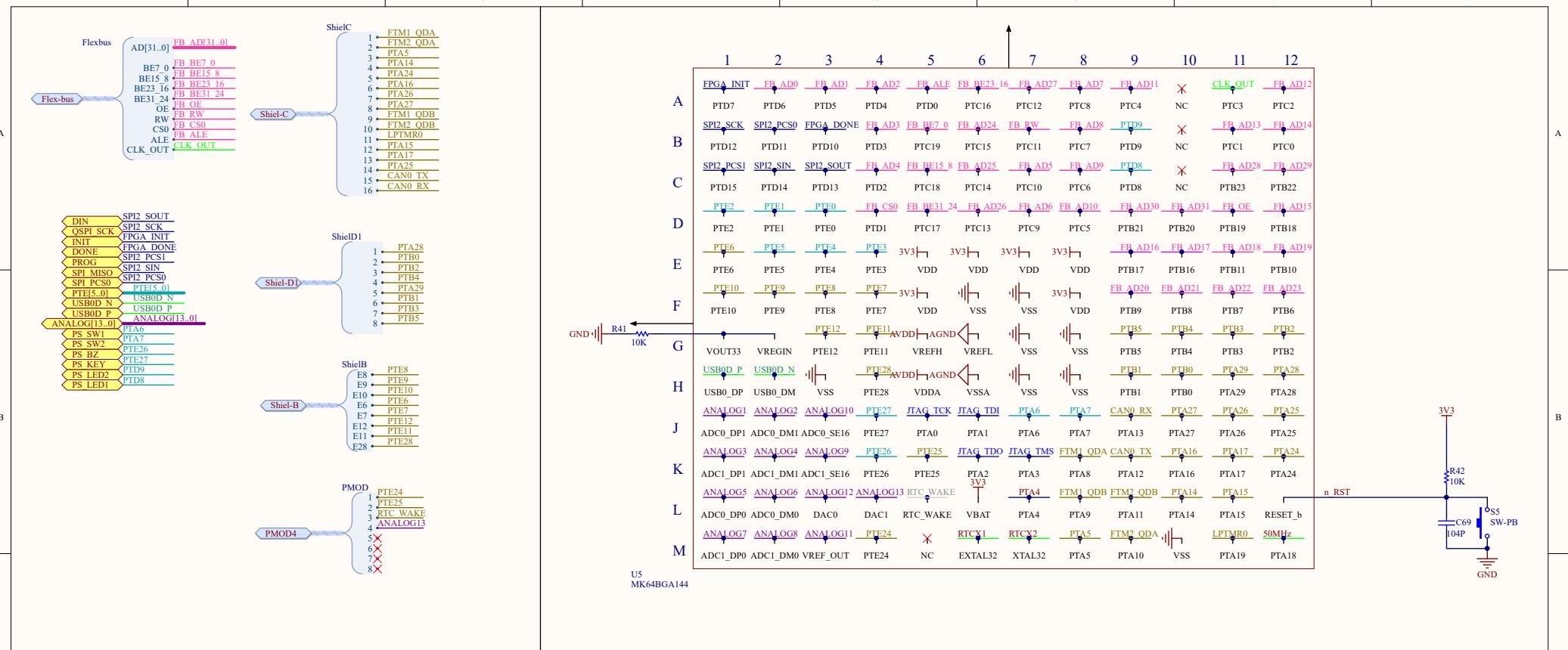
A

B

C

D





A

A

B

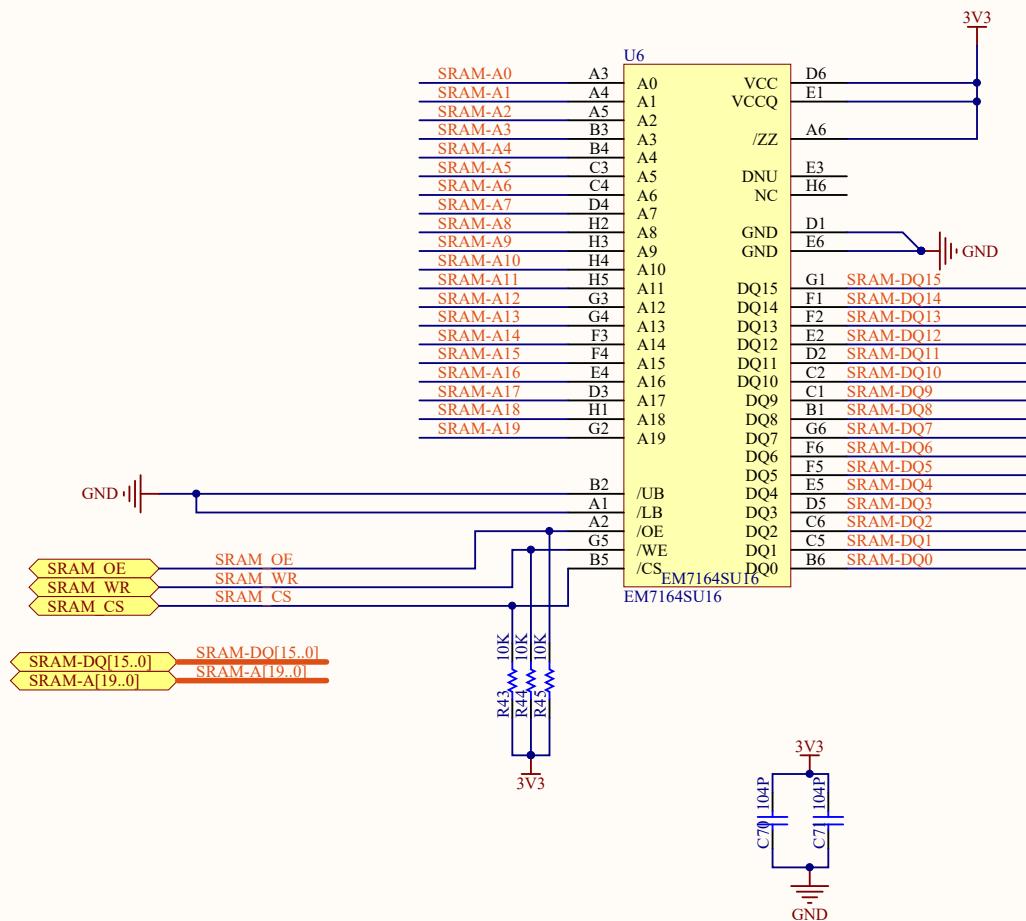
B

C

C

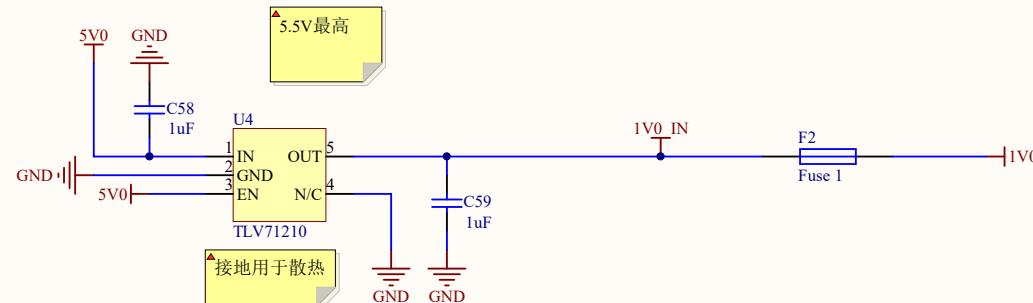
3

3

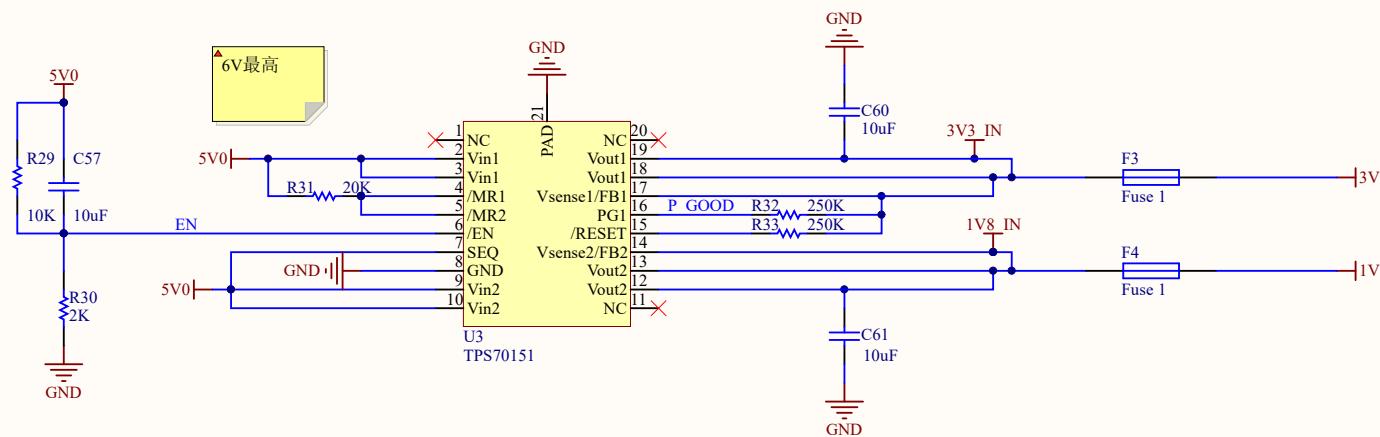


Title	
Size A4	Number
Date:	2018/6/17
File:	G:\通用平台\PCB\FP0G3A\MEMORYS
	Sheet E1 of 20 By: 王道

A



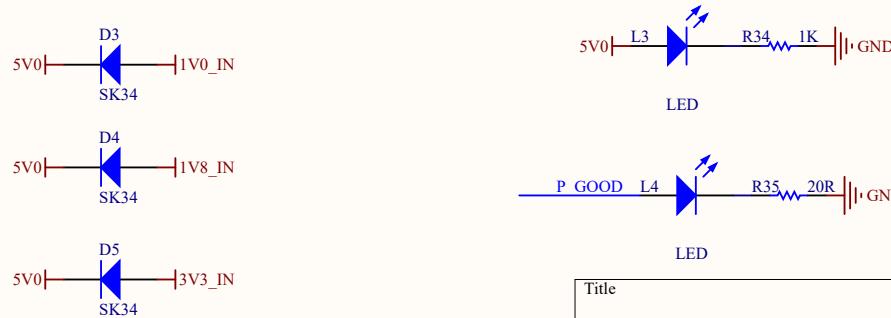
B



C

上电时序:
1 1V0
2 1V8
3 3V3

电源定义:
VBUS 4.76.5V 单向
3V3
1V8
1V0



Title		
Size	Number	Revision
A4		
Date: 2018/6/17	Sheet of	
File: G:\通用平台\PCB\FP0G3A\POWER.Sch	Drawn By:	

人民滿意，世人仰慕！

追求卓越
DDK

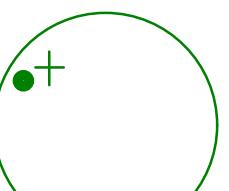
BANK34

5V	5V	PL-N14	PL-N13	PL-P15	PL-T15	PL-T14	GND	PL-T13	PL-R12	PL-T12	PL-P11	GND	PL-T10	PL-P9	PL-T8	PL-R8
3V3		GND	GND		PL-P16	GND	PL-P14	PL-P13	PL-R13	PL-N12	PL-R11	GND	PL-P10	PL-T9	PL-N9	GND
		PL-M16	PL-M14	PL-N16	PL-R16	PL-R15	3V3									

May the Force be with You !!!



PROG



PS-E27



PS-RST



PS-A6



PS-A7



PS-E24



PS-E25



RTC



PS-E0

PS-E1

PS-E2

PS-E3

PS-E4

PS-E5

PS-GND

PS-3V3

PS-5V

PS-PL-M16

PS-PL-M14

PS-PL-N16

PS-PL-P16

PS-PL-R16

PS-PL-R15

PS-PL-T15

PS-PL-T14

PS-PL-T13

PS-PL-R13

PS-PL-P14

PS-PL-P13

PS-PL-R12

PS-PL-T12

PS-PL-P11

PS-PL-T10

PS-PL-R11

PS-PL-P10

PS-PL-T9

PS-PL-N9

PS-PL-P8

PS-PL-T8

PS-PL-R8

PS-PL-P6

PS-PL-T5

PS-PL-R5

PS-PL-R7

PS-PL-M6

PS-PL-R6

PS-PL-T3

PS-PL-T4

PS-PL-R3

PS-PL-M5

PS-PL-T2

PS-PL-N3

PS-PL-R2

PS-PL-M4

PS-PL-N2

PS-PL-P1

PS-PL-N1

PS-PL-P4

PS-PL-M2

PS-PL-P3

PS-PL-M1

PS-PL-P1

10R

250K

25K

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

C

F

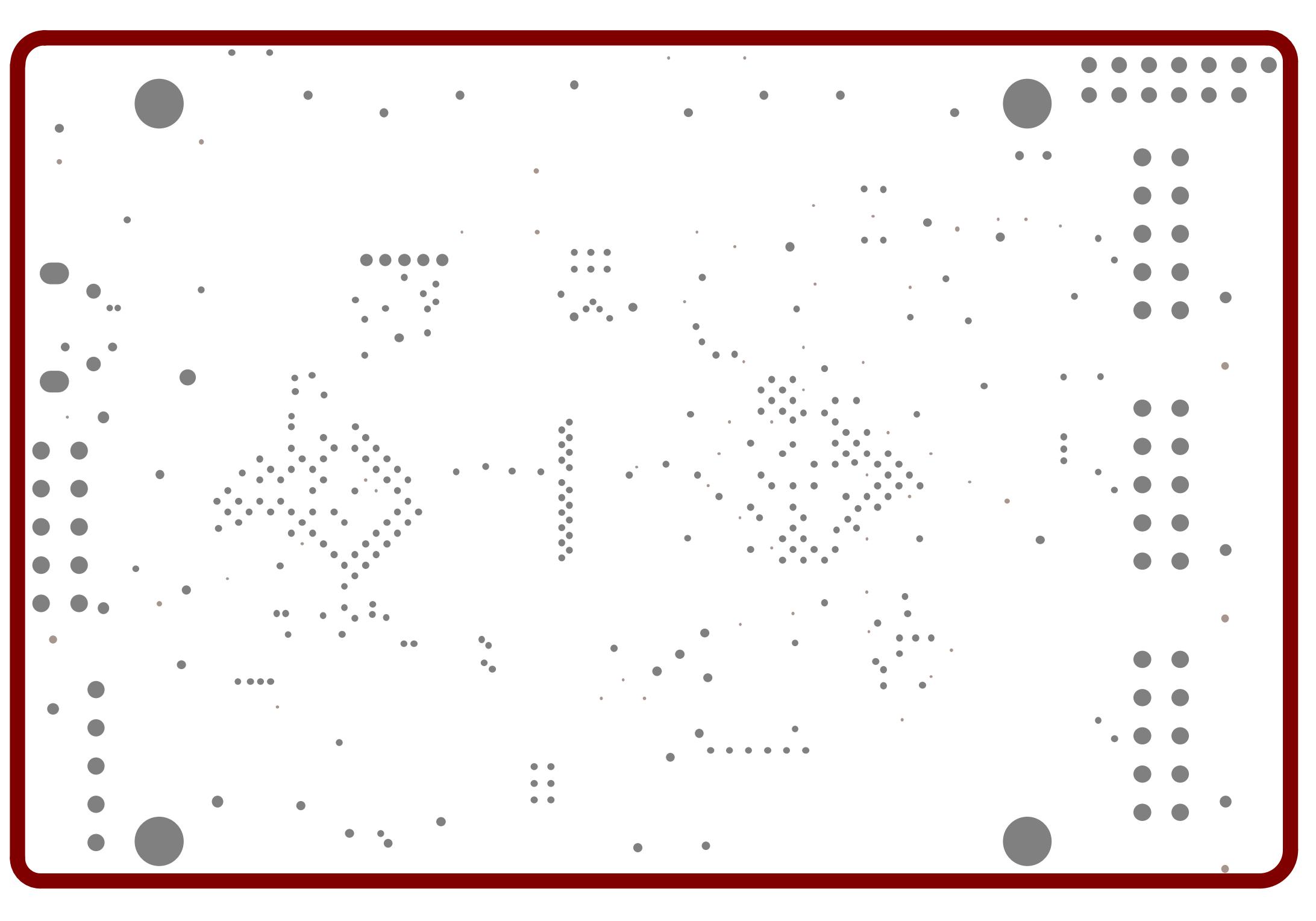
C

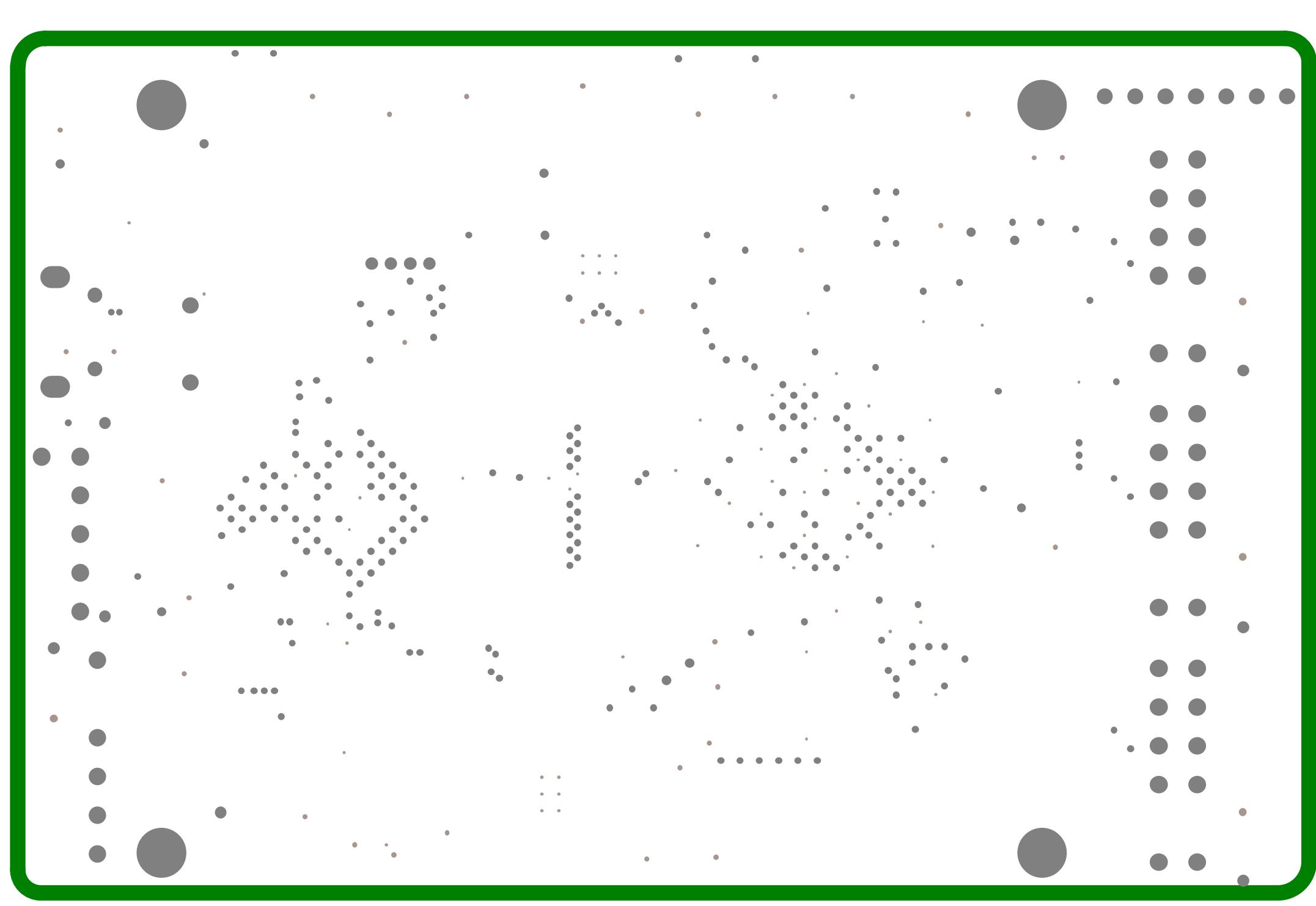
F

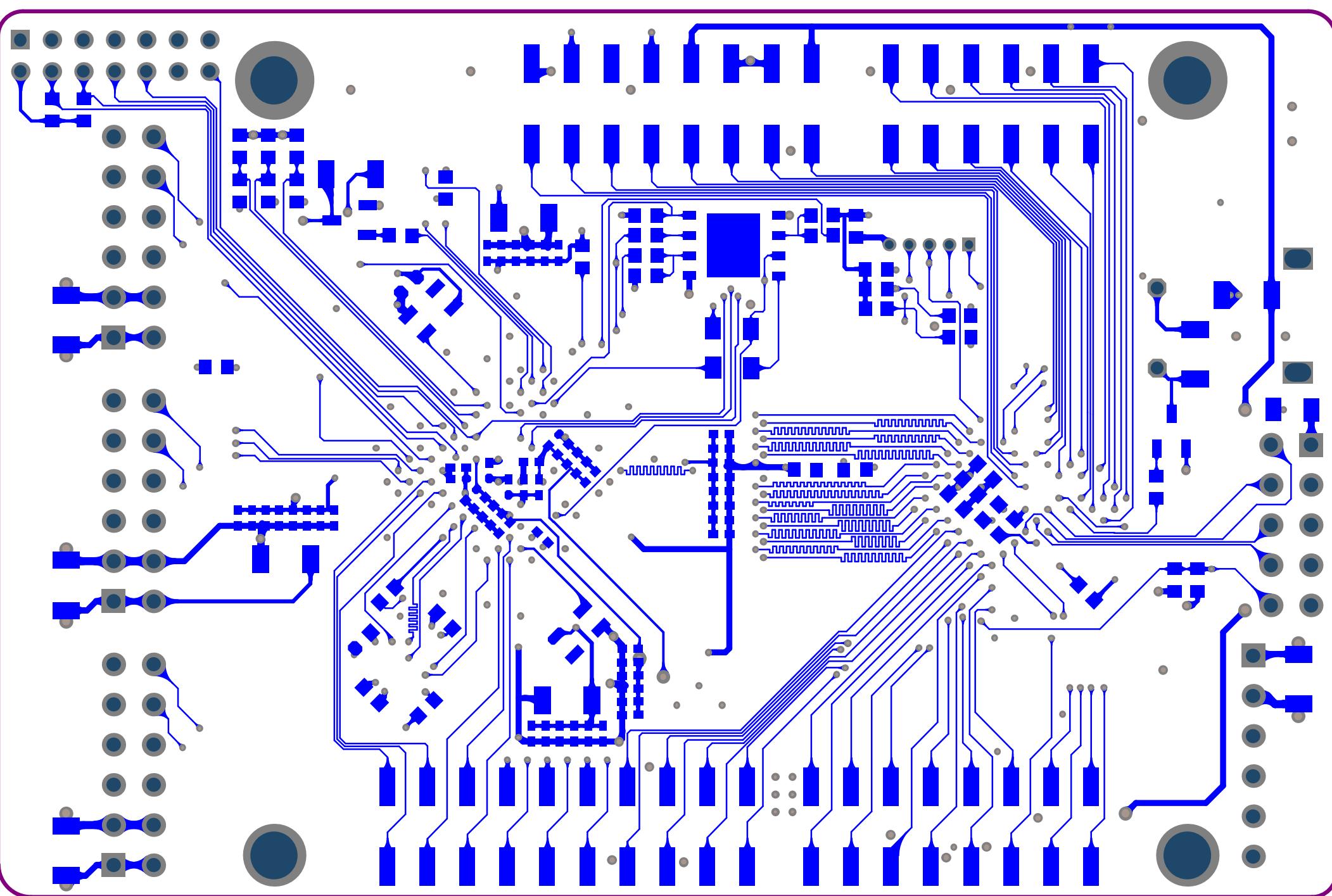
C

F

C

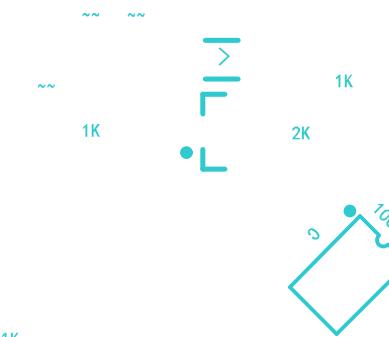






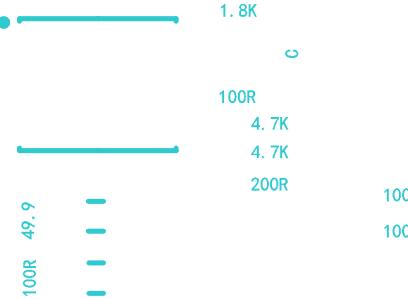
10k

C



GND	3V3	3V3	5V	GND	GND	5V	
PS-E28	PS-E11	PS-E12	PS-E7	PS-E6	PS-E10	PS-E9	PS-E8

ADC						
ADC	ADC	ADC	ADC	ADC	ADC	DAC



C

2K
2K

0R FB

2K

C 10K

C

C

C
C

10K
10K

C
C
10K
C

A/T

C
C

PL-L5	PL-K5	PL-E6	PL-C6	PL-D6	PL-A7	PS-B5	PS-B3	PS-B1	PS-A29
PL-L4	PL-F5	PL-D5	PL-B6	PL-C7	PL-B7	PS-B4	PS-B2	PS-B0	PS-A28

PS-A13	PS-A12	PS-A25	PS-A17	PS-A15	PS-A19	PS-A11	PS-A9
PS-A16							
PS-A27	PS-A26						
		PS-A24	PS-A14	PS-A5	PS-A10	PS-A8	