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FREEDOM K64F

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	Revisions & Chang	je Log		$\ \ $
v	Description	Date	Approved	
L	Initial Draft	Oct 18	Chung	
2	Feedbacks Implemented	Oct 22	Chung	$\ \ $
3	Feedbacks from Chung Implemented	Oct 23	Chung	$\ \ $
1	Components changed as per the feedbacks	Oct 24	Chung	$\ \ $
5	RF and Bluetooth headers added	Oct 28	Chung	D
5	Arduino connections added	Nov 1	Chung]
7	Net review and fix	Nov 3	Chung	
	Release to production, prototype build	Nov 8	Chung	
L	openSDA reset capacitor fix	Nov 22	del Rey	
	Release to production, production build	Jan 20	Chung	$\ \ $
	Release to production, production build	Feb 06	Chung	Н
	Fixing I2C swap to headers	Feb 26	Chung	$\ $
L	Enhancing USB PWR input filter for robustness	Mar 18	Chung	$\ \ $
	Swapping J2.2 connection to PTC12 Fix Eth Link status when 2 FRDMs are connecter to each other	Jul 11	Chung	
L	Updating J13 Ethernet connector part	Sep 03	Chung	c



1. Unless Otherwise Specified:

All resistors are in ohms, most are 1%, 1/10 Watt. Otherwise are 5%, 1/8 Watt.

All capacitors are in uF, some are 10% or 20%

All voltages are DC

All polarized capacitors are tantalum

- 2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
- 3. Device type number is for reference only. The number varies with the manufacturer.
- 4. Special signal usage:
 - _B Denotes Active-Low Signal
 - <> or [] Denotes Vectored Signals
- 5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.





















