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20. ALL PLATED AND NON-PLATED THROUGH HOLES ARE TO BE DRILLED AT PRIMARY DRILL STEP. ALL HOLE LOCATION TOLERANCES ARE TO BE $\pm .002$ IN REFERENCE TO THE PRIMARY DATUM.
21. FINISHED PCB MUST BE PANELIZED FOR ASSEMBLY ACCORDING TO CONTRACT MANUFACTURING REQUIREMENT. ADDITION OF .25" RAILS AND .125" NON-PLATED TOOLING HOLES ARE AT THE DISCRETION OF CONTRACT MANUFACTURER AND FAB HOUSE.

[illegible]

BELL COUNTY TOP 10 BOTTOM					
ALL UNITS ARE IN DOLLARS					
PIEDMONT	1519	10.7	0.0-15.75	PLATED	11
●	23.82	10.7	0.0-23.82	PLATED	6
●	29.0	10.7	0.0-29.0	PLATED	6
●	82.0	10.7	0.0-82.0	PLATED	14
●	40.0	10.7	0.0-40.0	PLATED	6
●	47.0	10.7	0.0-47.0	PLATED	11
●	79.0	10.7	0.0-79.0	PLATED	6
●	128.0	10.7	0.0-128.0	PLATED	6
●	96.0	10.7	0.0-96.0	NON-PLATED	6
●	83.0	10.7	0.0-83.0	NON-PLATED	6
●	87.0	10.7	0.0-87.0	PLATED	6

PART NO. 170-26006 THIS DOCUMENT CONTAINS CONFIDENTIAL INFORMATION PERTAINING TO FREESCALE AND SHALL NOT BE USED FOR ENGINEERING DESIGN OR DOCUMENTATION OR REPRODUCTION IN WHOLE OR IN PART WITHOUT THE CONSENT OF FREESCALE.		FREESCALE SEMICONDUCTOR	
6501 WILLIAM CANNON DRIVE WEST AUSTIN, TEXAS 78735 U.S.A.		TITLE PRINTED WIRING BOARD TWR-ELEV PRIMARY	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES (MM) TOLERANCES ARE: DECIMALS .010 .XX .XX FRACTIONS 1/32"		APPROVALS DRAWN PONTJAY MCWILLY TONG CHECKED MARILYN HUBER DESIGN ENGINEER DATE 05-19-10 BY DAVID KRUCKER	
ALL UNFINISHED SURFACES SHALL BE SHARP EDGES AND CORNERS. UNLESS NOTED, DIMS. NOT TO SCALE. THREE ANGLE ORTHOGRAPHIC PROJECTION IS USED.		SIZE D SCALE	CAD FILE NAME Dwg. No. DAY-26006 FAB-26006
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