

Title		
Size	Number	Revision
A4		
Date:	2018/6/22	Sheet of
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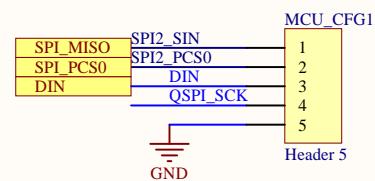
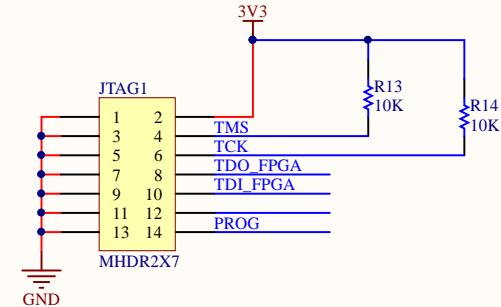
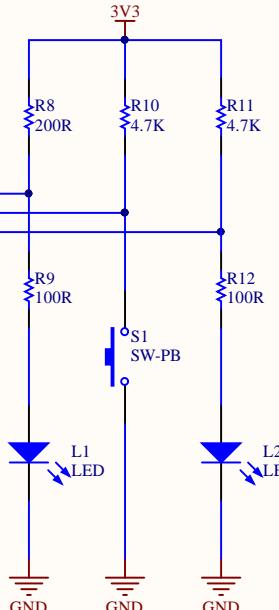
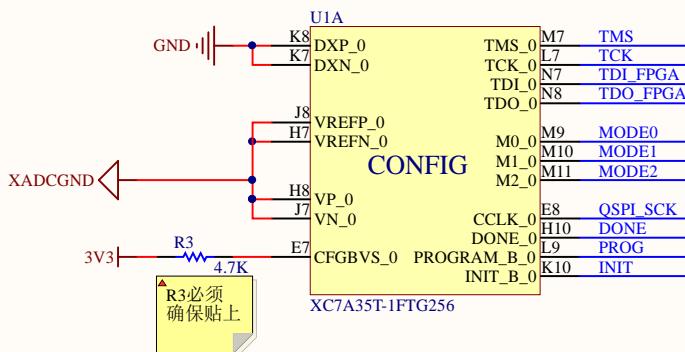
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2

3

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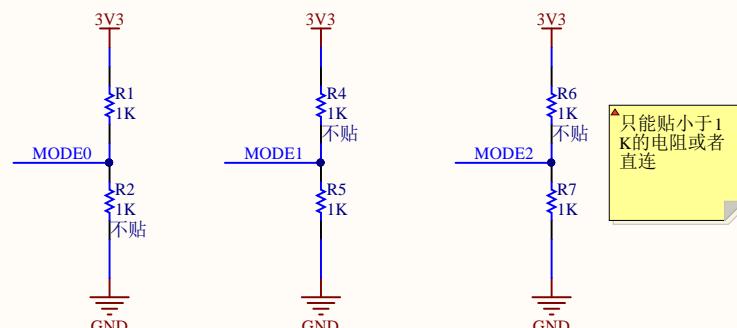
A



B



C



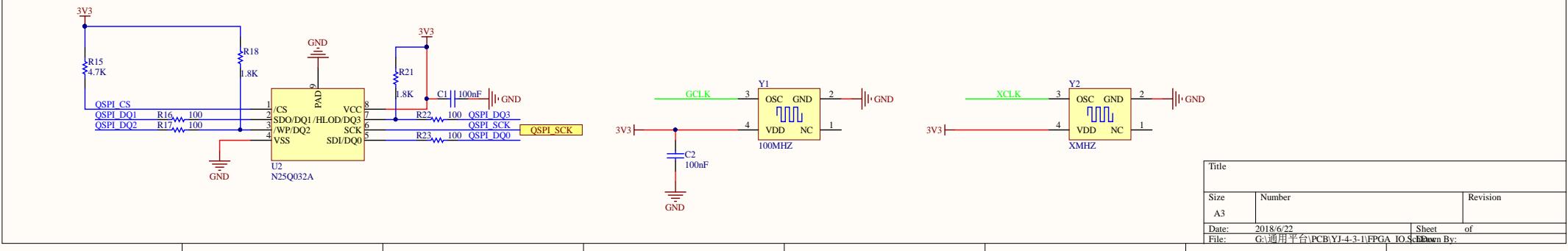
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Date:	2018/6/22	Sheet of
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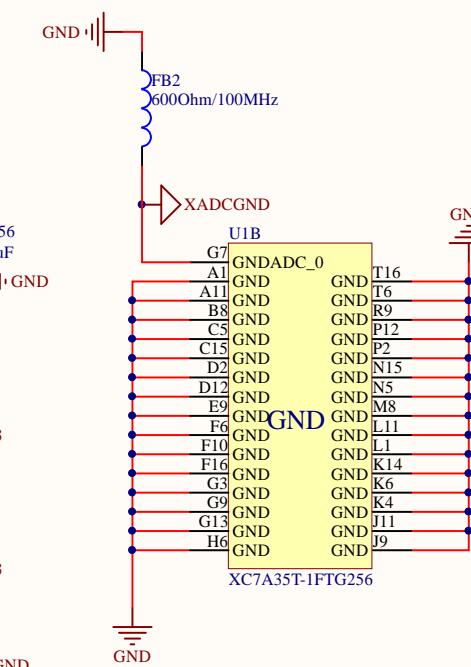
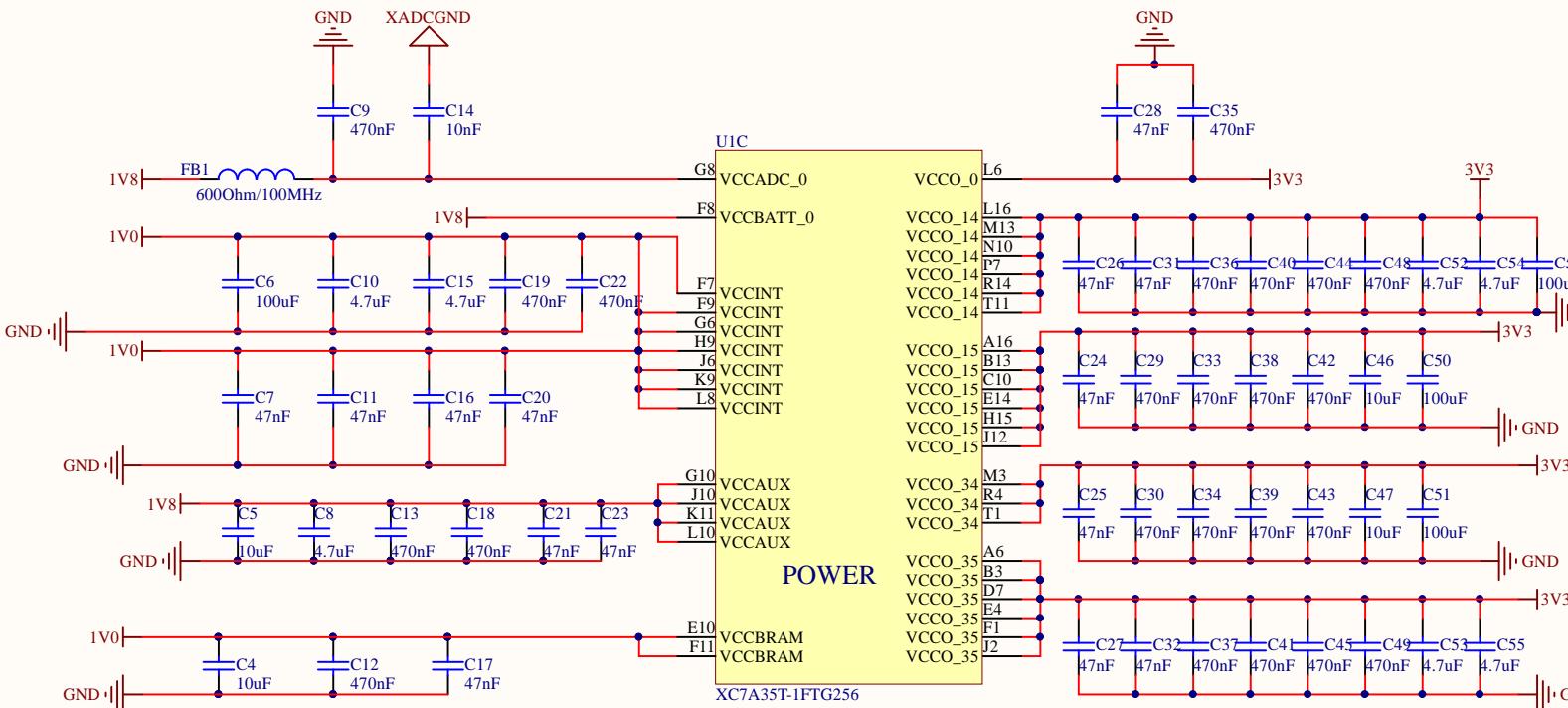
2

3

4



A



VCCINT 1.0V 95mA+120mA
 VCCO 3.3V 1mA+40mA * 4
 VCCAUX 1.8V 22mA+40mA
 VCCBRAM 1.0V 2mA + 60mA

Title

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Number

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 File: G:\通用平台\PCB\YJ-4-3-1\FPGA_POWD&RefBpc

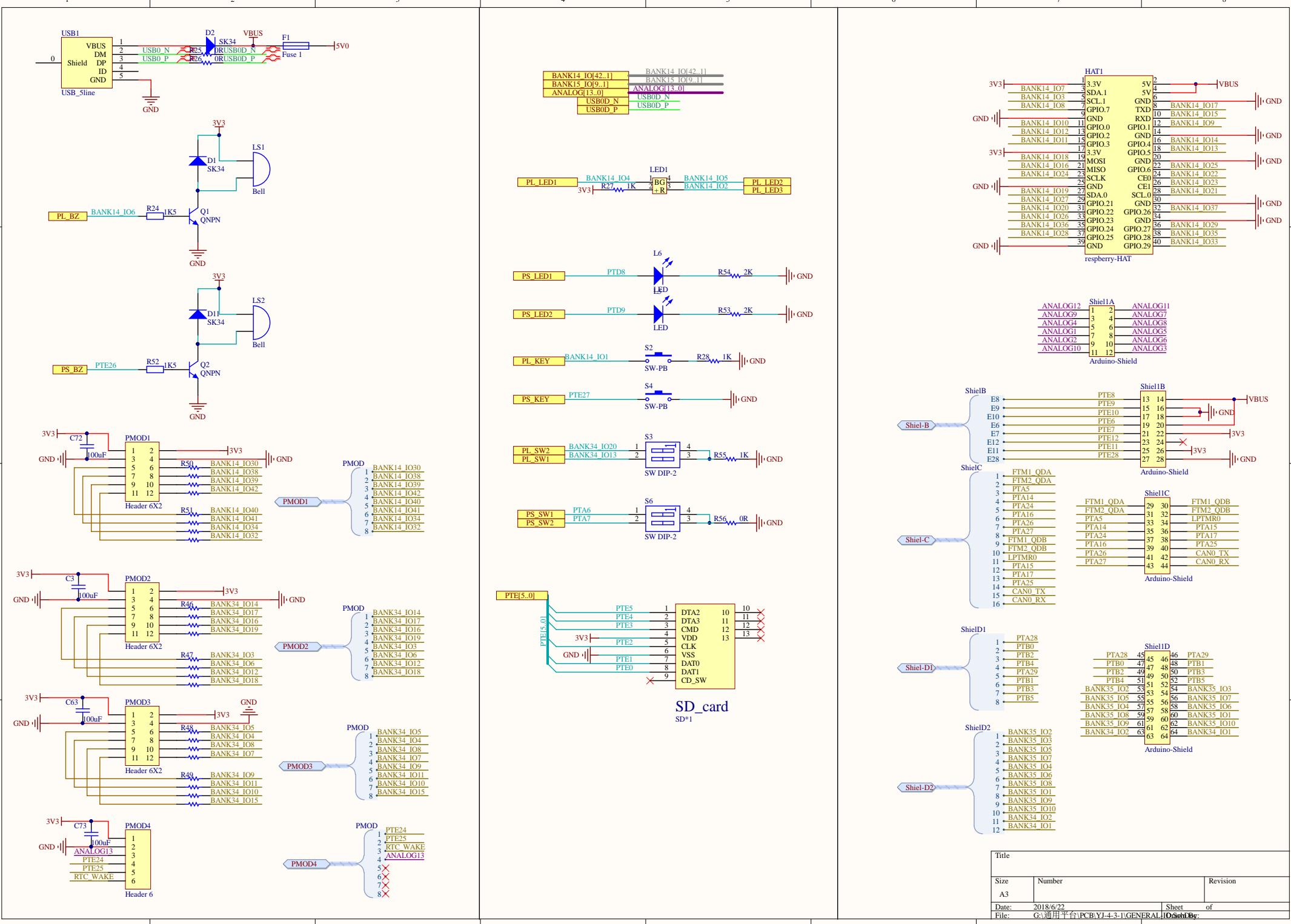
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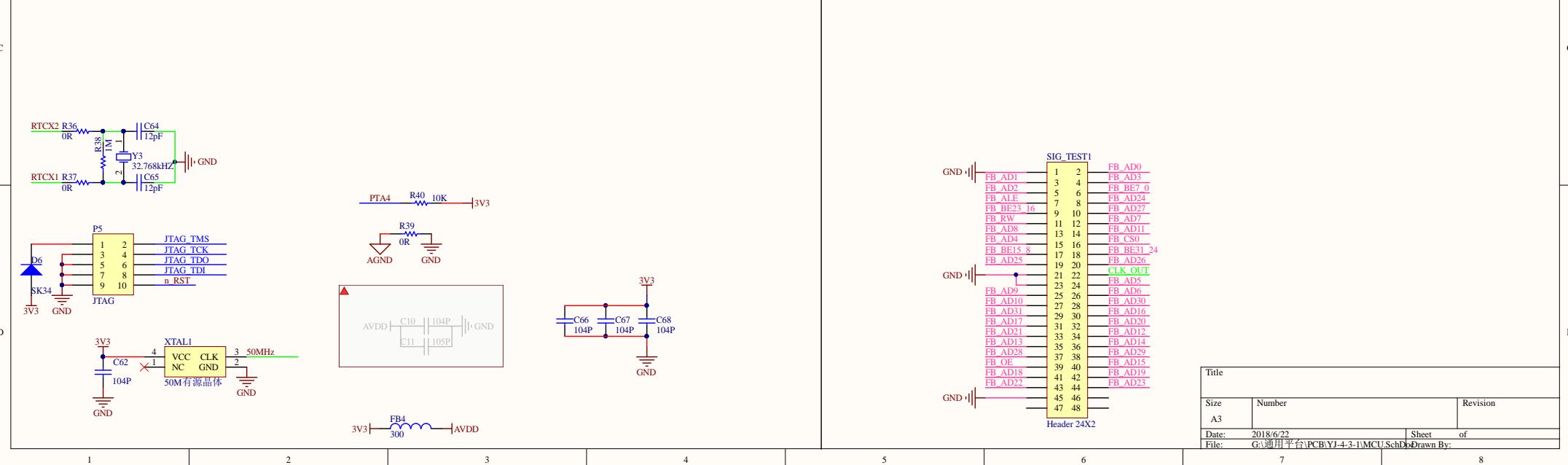
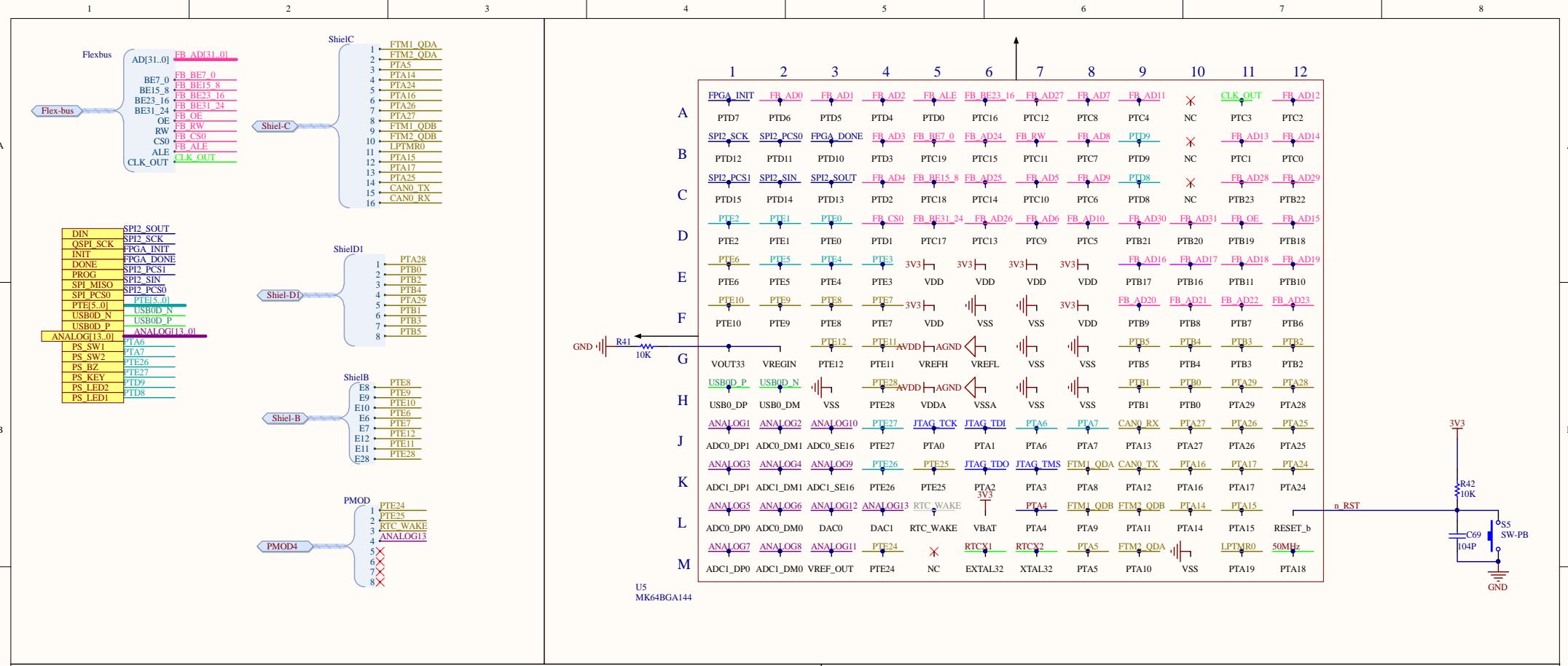
A

B

C

D





A

A

B

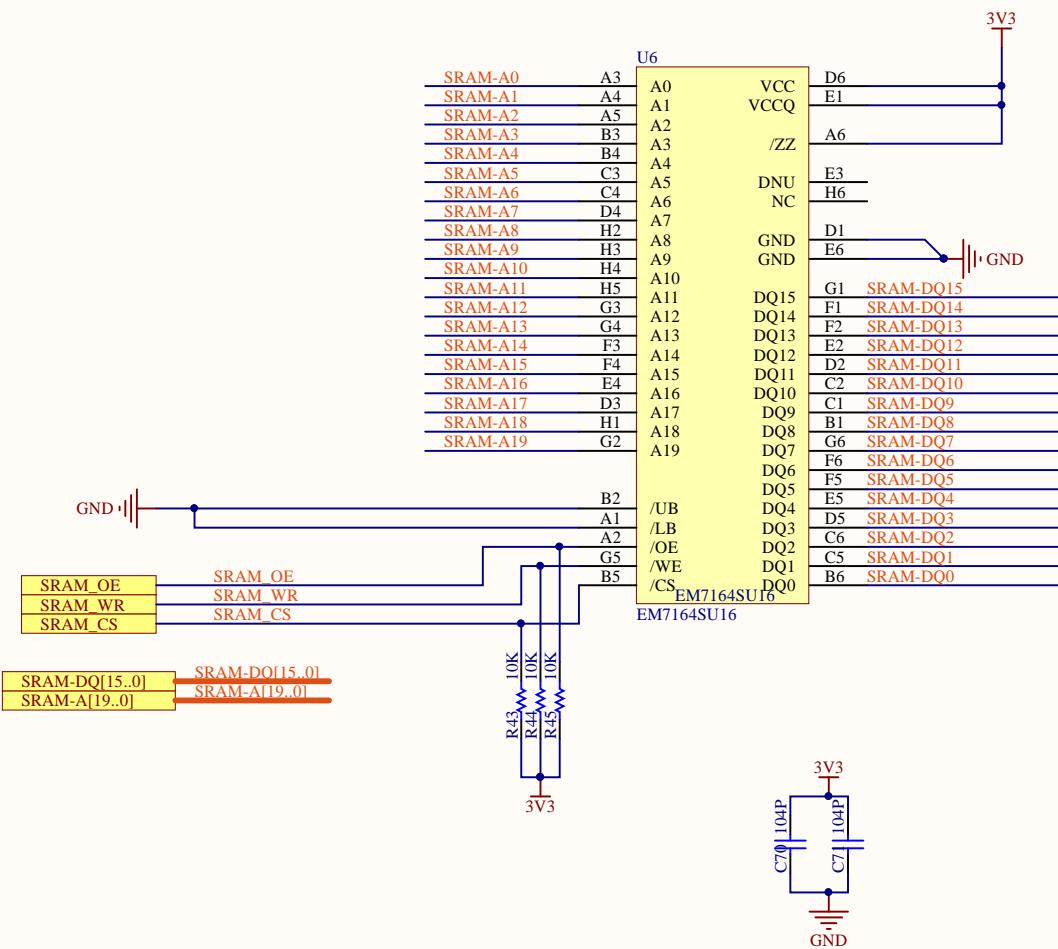
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C

C

D

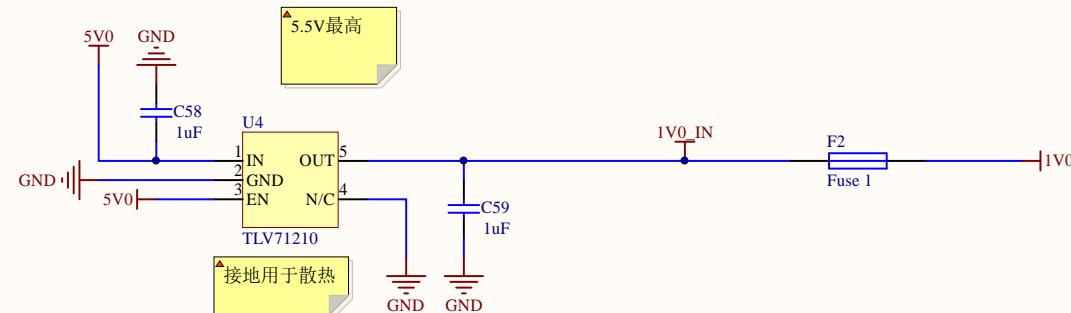
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Title

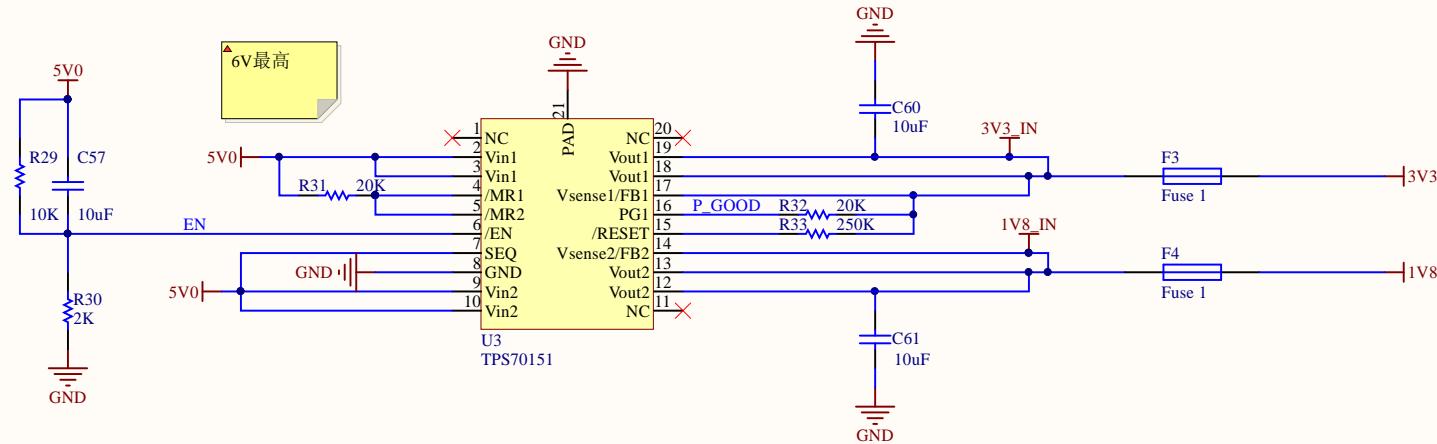
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Date:	2018/6/22	Sheet of
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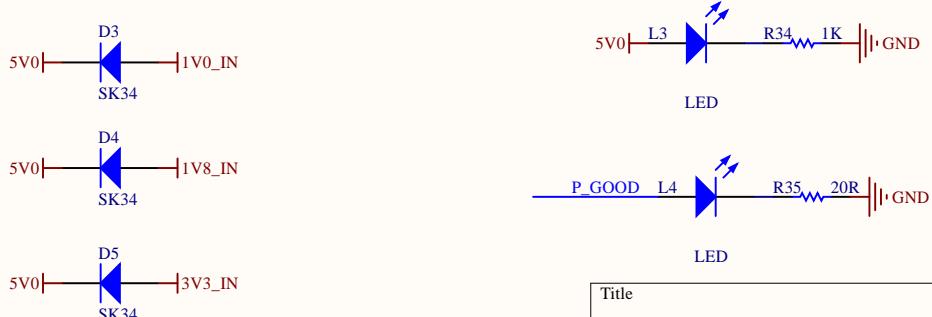
A

B



B

C



C

上电时序:
1 1V0
2 1V8
3 3V3

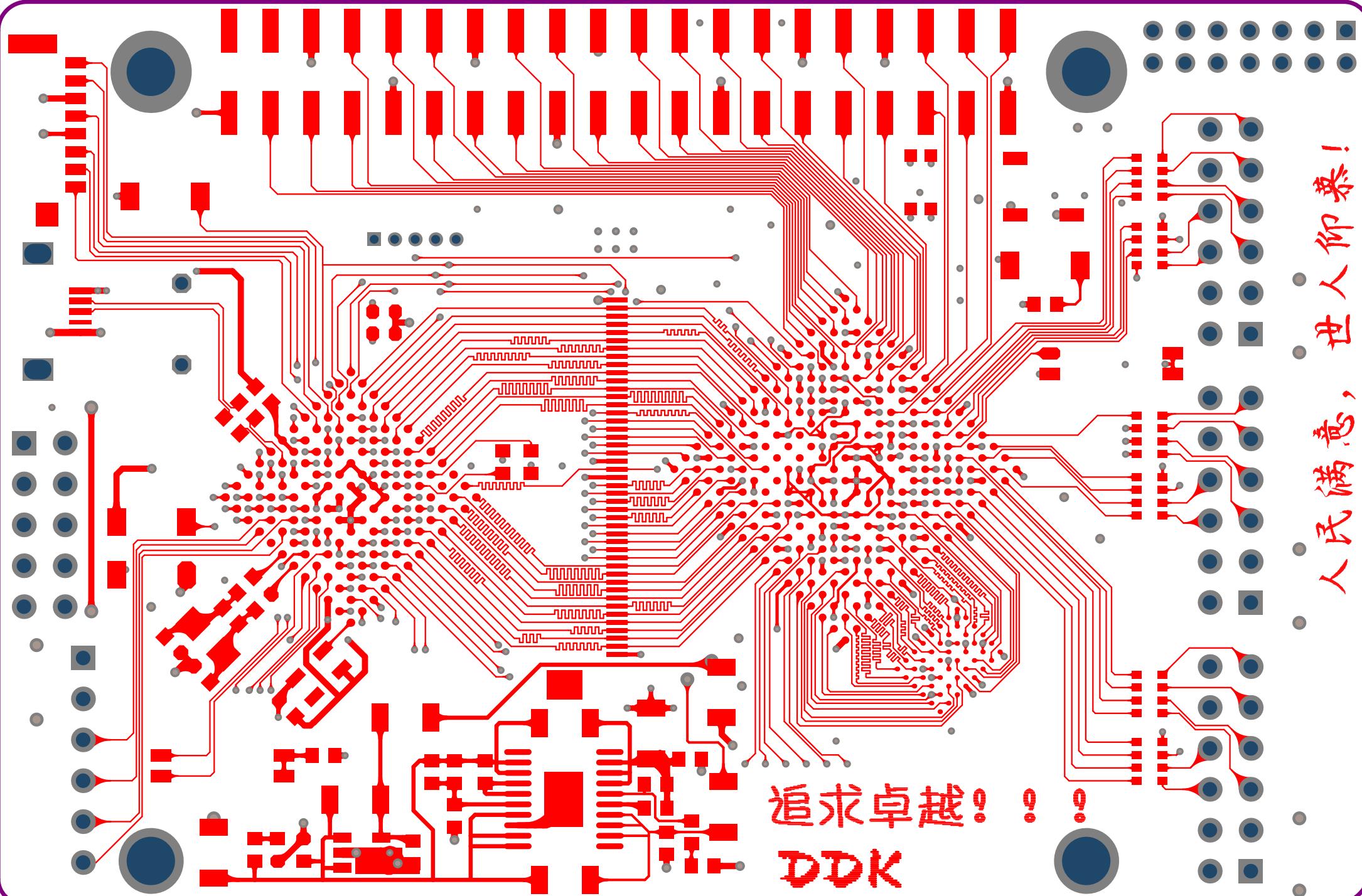
电源定义:
VBUS 4.76.5V 单向
3V3
1V8
1V0

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Date: 2018/6/22	Sheet of	
File: G:\通用平台\PCB\YJ-4-3-1\POWER.Sch		

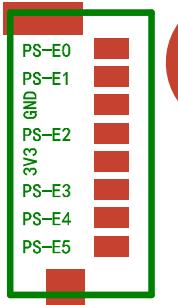
世人仰慕！

人民滿意，

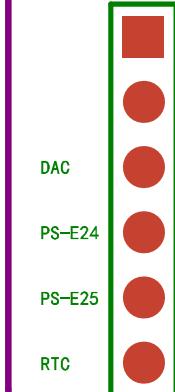
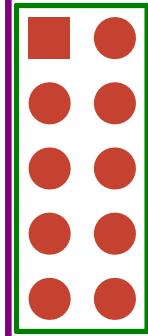
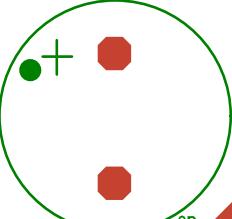
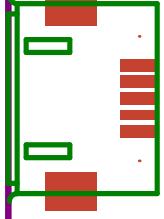
偉業輝煌！
DDK



BANK34



May the Force be with You !! !



PS-A6

PS-A7

RTC

DAC

PS-E24

PS-E25

F

C

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F

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C

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F

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