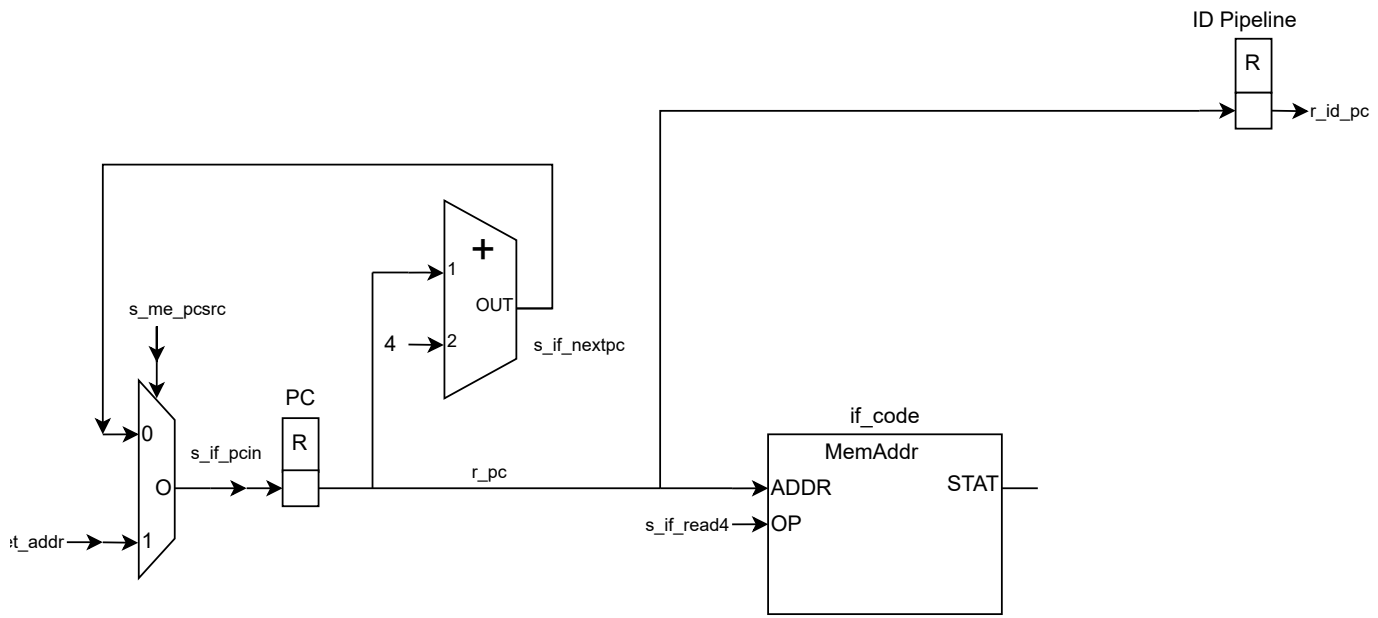
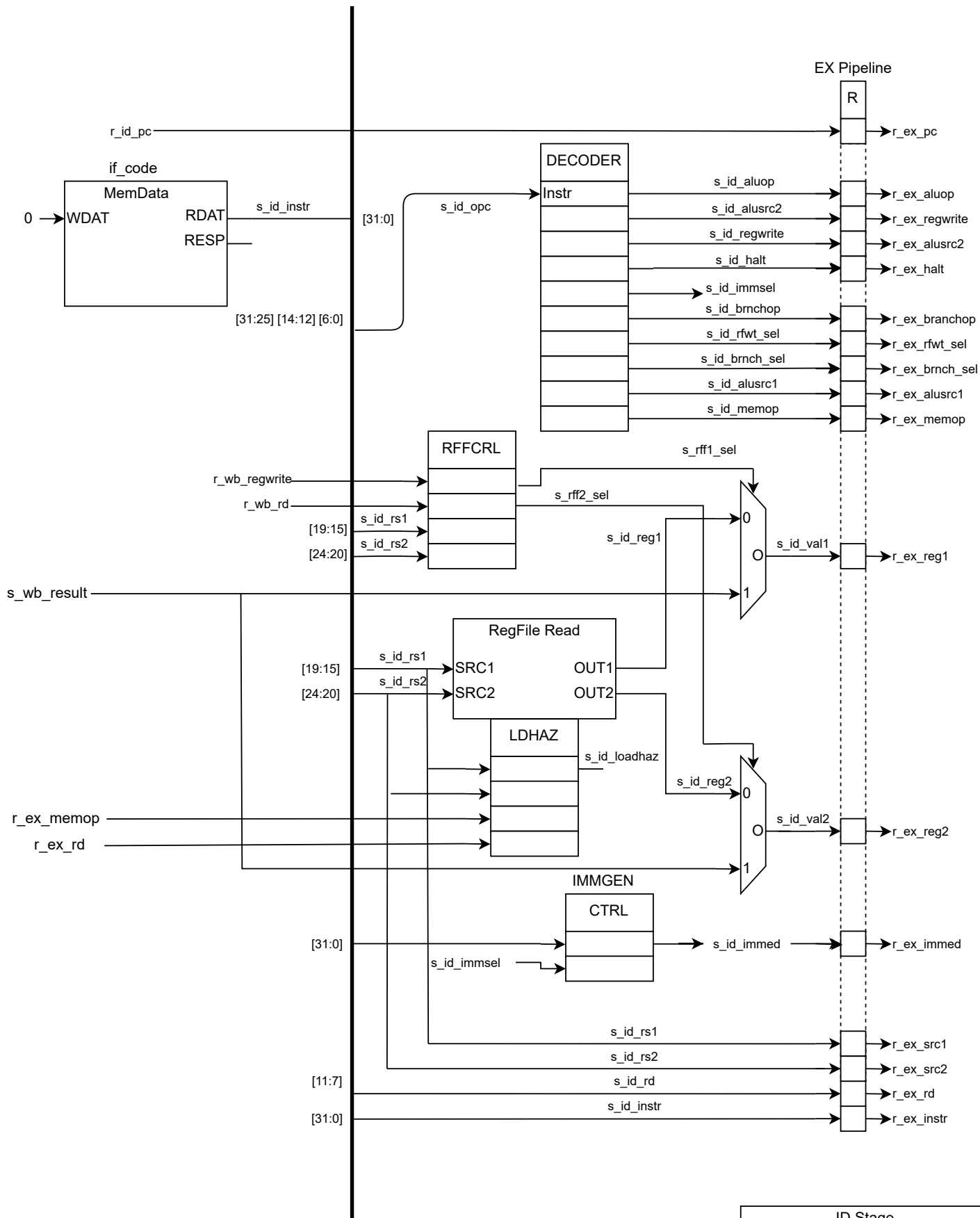


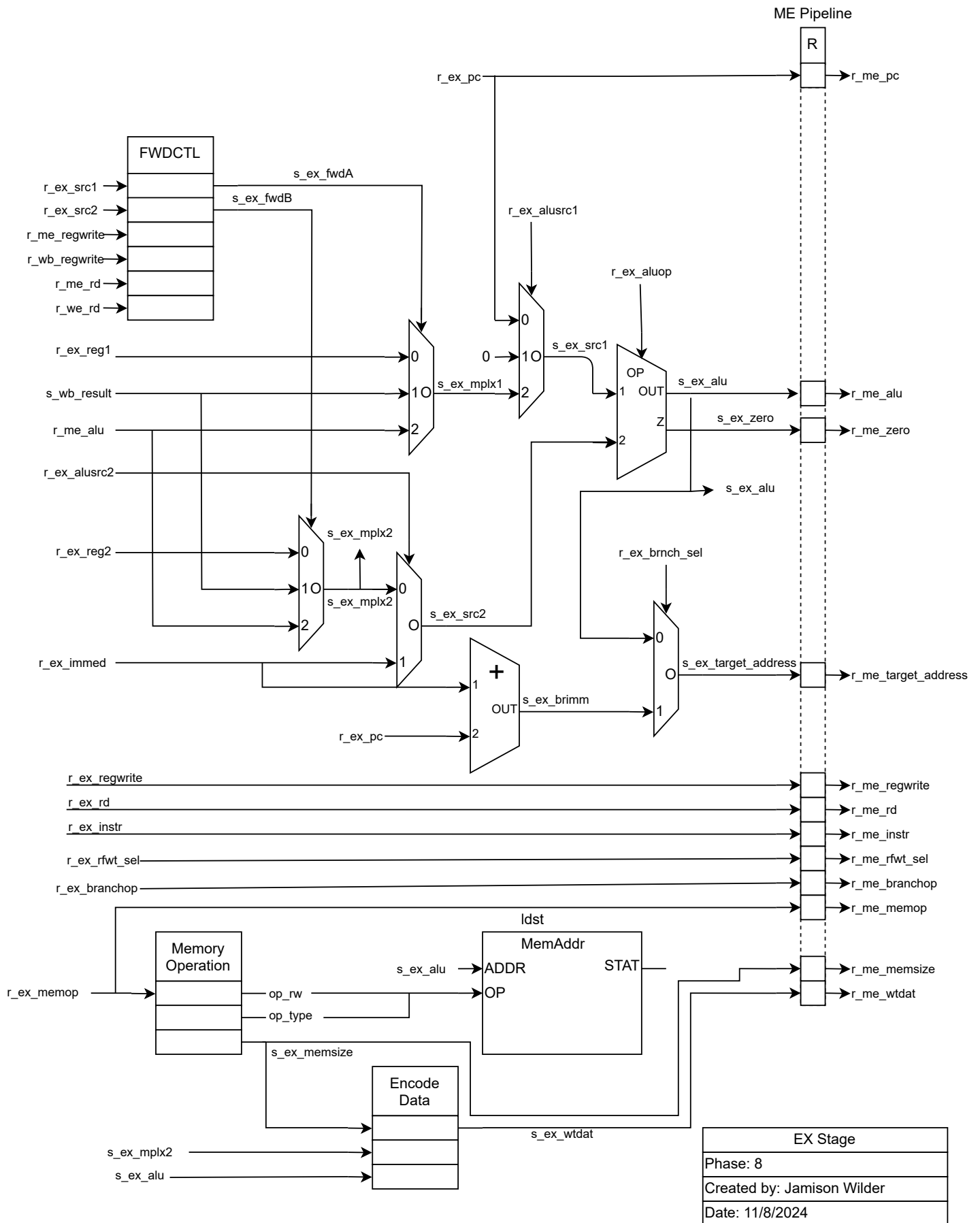
r\_me\_targe

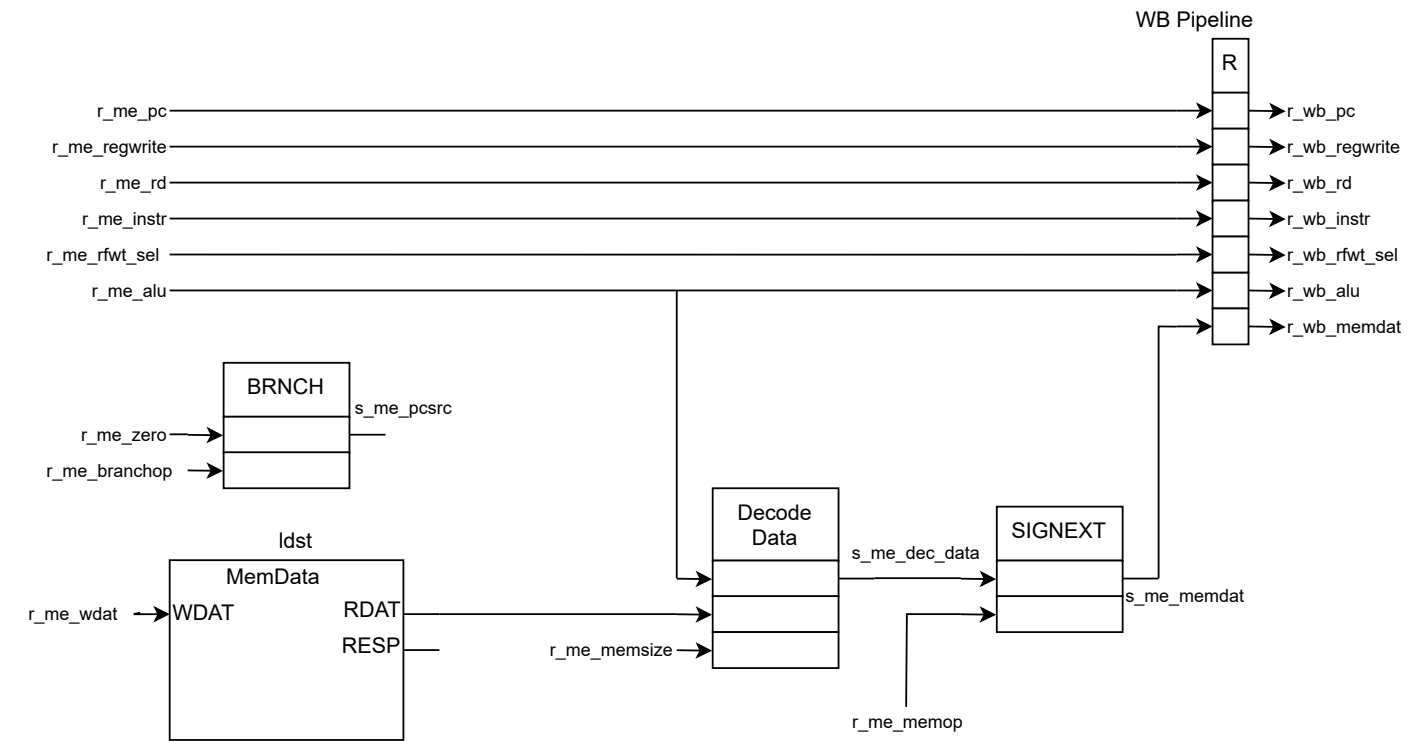


IF Stage
Phase: 8
Created by: Jamison Wilder
Date: 11/8/2024



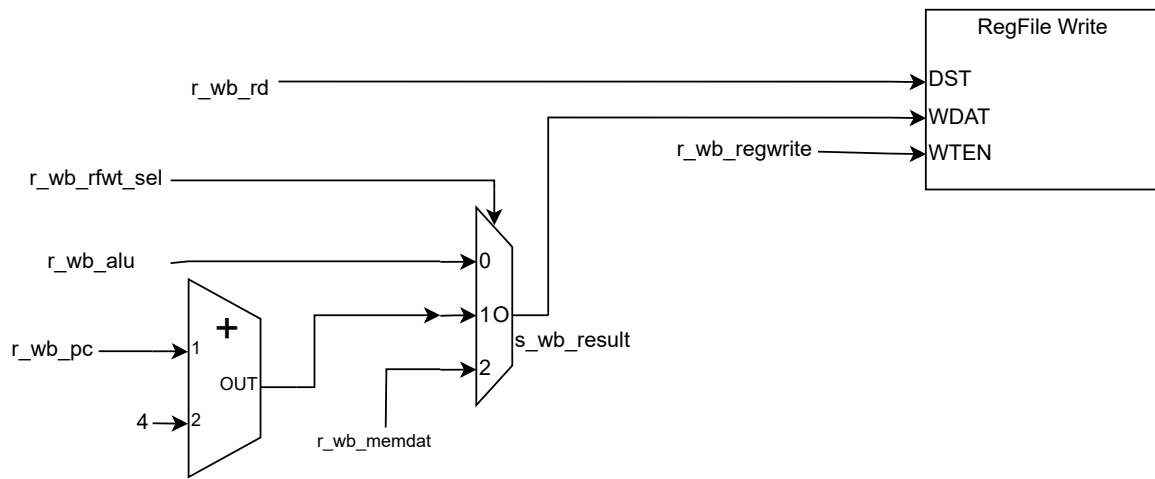
ID Stage
Phase: 8
Created by: Jamison Wilder
Date: 11/8/2024



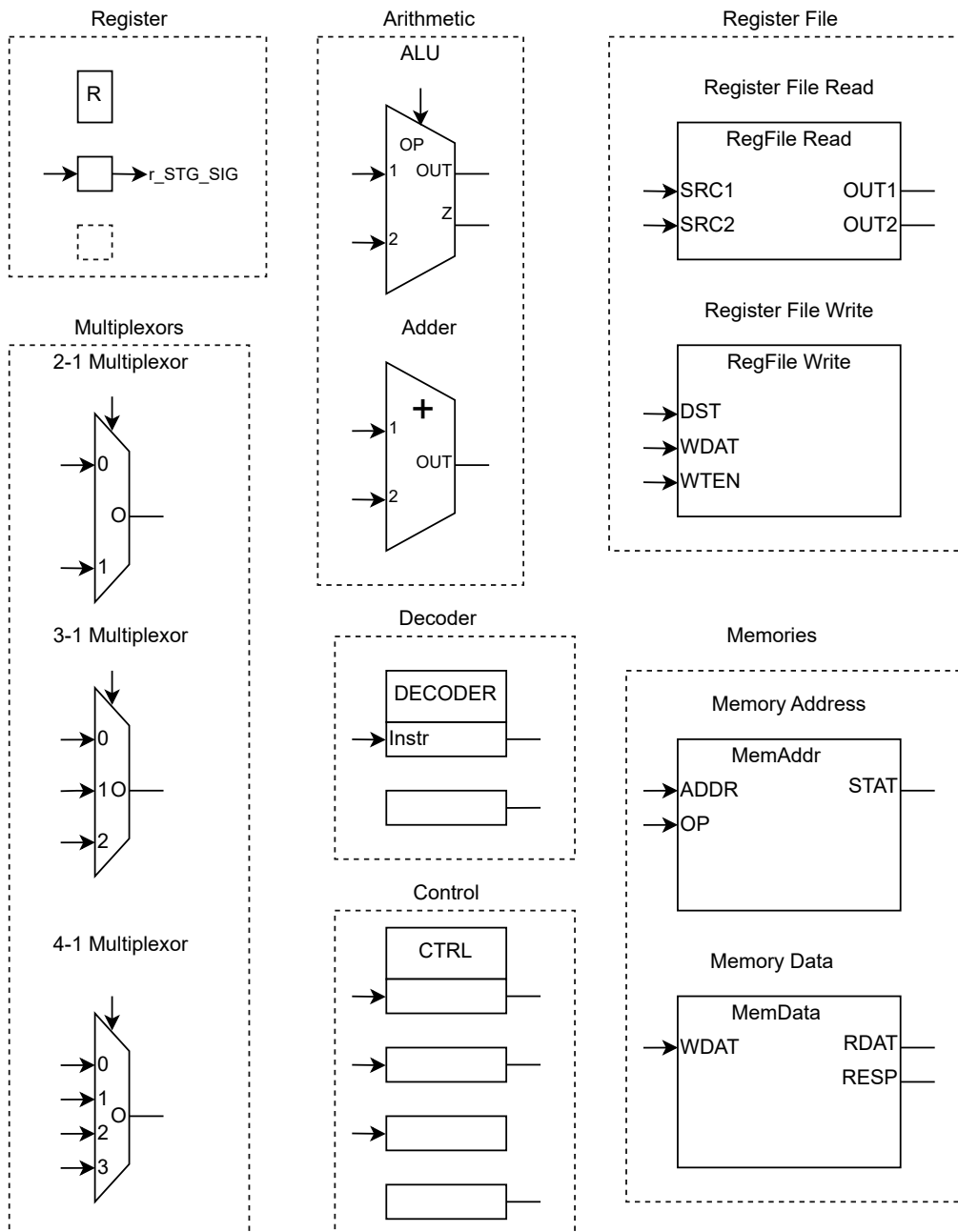


`r_me_target_address`

ME Stage
Phase: 8
Created by: Jamison Wilder
Date: 11/8/2024



WB Stage
Phase: 8
Created by: Jamison Wilder
Date: 11/8/2024



Signal Labels		
Signals In	Signals Out	Signals Internal
r_STG_SIG	s_STG_SIG	s_STG_SIG
s_STG_SIG		

Shape Library
Phase: ALL
Created by: Steve Sheafor
Date: 5/28/2018