## ASEN 5067: Lab 5

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Questions

1. I kept having problems getting my PWM output

to work. One upload it would work fine, the next

it would be stack in a reset loop. I believe the

problem is due to using Timer I for both ECCPs.

This will be reditied by switching ECCP3 to

Timer 3: shead of Timer I.

Edra Credit

An Interrupt Service Rowline (ISR) : s a special subscutine designed and written to accomplish a specific task requested by the interrupt request. The only difference between an ISR and a regular subradine :s an ISR must use a special RITURN stelement - RITTE IE (1). This recuebles etgansola:

The only pins that can accept external interrupt sources are:

- RBO/INTO
- RB9/INT9
- RBZ/INTZ
- RB3/INT3
- PORTBC7:42

## 10.14

The WREG, STATUS, and BSR registers are sured on the stack when a light priority interrupt is triggered. This is in contrast to - low priority; interrupt, which requires those registers to be moundly saved