

6

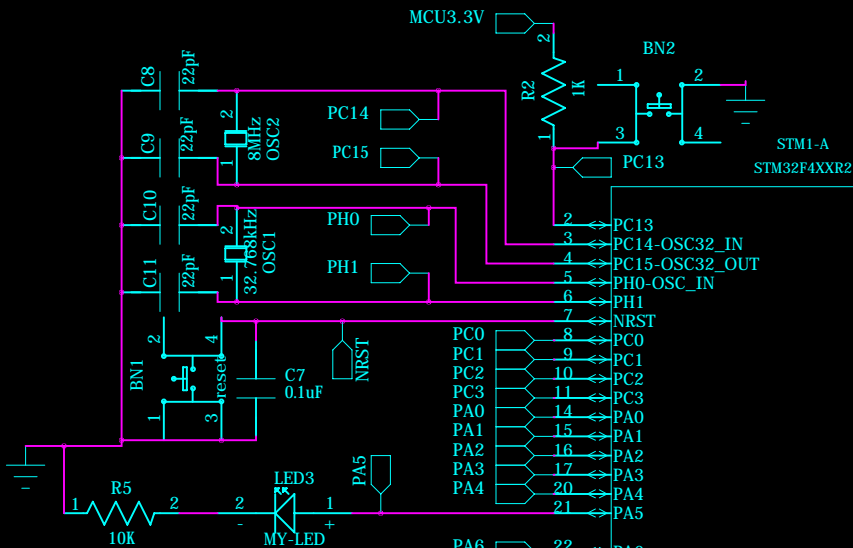
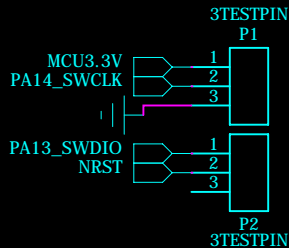
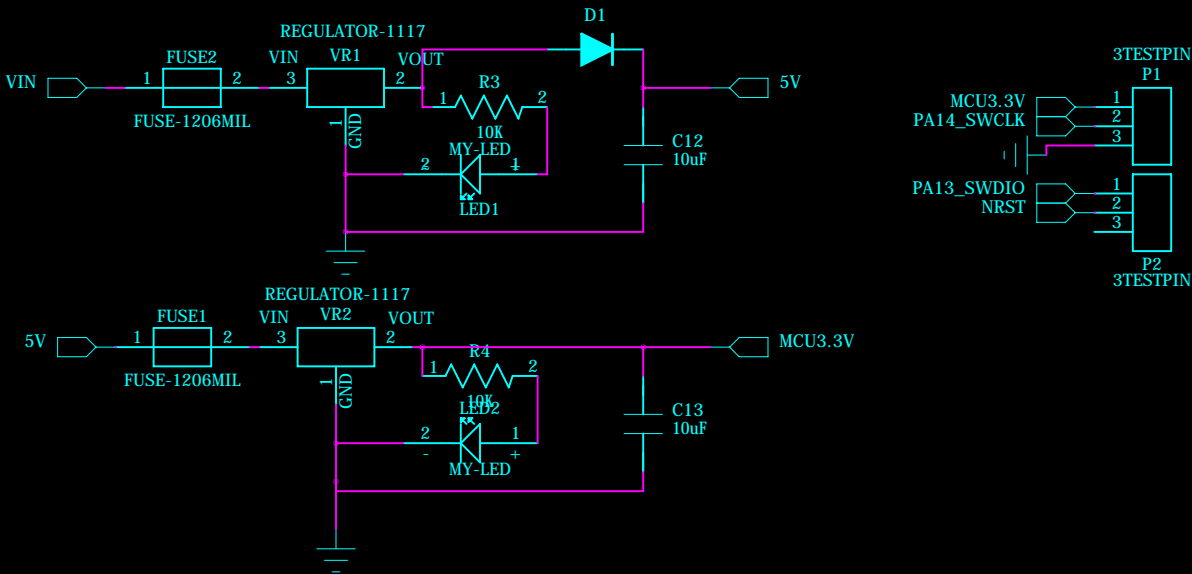
5

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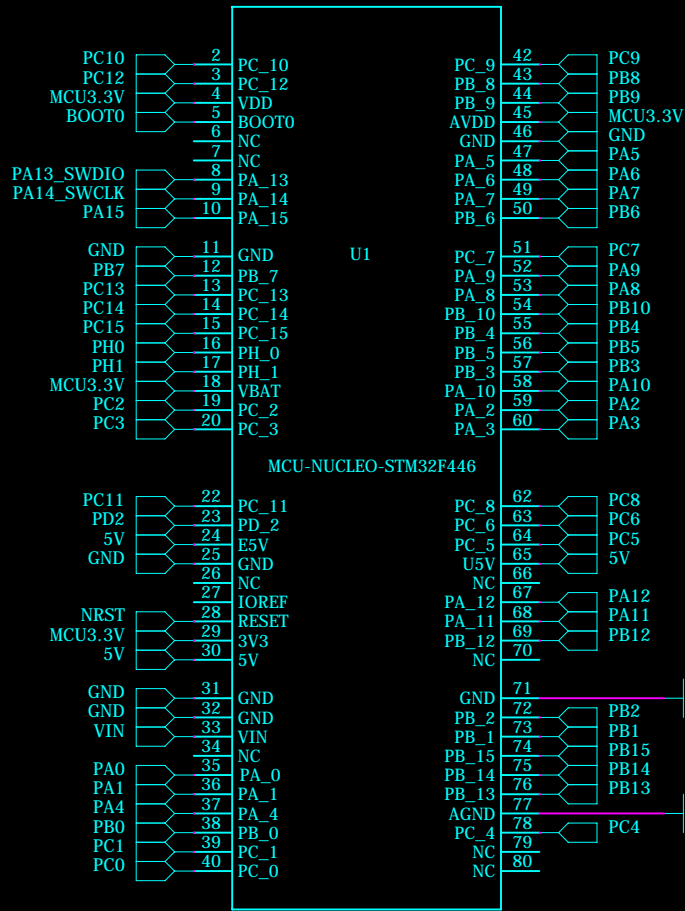
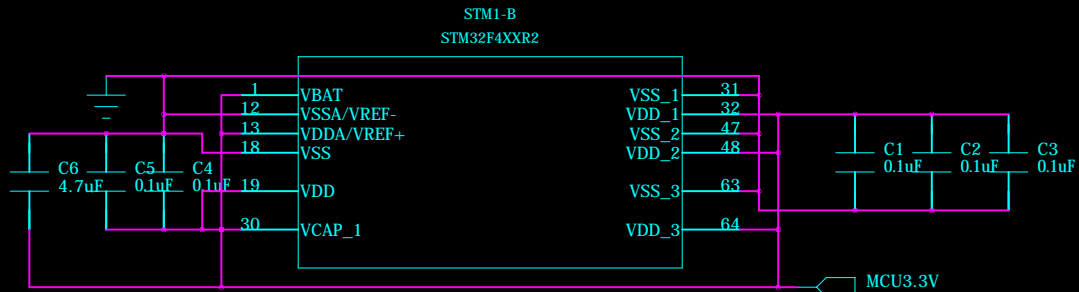
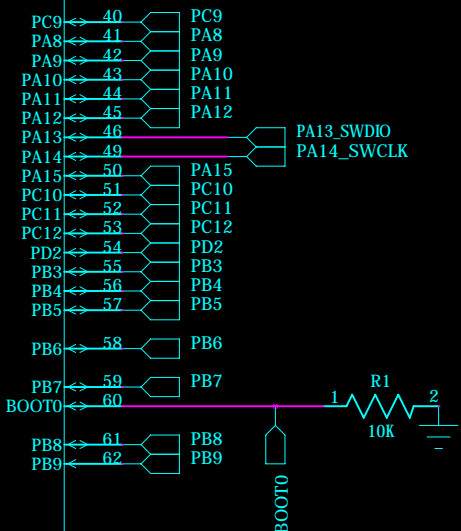
3

2

1



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: William product			
TITLE: chip level testing board			
DRAWN: <Drawn By>	DATED: <Drawn Date>	CODE: <Code>	
CHECKED: <Checked By>	DATED: <Checked Date>	SIZE: A3	DRAWING NO: 1
QUALITY CONTROL: <QC By>	DATED: <QC Date>	REV: 1	
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>	
SHEET: 1 of 1			

DRAWN: <Drawn By>	DATED: <Drawn Date>
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>