

6

5

4

3

2

1

E

D

C

B

A

Sigfox Configuration 2 EVB Schematics

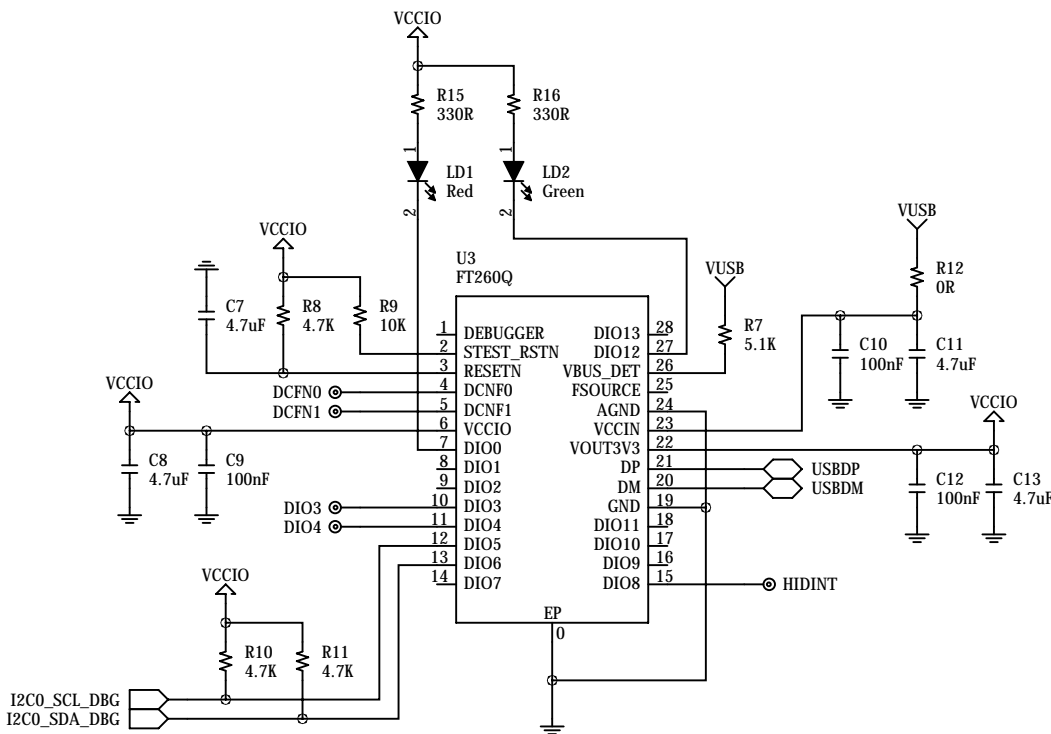
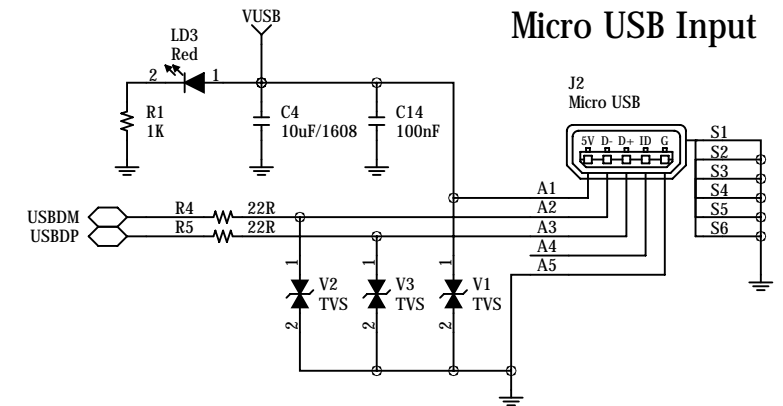
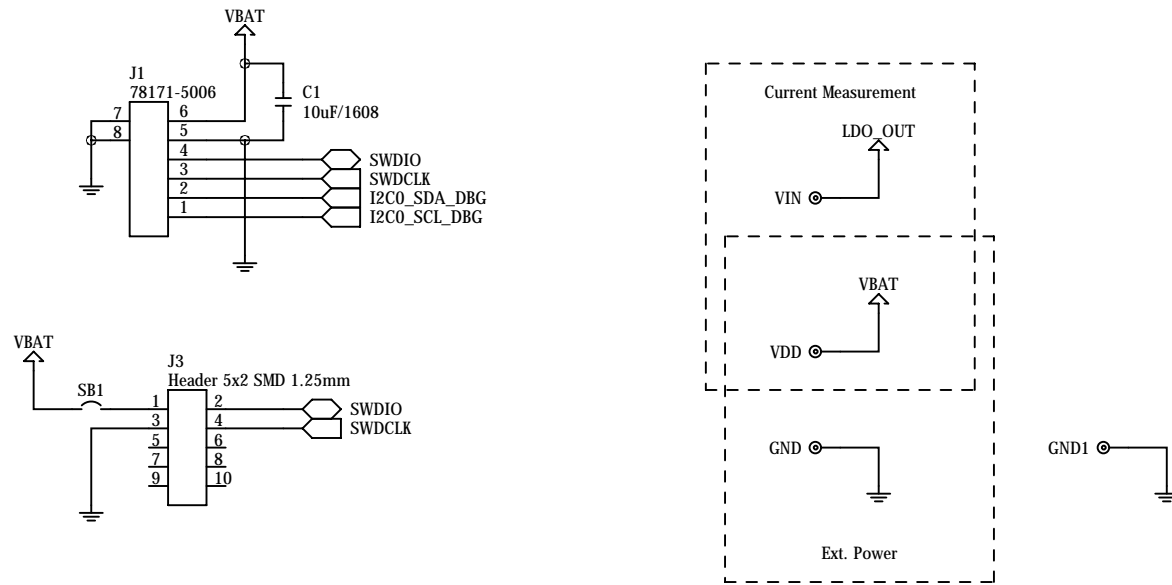
Block Diagram



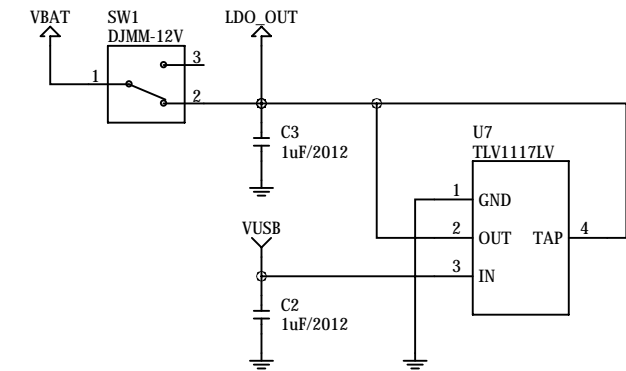
Revision history

| | | | | | |
|-------------|--|-----------|----------------|---------------|--|
| COMPANY: | | | WISOL | | |
| TITLE: | | | ihere debugger | | |
| DRAWING NO: | | REV: | | SIZE: | |
| ihere sub | | 0.0 | | A4 | |
| Date: | | 2/25/2017 | | SHEET: 1 OF 2 | |

[USB - Bridge / Sensors Circuit]



USB Bridge Power



X1 NTH-3.2 X2 NTH-3.2 X3 NTH-3.2 X4 NTH-3.2

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|-----------------|--|--|----------------|--|----------|
| COMPANY: | | | WISOL | | |
| TITLE: | | | ihere debugger | | |
| DRAWING NO: | | | ihere sub | | REV: 0.0 |
| Date: 2/25/2017 | | | SHEET: 2 | | OF 2 |
| | | | SIZE: A4 | | |