定向覆盖模糊测试工具的设计与实现 毕业设计中期检查

雷尚远

南京邮电大学计算机学院

2023年4月17日





- 1 Background
- 2 Theory
- **3** Work
- 4 References

Pre-Knowledge Motivation Research Status

- 2 Theory
- **3** Work
- 4 References

南京邮电大学计算机学院

- 1 Background Pre-Knowledge

南京邮电大学计算机学院

What Fuzzing is?

Defination[1]

- Fuzzing Fuzzing is the execution of the PUT using input(s) sampled from an input space (the "fuzz input space") that protrudes the expected input space of the PUT.
 - PUT: Program Under Test
- Fuzz testing Fuzz testing is the use of fuzzing to test if a PUT violates a correctness policy.
- Fuzzer A fuzzer is a program that performs fuzz testing on a PUT.
- Bug Oracle A bug oracle is a program, perhaps as part of a fuzzer, that
 determines whether a given execution of the PUT violates a specific
 correctness policy.
- Fuzz Configuration A fuzz configuration of a fuzz algorithm comprises the parameter value(s) that control(s) the fuzz algorithm.
- **Seed** A seed is a (commonly well-structured) input to the PUT, used to generate test cases by modifying it.



Fuzz Testing

```
\begin{split} & \text{Input: } \mathbb{C}, \, t_{\text{limit}} \\ & \text{Output: } \mathbb{B} \, / / \text{ a finite set of bugs} \\ & 1 \ \mathbb{B} \leftarrow \varnothing \\ & 2 \ \mathbb{C} \leftarrow \text{Preprocess}(\mathbb{C}) \\ & 3 \ \text{while } t_{\text{elapsed}} < t_{\text{limit}} \land \text{Continue}(\mathbb{C}) \text{ do} \\ & 4 \ \quad \text{conf} \leftarrow \text{Schedule}(\mathbb{C}, t_{\textit{elapsed}}, t_{\textit{limit}}) \\ & 5 \ \quad \text{tcs} \leftarrow \text{InputGen}(\textit{conf}) \\ & \ \quad / / \ O_{\text{bug}} \text{ is embedded in a fuzzer} \\ & 6 \ \quad \mathbb{B}', \, \text{execinfos} \leftarrow \text{InputEval}(\textit{conf}, \, \textit{tcs}, \, O_{\textit{bug}}) \\ & 7 \ \quad \mathbb{C} \leftarrow \text{ConfUpdate}(\mathbb{C}, \, \textit{conf}, \, \textit{execinfos}) \\ & 8 \ \quad \mathbb{B} \leftarrow \mathbb{B} \cup \mathbb{B}' \end{split}
```

9 return B

00000000000

```
1 Input: C, t<sub>limit</sub>
 2 Output: B // a finite set of bugs
   \mathbb{C} \leftarrow \mathtt{Preprocess}(\mathbb{C})
   while t_{\tt elapsed} < t_{\tt limit} \land {\tt Continue}(\mathbb{C}) do
             \texttt{conf} \leftarrow \texttt{Schedule}(\mathbb{C},\, t_{\textit{elapsed}},\, t_{\textit{limit}})
 7
             tcs \leftarrow InputGen(conf)
             // O_{\text{bug}} is embedded in a fuzzer
             \mathbb{B}', execinfos \leftarrow InputEval(conf, tcs, O_{bus})
 8
             \mathbb{C} \leftarrow \texttt{ConfUpdate}(\mathbb{C}, conf, execinfos)
 9
             \mathbb{B} \leftarrow \mathbb{B} \cup \mathbb{B}'
11 return B
```

- C:a set of fuzz configurations
- t_{limit}: timeout
- B: a set of discovered bugs

```
Input: \mathbb{C}, t_{limit}
   Output: \mathbb{B} // a finite set of bugs
   \mathbb{R} \leftarrow \varnothing
   \mathbb{C} \leftarrow \text{Preprocess}(\mathbb{C})
  while t_{\tt elapsed} < t_{\tt limit} \land {\tt Continue}(\mathbb{C}) do
            conf \leftarrow Schedule(\mathbb{C}, t_{elansed}, t_{limit})
           tcs \leftarrow InputGen(conf)
5
            // O_{\text{bug}} is embedded in a fuzzer
           \mathbb{B}', execinfos \leftarrow InputEval(conf, tcs, O_{bug})
6
           \mathbb{C} \leftarrow \texttt{ConfUpdate}(\mathbb{C}, conf, execinfos)
7
           \mathbb{B} \leftarrow \mathbb{B} \cup \mathbb{B}'
```

Preprocess $(\mathbb{C}) \to \mathbb{C}$

- Instrumentation
 - grey-box and white-box fuzzers
 - static/dynamic(INPUTEVAL)
- Seed Selection
 - weed out potentially redundant configurations
- Seed Trimming
 - reduce the size of seeds
- Preparing a Driver Application
 - library Fuzzing, kernal Fuzzing

9 return B

```
Input: \mathbb{C}, t_{limit}
   Output: \mathbb{B} // a finite set of bugs

    B ← Ø

   \mathbb{C} \leftarrow \mathtt{Preprocess}(\mathbb{C})
  while t_{\tt elapsed} < t_{\tt limit} \land {\tt Continue}(\mathbb{C}) do
            conf \leftarrow Schedule(\mathbb{C}, t_{elapsed}, t_{limit})
           tcs \leftarrow InputGen(conf)
5
           // O_{\text{bug}} is embedded in a fuzzer
           \mathbb{B}', execinfos \leftarrow InputEval(conf, tcs, O_{bug})
6
           \mathbb{C} \leftarrow \texttt{ConfUpdate}(\mathbb{C}, conf, execinfos)
7
           \mathbb{B} \leftarrow \mathbb{B} \cup \mathbb{B}'
9 return B
```

Stop Condition

- t_{elapsed} < t_{limit}
- CONTINUE (ℂ) → {True, False} - Determine whether a new fuzz iteration should occur

```
Input: \mathbb{C}, t_{limit}
    Output: \mathbb{B} // a finite set of bugs
  \mathbb{R} \leftarrow \emptyset
  \mathbb{C} \leftarrow \mathtt{Preprocess}(\mathbb{C})
  while t_{\tt elapsed} < t_{\tt limit} \land {\tt Continue}(\mathbb{C}) do
            conf \leftarrow Schedule(\mathbb{C}, t_{elansed}, t_{limit})
            tcs \leftarrow InputGen(conf)
5
            // O_{\text{bug}} is embedded in a fuzzer
            \mathbb{B}', execinfos \leftarrow InputEval(conf, tcs, O_{bug})
6
            \mathbb{C} \leftarrow \texttt{ConfUpdate}(\mathbb{C}, conf, execinfos)
7
            \mathbb{B} \leftarrow \mathbb{B} \cup \mathbb{B}'
9 return B
```

Schedule (\mathbb{C} , $t_{elapsed}$, t_{limit}) \rightarrow conf

- Function
 - Pick important information(conf)
 - FCS Problem
 - exploration: Spent time on gathering more accurate information on each configuration to inform future decisions
 - exploitation: Spent time on fuzzing the configurations that are currently believed to lead to more favorable outcomes

00000000000

```
Input: \mathbb{C}, t_{limit}
   Output: \mathbb{B} // a finite set of bugs

    B ← Ø

_{2} \mathbb{C}\leftarrow \mathtt{Preprocess}(\mathbb{C})
3 while t_{\tt elapsed} < t_{\tt limit} \land {\tt Continue}(\mathbb{C}) do
            conf \leftarrow Schedule(\mathbb{C}, t_{elapsed}, t_{limit})
           tcs \leftarrow InputGen(conf)
5
           // O_{\text{bug}} is embedded in a fuzzer
           \mathbb{B}', execinfos \leftarrow InputEval(conf, tcs, O_{bug})
6
           \mathbb{C} \leftarrow \texttt{ConfUpdate}(\mathbb{C}, conf, execinfos)
7
           \mathbb{B} \leftarrow \mathbb{B} \cup \mathbb{B}'
9 return B
```

INPUTGEN (conf) \rightarrow tcs

function

- Generate testcases

classification

- Generation-based(Model-based)
- Mutation-based(Model-less)
- White-box Fuzzers: symbolic execution

Background റെറ്റെറററററ

```
Input: \mathbb{C}, t_{limit}
   Output: \mathbb{B} // a finite set of bugs

    B ← Ø

_{2} \mathbb{C}\leftarrow \mathtt{Preprocess}(\mathbb{C})
3 while t_{\texttt{elapsed}} < t_{\texttt{limit}} \land \texttt{Continue}(\mathbb{C}) do
            conf \leftarrow Schedule(\mathbb{C}, t_{elapsed}, t_{limit})
           tcs \leftarrow InputGen(conf)
5
           // O_{\text{bug}} is embedded in a fuzzer
           \mathbb{B}', execinfos \leftarrow InputEval(conf, tcs, O_{bug})
6
           \mathbb{C} \leftarrow \texttt{ConfUpdate}(\mathbb{C}, conf, execinfos)
7
           \mathbb{B} \leftarrow \mathbb{B} \cup \mathbb{B}'
9 return B
```

```
InputEval (conf, tcs, O_{bug})
      \to \mathbb{B}', execinfos
```

- **Fuzzing PUT**
 - -tcs
- ℝ'
- Feedback Information
 - conf, tcs
 - execinfos (tcs,crashes,stack backtrace hash,edge coverage,etc.)

00000000000

```
Input: \mathbb{C}, t_{limit}
   Output: B // a finite set of bugs

    B ← Ø

_{2} \mathbb{C}\leftarrow \mathtt{Preprocess}(\mathbb{C})
3 while t_{\tt elapsed} < t_{\tt limit} \land {\tt Continue}(\mathbb{C}) do
           conf \leftarrow Schedule(\mathbb{C}, t_{elapsed}, t_{limit})
           tcs \leftarrow InputGen(conf)
5
           // Obug is embedded in a fuzzer
           \mathbb{B}', execinfos \leftarrow InputEval(conf, tcs, O_{bug})
6
           \mathbb{C} \leftarrow \text{ConfUpdate}(\mathbb{C}, conf, execinfos)
7
           \mathbb{B} \leftarrow \mathbb{B} \cup \mathbb{B}'
```

```
    CONFUPDATE (C, conf,

    execinfos) \rightarrow \mathbb{C}
    - Update Fuzz
    Configuration(distinguishablity)
    - Seed Pool Update
• \mathbb{B} \cup \mathbb{B}' \to \mathbb{B}
```

- Update Bugs Set

9 return B

```
Input: \mathbb{C}, t_{limit}
    Output: \mathbb{B} // a finite set of bugs
  \mathbb{R} \leftarrow \emptyset
   \mathbb{C} \leftarrow \mathtt{Preprocess}(\mathbb{C})
   while t_{\tt elapsed} < t_{\tt limit} \land {\tt Continue}(\mathbb{C}) do
            conf \leftarrow Schedule(\mathbb{C}, t_{elapsed}, t_{limit})
            tcs \leftarrow InputGen(conf)
5
            // O_{\text{bug}} is embedded in a fuzzer
            \mathbb{B}', execinfos \leftarrow InputEval(conf, tcs, O_{bug})
6
            \mathbb{C} \leftarrow \texttt{ConfUpdate}(\mathbb{C}, conf, execinfos)
7
            \mathbb{B} \leftarrow \mathbb{B} \cup \mathbb{B}'
9 return B
```

stop condition

- t_{elapsed} < t_{limit}
- CONTINUE (ℂ) → {True, False} - Determine whether a new fuzz iteration should occur

Background

Pre-Knowledge

Motivation

南京邮电大学计算机学院

00000000000

The amount of collected information defines the color of a fuzzer[1].

```
Input: C, t<sub>limit</sub>
    Output: \mathbb{B} // a finite set of bugs
   \mathbb{B} \leftarrow \varnothing
2 \mathbb{C} \leftarrow \text{Preprocess}(\mathbb{C})
3 while t_{\tt elapsed} < t_{\tt limit} \land {\tt Continue}(\mathbb{C}) do
            conf \leftarrow Schedule(\mathbb{C}, t_{elansed}, t_{limit})
            tcs \leftarrow InputGen(conf)
5
            // O_{\text{bug}} is embedded in a fuzzer
            \mathbb{B}', execinfos \leftarrow InputEval(conf, tcs, O_{bus})
6
            \mathbb{C} \leftarrow \text{ConfUpdate}(\mathbb{C}, conf, execinfos)
7
            \mathbb{B} \leftarrow \mathbb{B} \cup \mathbb{B}'
9 return B
```

- program instrumentation
 - static
 - dynamic
- processor traces
- system call usage
- etc.

Program Instrumentation

- Static
 - source code
 - intermediate code
 - binary-level
- Dynamic

Program Instrumentation

- Static
- Dynamic
 - dynamically-linked libraries
 - execution feedback: branch coverage, new path, etc.
 - race condition bugs: thread scheduling

Classification of Fuzzing

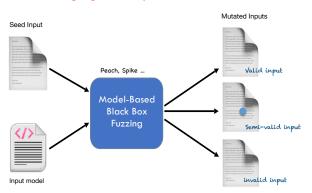
- Black-box Fuzzing
 - no program analysis, no feedback
- White-box Fuzzing
 - mostly program analysis
- Grey-box Fuzzing
 - no program analysis, but feedback



Black-box Fuzzing

Defination: techniques that do not see the internals of the PUT, and can observe only the input/output behavior of the PUT, treating it as a black-box[1].

-No program analysis, no feedback

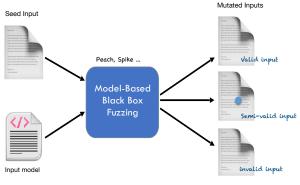




Black-box Fuzzing

Defination: techniques that do not see the internals of the PUT, and can observe only the input/output behavior of the PUT, treating it as a black-box[1].

- No program analysis, no feedback



- You have no view of the PUT,but have some view of the input/output domain
- Fuzzing congfigurations are not changed according to some feedback - some fuzzer may add the testcases to seed pool
- Not effective

References

Why Grey-box Fuzzing?

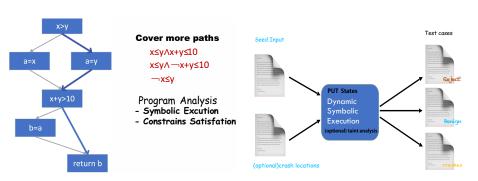
Background

00000000000

White-box Fuzzing

Defination: techniques that generates test cases by analyzing the internals of the PUT and the information gathered when executing the PUT[1].

- Requires heavy-weight program analysis and constraint solving.



Why Grey-box Fuzzing?

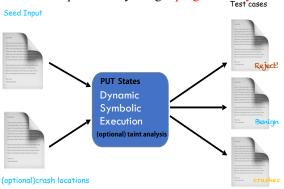
Background

<u>೧೧೧00000</u>000

White-box Fuzzing

Defination: techniques that generates test cases by analyzing the internals of the PUT and the information gathered when executing the PUT[1].

- Requires heavy-weight program analysis and constraint solving. Test cases

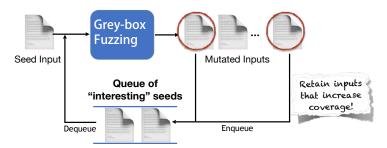


- You have the view of the PUT state(CFG,CG)
- Heavy-weight Program analysis (effective but not efficient!)

Grey-box Fuzzing

Defination: techniques that can obtain *some* information internal to the PUT and/or its executions to generates test cases[1].

- Uses only lightweight instrumentation to glean some program structure
- And coverage feedback





Grey-box Fuzzing is frequently used

- State-of-the-art in automated vulnerability detection
- Extremely efficient coverage-based input generation
 - All program analysis before/at instrumentation time.
 - Start with a seed corpus, choose a seed file, fuzz it.
 - Add to corpus only if new input increases coverage.



Directed Fuzzing has many applications

- Patch Testing: reach changed statements
- Crash Reproduction: exercise stack trace
- SA Report Verification: reach "dangerous" location
- **Information Flow Detection**: exercise source-sink pairs



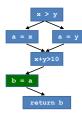
Why Directed Grey-box Fuzzing?

Directed Fuzzing

- Goal:reach a specific target
 - Target Locations: the line number in the source code or the virtual memory address at the binary level[2].
 - Target Bugs: use-after-free vulnerabilities, etc.

DSE:classical constraint satisfaction problem

- uses program analysis and constraint solving to generate inputs that systematically and effectively explore the state space of feasible paths[3].
- Program analysis to identify program paths that reach given program locations.
- Symbolic Execution to derive path conditions for any of the identified paths.
- Constraint Solving to find an input



 $\varphi_1 = (x>y) \wedge (x+y>10)$ $\varphi_2 = \neg (x>y) \wedge (x+y>10)$

- Effectiveness comes at the cost of efficiency
- Heavy-weight program analysis

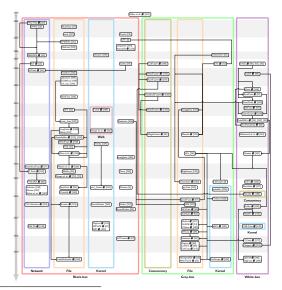


00000000000

- Pre-Knowledge
- Research Status



Genealogy tracing significant fuzzers' lineage¹



¹paper[1]-Figure1

Representative Work

- AFLGo(2017)[4]
- Hawkeye(2018)[5]

Theory ●○○○○

- Background
- 2 Theory
- 4 References

南京邮电大学计算机学院

- Background
- 2 Theory AFLGo
- 3 Work
- 4 References

南京邮电大学计算机学院

Directed Fuzzing as optimisation problem!

- Instrumentation Time
 - 1 Extract call graph (CG) and control-flow graphs (CFGs).
 - 2 For each **BB**, compute **distance** to target locations.
 - 3 Instrument program to aggregate distance values.
- Runtime
 - 1 collect coverage and distance information, and
 - 2 decide how long to be fuzzed based on distance.
 - If input is closer to the targets, it is fuzzed for longer.
 - If input is further away from the targets, it is fuzzed for shorter.



OverView

AFLGo Architecture



南京邮电大学计算机学院

Algorithm

Directed Grey-box Fuzzing

```
Input: S // a finite set of seeds
   Input: \mathbb{T}// a finite set of targer sits
   Output: \mathbb{S}' // a finite set of buggy seeds
1 S' \leftarrow \emptyset
2 SeedQueue \leftarrow \mathbb{S}
3 Graphs ← GraphExt (Code)
  BBdistance \leftarrow \mathtt{DisCalcu}(\mathbb{T}, Graphs)
5 while !siganl do
         s \leftarrow Dequeue(SeedQueue)
         e \leftarrow \texttt{AssinEnergy}(s, t_{elapsed}, \textit{BBdistance})
 7
        for i \leftarrow 1 to e do
 8
              s' \leftarrow Mutation(s)
 9
              trace \leftarrow Execution(s')
10
              if s' crashes then \mathbb{S}' \leftarrow \mathbb{S}' \cup \mathbf{s}'
11
              if IsIntersting(s') then
12
                    distance \leftarrow SeedEva(trace, BBdistance)
13
                    SeedQueue \leftarrow SortInser(distance,s',SeedQueue)
14
15 return S'
```

- 4 ロ b 4 個 b 4 差 b 4 差 b - 差 - 釣 9 0 0

```
Input: S// a finite set of seeds
   Input: \mathbb{T}// a finite set of targer sits
   Output: \mathbb{S}' // a finite set of buggy seeds
1 S' \leftarrow \emptyset
2 SeedQueue \leftarrow \mathbb{S}
3 Graphs ← GraphExt(Code)
4 BBdistance \leftarrow DisCalcu(T, Graphs)
5 while !siganl do
        s \leftarrow Dequeue(SeedQueue)
        e \leftarrow \texttt{AssinEnergy}(s, t_{elapsed}, \textit{BBdistance})
 7
        for i \leftarrow 1 to e do
 8
              s' \leftarrow Mutation(s)
 9
              trace \leftarrow Execution(s')
10
              if s' crashes then \mathbb{S}' \leftarrow \mathbb{S}' \cup \mathbf{s}'
11
              if IsIntersting(s') then
12
                   distance \leftarrow SeedEva(trace, BBdistance)
13
                   SeedQueue \leftarrow SortInser(distance, s', SeedQueue)
14
15 return S'
```

Work

- Background
- 2 Theory
- 3 Work
- 4 References

南京邮电大学计算机学院

- 1 Background
- **3** Work
- 4 References

- [1] MANÈS V J, HAN H, HAN C, et al. The art, science, and engineering of fuzzing: A survey[J]. IEEE Transactions on Software Engineering, 2019, 47(11): 2312–2331.
- [2] WANG P, ZHOU X, LU K, et al. The Progress, Challenges, and Perspectives of Directed Greybox Fuzzing[EB]. arXiv, 2022.
- [3] MA K-K, YIT PHANG K, FOSTER J S, et al. Directed symbolic execution[C] // Static Analysis: 18th International Symposium, SAS 2011, Venice, Italy, September 14-16, 2011. Proceedings 18. 2011: 95-111.
- [4] BÖHME M, PHAM V-T, NGUYEN M-D, et al. Directed Greybox Fuzzing[C/OL] // Proceedings of the 2017 ACM SIGSAC Conference on Computer and Communications Security. Dallas Texas USA: ACM, 2017: 2329-2344. http://dx.doi.org/10.1145/3133956.3134020.

- 4 ロ ト 4 御 ト 4 重 ト 4 重 ト 9 Q Q

[5] CHEN H, XUE Y, LI Y, et al. Hawkeye: Towards a Desired Directed Grey-Box Fuzzer[C/OL] // Proceedings of the 2018 ACM SIGSAC Conference on Computer and Communications Security. Toronto Canada: ACM, 2018: 2095-2108. http://dx.doi.org/10.1145/3243734.3243849.

Thanks!