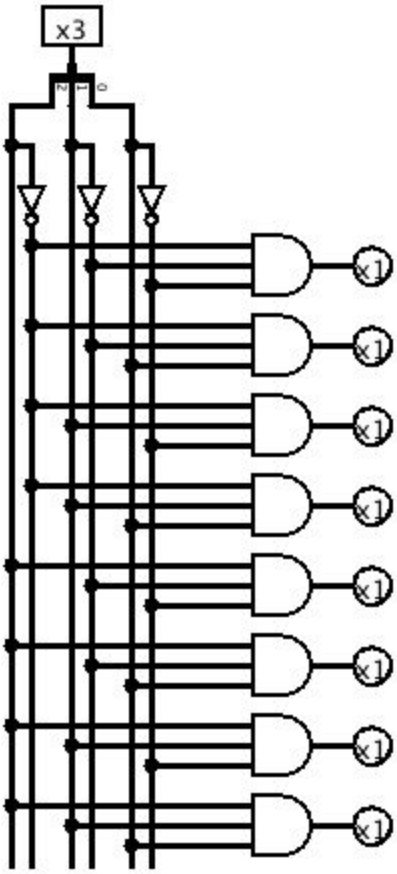
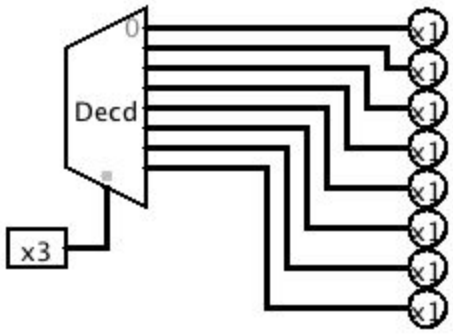
William “Chris” Fenton
CSF - Digital Logic
Lab 3

Decoders

1.

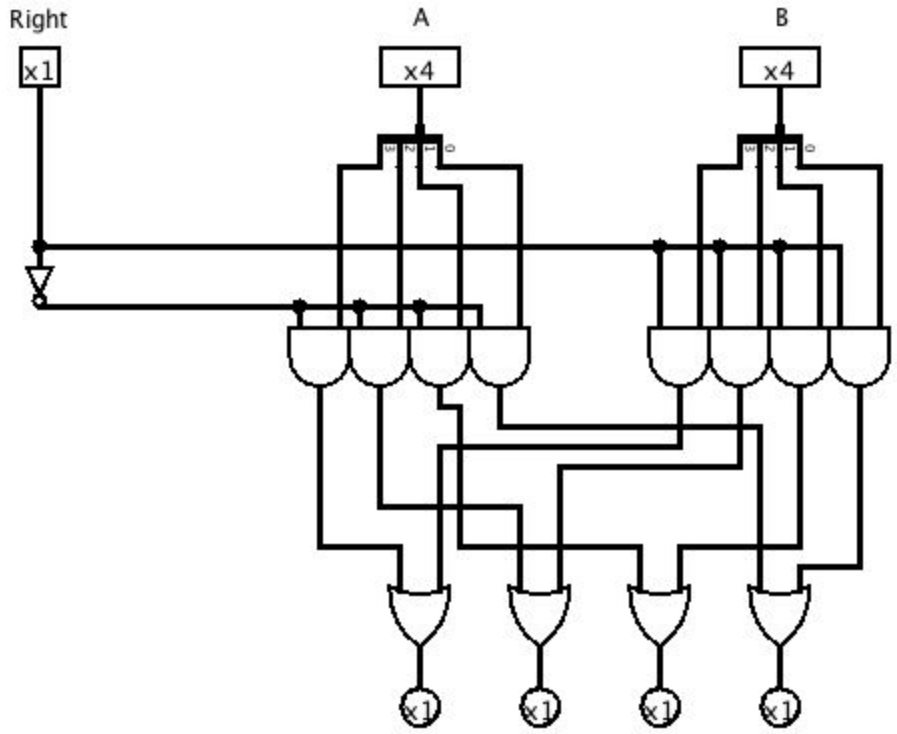


2.

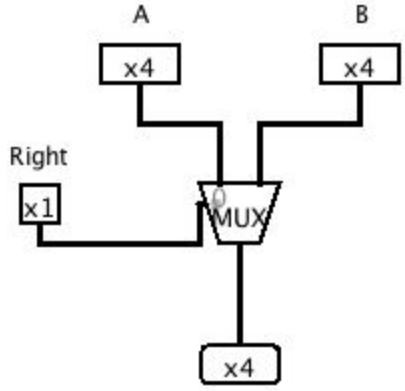


Multiplexers

1.

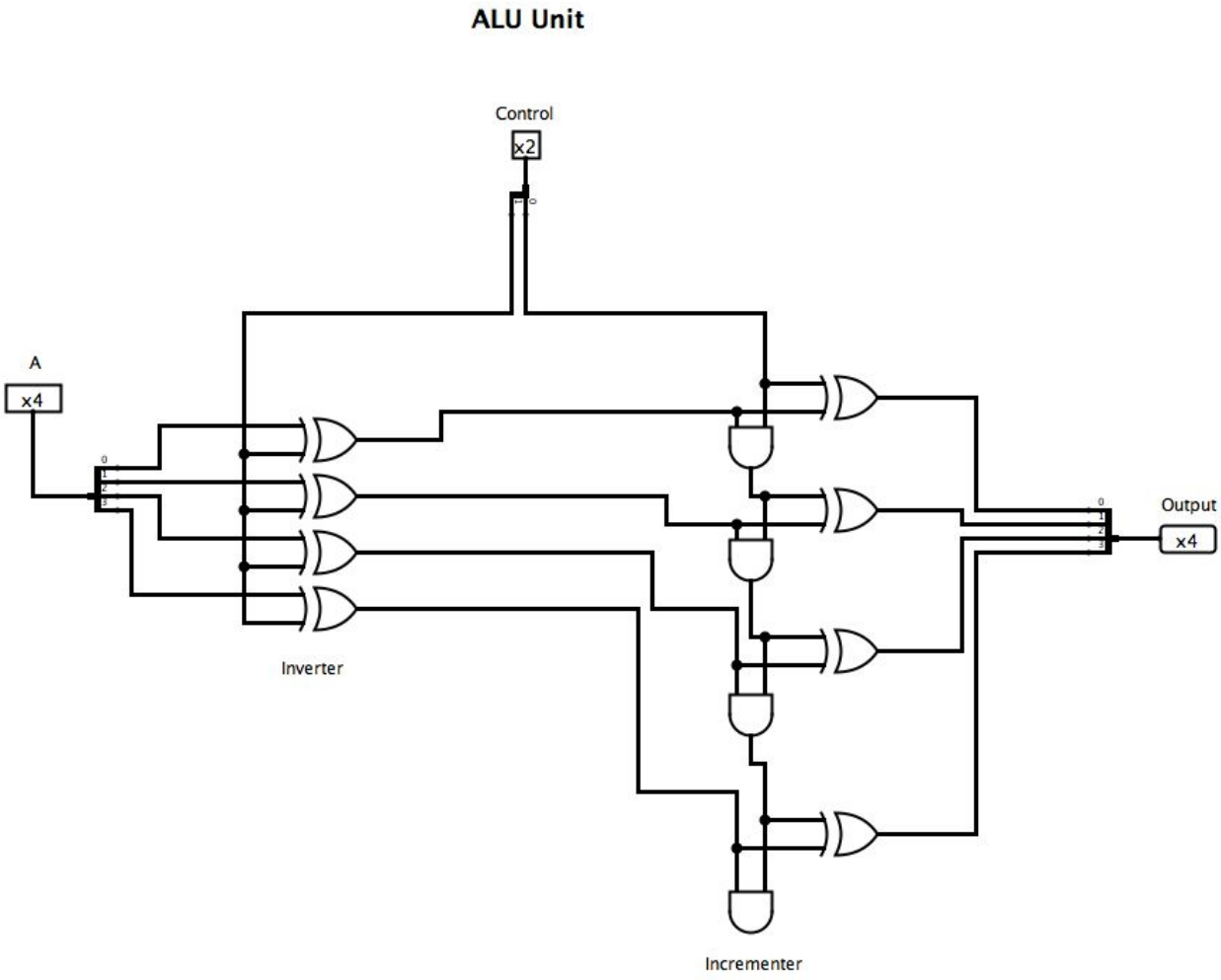


2.



ALU Circuit

1.



ALU Logic Table

A	**Control**	**Out**
k	00	k
k	01	~k
k	10	(k + 1) mod 2^8
k	11	~((k + 1) mod 2^8)