
Conv1_1
3 × 3 × 64
Stride: (1,1)
Pad: 1
ReLU
Conv1_2
3 × 3 × 64
Stride: (1,1)
Pad: 1
ReLU
Pool1
Max pooling
2 × 2
Stride: (2,2)
Conv2_1
3 × 3 × 128
Stride: (1,1)
Pad: 1
ReLU
Conv2_2
3 × 3 × 128
Stride: (1,1)
Pad: 1
ReLU
Pool2
Max pooling
2 × 2
Stride: (2,2)
Conv3_1
3 × 3 × 256
Stride: (1,1)
Pad: 1
ReLU
Conv3_2
3 × 3 × 256
Stride: (1,1)
Pad: 1
ReLU
Conv3_3
3 × 3 × 256
Stride: (1,1)
Pad: 1
ReLU
Pool3
Max pooling
2 × 2
Stride: (2,2)
Conv4_1
3 × 3 × 512
Stride: (1,1)
Pad: 1
ReLU
Conv4_2
3 × 3 × 512
Stride: (1,1)
Pad: 1
ReLU
Conv4_3
3 × 3 × 512
Stride: (1,1)
Pad: 1
ReLU
Pool4
Max pooling
2 × 2
Stride: (2,2)
Conv5_1
3 × 3 × 512
Stride: (1,1)
Pad: 1
ReLU
Conv5_2
3 × 3 × 512
Stride: (1,1)
Pad: 1
ReLU
Conv5_3
3 × 3 × 512
Stride: (1,1)
Pad: 1
ReLU
Pool5
Max pooling
3 × 3
Stride: (2,2)
FC6
1 × 1 × 4096
ReLU
FC7
1 × 1 × 4096
ReLU
FC8
1 × 1 × 1000
Softmax