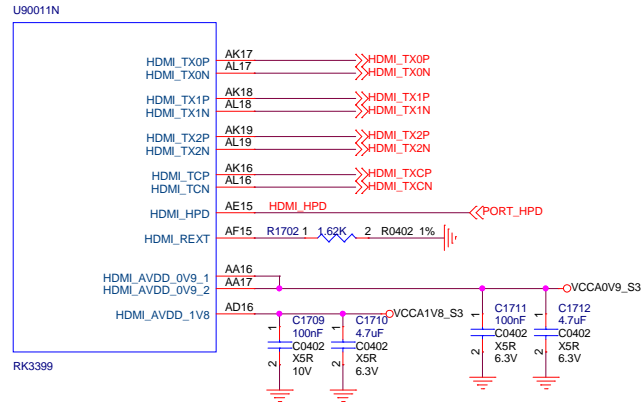
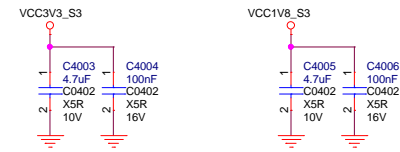

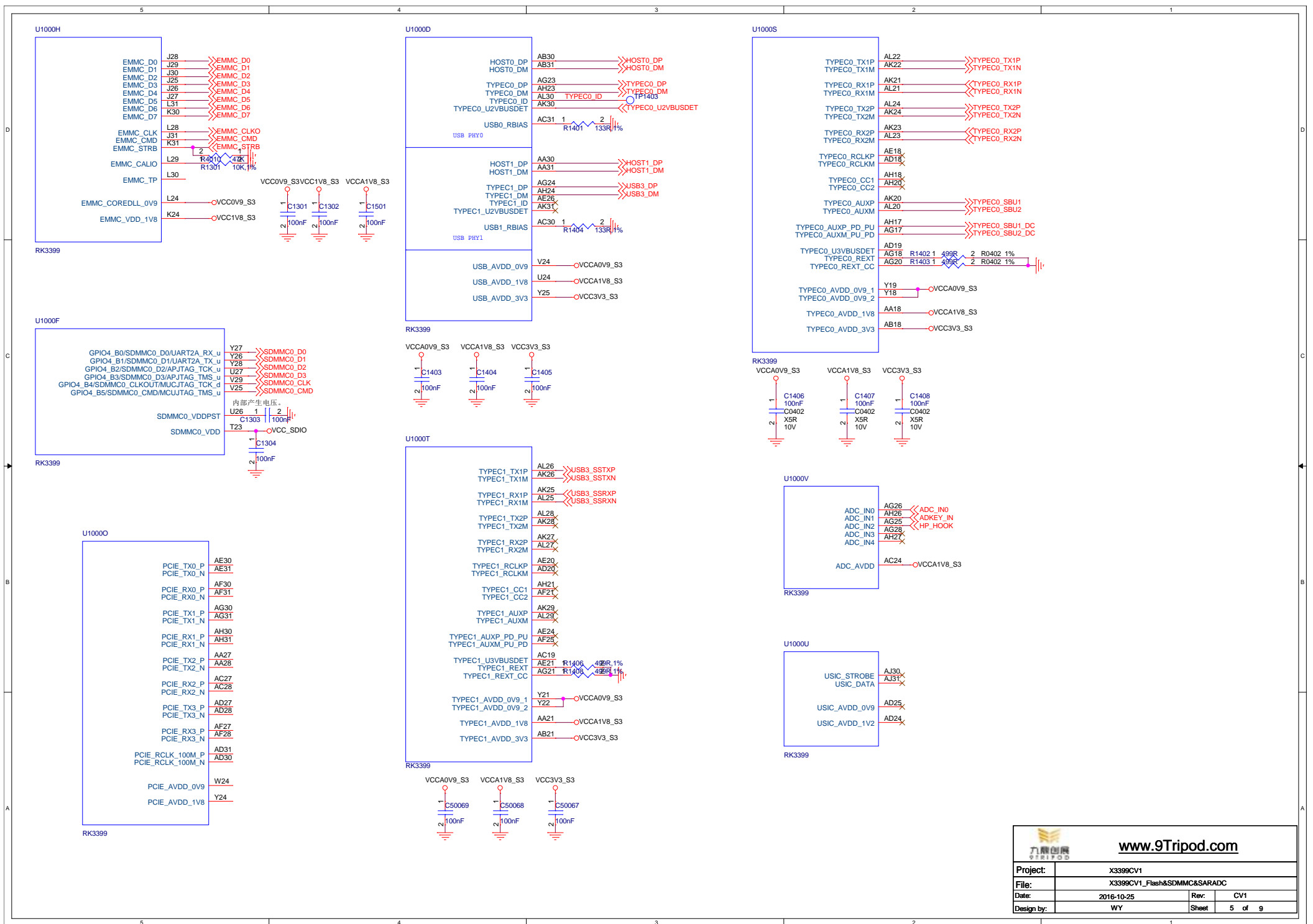


Note:
Reserve PAD for Update.

EMMC_D6 TP4004



| | | | |
|---|------------------------|--|--------|
|  | | www.9Tripod.com | |
| Project: | X3399CV1 | | |
| File: | X3399CV1_PMU cotroller | | |
| Date: | 2016-10-25 | Rev: | CV1 |
| Design by: | WY | Sheet | 3 of 9 |

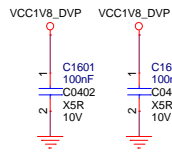


U1000L

GPIO2_A0/VOP_D0/CIF_D0/I2C2_SDA_u
 GPIO2_A1/VOP_D1/CIF_D1/I2C2_SCL_u
 GPIO2_A2/VOP_D2/CIF_D2_d
 GPIO2_A3/VOP_D3/CIF_D3_d
 GPIO2_A4/VOP_D4/CIF_D4_d
 GPIO2_A5/VOP_D5/CIF_D5_d
 GPIO2_A6/VOP_D6/CIF_D6_d
 GPIO2_A7/VOP_D7/CIF_D7/I2C7_SDA_u
 GPIO2_B0/VOP_CLK/CIF_VSYNC/I2C7_SCL_u
 GPIO2_B1/SPI2_RXD/CIF_HREF/I2C6_SDA_u
 GPIO2_B2/SPI2_TXD/CIF_CLKIN/I2C6_SCL_u
 GPIO2_B3/SPI2_CLK/VOP_DEN/CIF_CLKOUTA_u
 GPIO2_B4/SPI2_CSn0_u

APIO2_VDDPST
 APIO2_VDD

| func1 | func2 | func3 |
|-----------------------------|----------|----------|
| GPIO2_A0/CIF_D0 | | I2C2_SDA |
| GPIO2_A1/CIF_D1 | | I2C2_SCL |
| GPIO2_A2/CIF_D2 | | I2C7_SDA |
| GPIO2_A3/CIF_D3 | | I2C7_SCL |
| GPIO2_A4/CIF_D4 | | |
| GPIO2_A5/CIF_D5 | | |
| GPIO2_A6/CIF_D6 | | |
| GPIO2_A7/I2C7_SDA/CIF_D7 | | |
| GPIO2_B0/I2C7_SCL/CIF_VSYNC | | |
| GPIO2_B1/I2C6_SDA/CIF_HREF | | |
| GPIO2_B2/I2C6_SCL/CIF_CLKI | | |
| GPIO2_B3/CIF_CLKO | SPI2_CLK | |
| GPIO2_B4/DVP_PDN0_H | SPI2_CSN | |



RK3399

U1000M

EDP_TX0P B29 EDPTX0P C1701 1 2 100nF C0402 X5R 10V
 EDP_TX0N A29 EDPTX0N C1702 1 2 100nF C0402 X5R 10V
 EDP_TX1P B30 EDPTX1P C1703 1 2 100nF C0402 X5R 10V
 EDP_TX1N A30 EDPTX1N C1704 1 2 100nF C0402 X5R 10V
 EDP_TX2P C30 EDPTX2P C1705 1 2 100nF C0402 X5R 10V
 EDP_TX2N C31 EDPTX2N C1706 1 2 100nF C0402 X5R 10V
 EDP_TX3P D30 EDPTX3P C1707 1 2 100nF C0402 X5R 10V
 EDP_TX3N D31 EDPTX3N C1708 1 2 100nF C0402 X5R 10V

EDP_AUXP B28 EDPAUXP
 EDP_AUXN A28 EDPAUXN

EDP_DC_TP G20
 EDP_CLK24M_IN H21

EDP_REXT G21 1 R1703 6.04K 2 R0402 1%

EDP_AVDD_0V9 H20
 EDP_AVDD_1V8_1 J19
 EDP_AVDD_1V8_2 J20

EDP_AVSS_1 C28
 EDP_AVSS_2 C29
 EDP_AVSS_3 D29
 EDP_AVSS_4 H19
 EDP_AVSS_5 J21
 EDP_AVSS_6

C1713 100nF
 C0402 X5R 10V
 C1714 100nF
 C0402 X5R 10V

VCC0V9_S3
 VCC1V8_S3

VCC1V8_DVP
 VCC1V8_DVP

VCC1V8_DVP
 VCC1V8_DVP

VCC1V8_DVP
 VCC1V8_DVP

VCC1V8_DVP
 VCC1V8_DVP

VCC1V8_DVP
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VCC1V8_DVP
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VCC1V8_DVP
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VCC1V8_DVP
 VCC1V8_DVP

VCC1V8_DVP
 VCC1V8_DVP

VCC1V8_DVP
 VCC1V8_DVP

VCC1V8_DVP
 VCC1V8_DVP

VCC1V8_DVP
 VCC1V8_DVP

VCC1V8_DVP
 VCC1V8_DVP

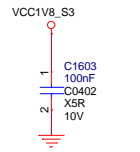
VCC1V8_DVP
 VCC1V8_DVP

VCC1V8_DVP
 VCC1V8_DVP

VCC1V8_DVP
 VCC1V8_DVP

U1000R

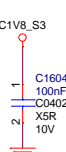
MIPI_RX0_D0P AK15
 MIPI_RX0_D0N AL15
 MIPI_RX0_D1P AK14
 MIPI_RX0_D1N AL14
 MIPI_RX0_CLKP AK13
 MIPI_RX0_CLKN AL13
 MIPI_RX0_D2P AK12
 MIPI_RX0_D2N AL12
 MIPI_RX0_D3P AK11
 MIPI_RX0_D3N AL11
 MIPI_RX0_REXT AF14 R1605 1 4.02K 2 R0402 1%
 MIPI_RX0_AVDD_1V8 AB14



RK3399

U1000P

MIPI_TX1/RX1_D0P AK6
 MIPI_TX1/RX1_D0N AL6
 MIPI_TX1/RX1_D1P AK7
 MIPI_TX1/RX1_D1N AL7
 MIPI_TX1/RX1_CLKP AK8
 MIPI_TX1/RX1_CLKN AL8
 MIPI_TX1/RX1_D2P AK9
 MIPI_TX1/RX1_D2N AL9
 MIPI_TX1/RX1_D3P AK10
 MIPI_TX1/RX1_D3N AL10
 MIPI_TX1/RX1_REXT AF11 R1606 1 4.02K 2 R0402 1%
 MIPI_TX1/RX1_AVDD_1V8 AC10

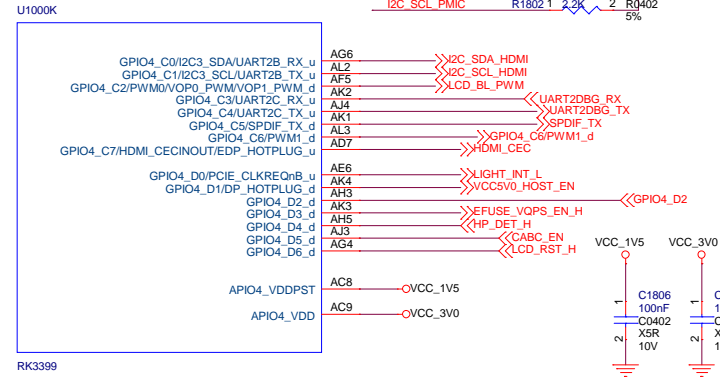
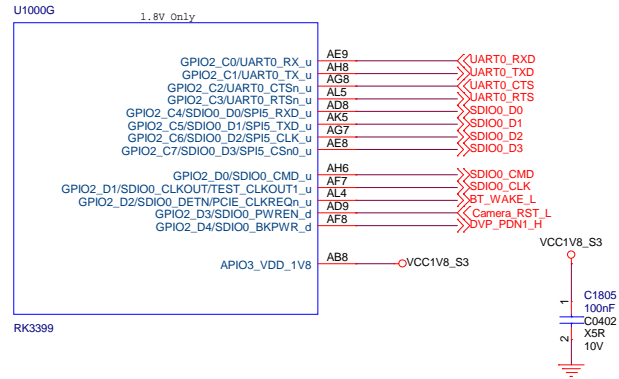
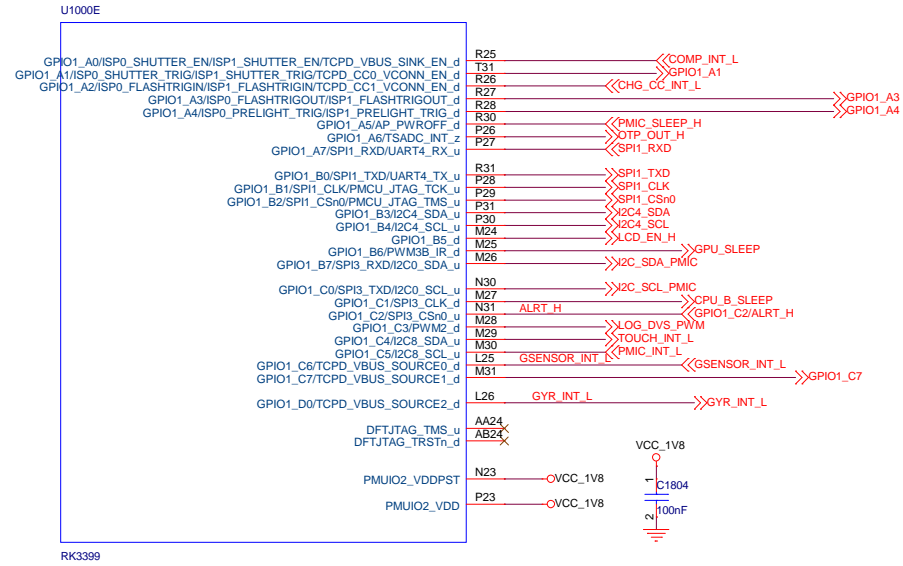
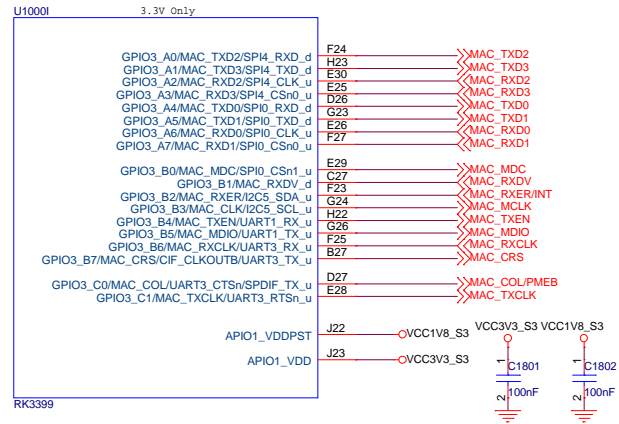


RK3399


U1000Q

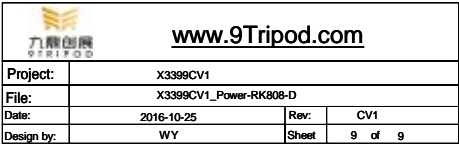
MIPI_TX0_D0P AG15
 MIPI_TX0_D0N AH15
 MIPI_TX0_D1P AG14
 MIPI_TX0_D1N AH14
 MIPI_TX0_CLKP AG12
 MIPI_TX0_CLKN AH12
 MIPI_TX0_D2P AG11
 MIPI_TX0_D2N AH11
 MIPI_TX0_D3P AG9
 MIPI_TX0_D3N AH9
 MIPI_TX0_REXT AF12 R1704 1 4.02K 2 R0402 1%
 MIPI_TX0_AVDD_1V8 AB12

RK3399



| | |
|-----------|--|
| 1.8V Only | VDDPST=VDDIO=1.8V |
| 3.3V Only | VDDPST=1.8V, VDDIO=3.3V |
| other | 3.0V mode: VDDPST=1.5V, VDDIO=3.0V 1.8V mode: VDDPST=1.8V, VDDIO=1.8V |

| | | | |
|---|---------------|------------------------|--------|
|  | | <u>www.9Tripod.com</u> | |
| Project: | X3399CV1 | | |
| File: | X3399CV1_GPIO | | |
| Date: | 2016-10-25 | Rev: | CV1 |
| Design by: | WY | Sheet | 7 of 9 |



LPDDR3

U3600
K4E8E304EE-EGCE
FBGA178_13R00X11R50X1R20

U3601
K4E8E304EE-EGCE
FBGA178_13R00X11R50X1R20

DDR_VREFCA

DDR_VREFDQ

DDR_VREFCA

DDR_VREFDQ

| | | | |
|---|------------------------|-------|--------|
|  | | | |
| Project: | X3399CV1 | | |
| File: | X3399CV1_Power-RK808-D | | |
| Date: | 2016-10-25 | Rev: | CV1 |
| Design by: | WY | Sheet | 8 of 9 |