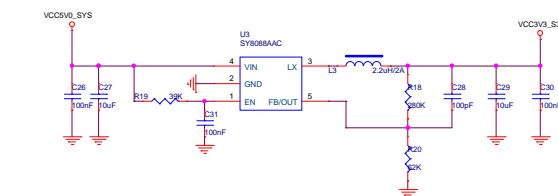
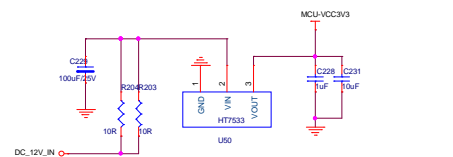
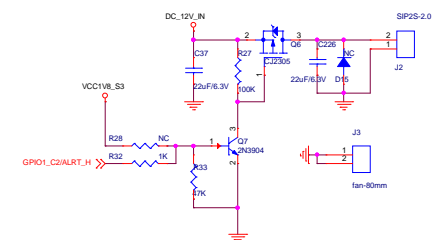
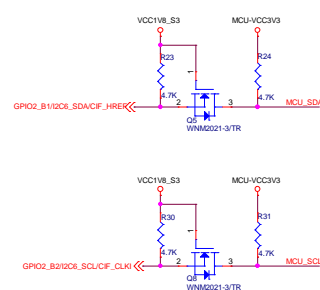
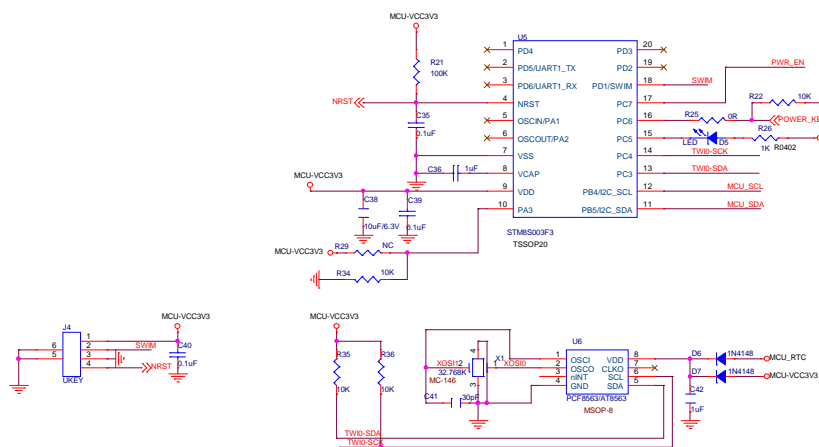


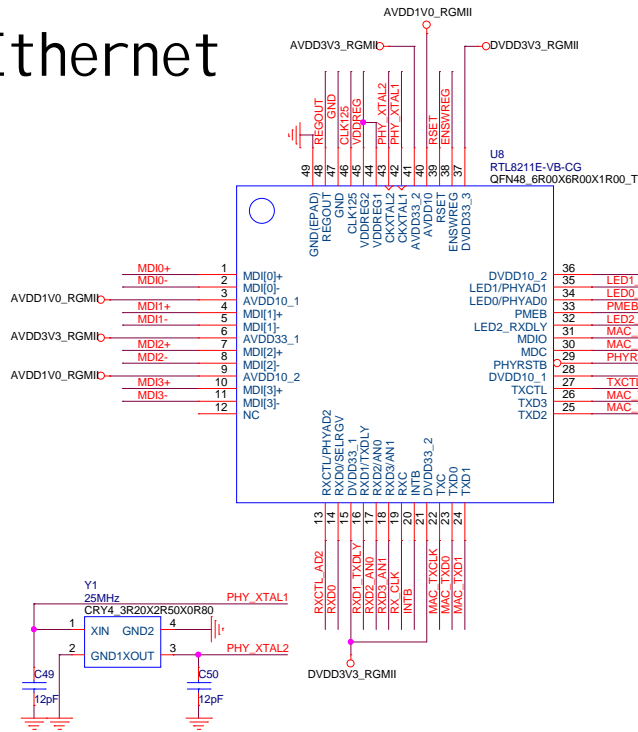
MCU



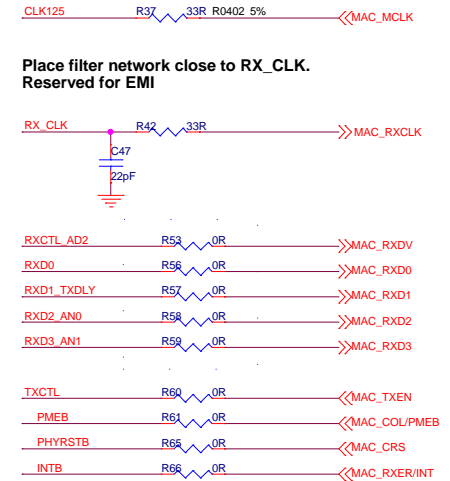
FAN



Ethernet

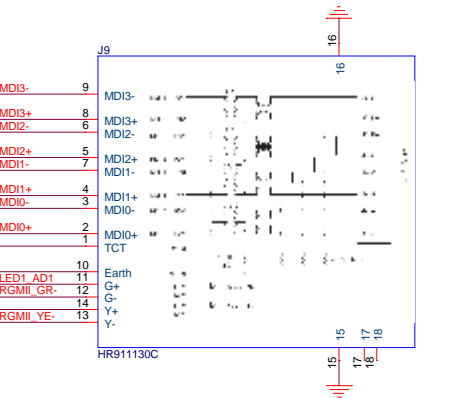


R61,R62: Config for all capability
R51,R54,R148: PHY Address=001(RTL8211E)
R64,R50: Without TX/RX Delay

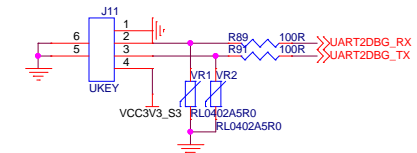


Note 1: The Trace length between L1 and PHY's Pin48 must be within 0.5 cm. C35 and C36 to L1 must be within 0.5cm. Any inductance or bead except for L1 is not allowed on the path from REGOUT to DVDD10/AVDD10.

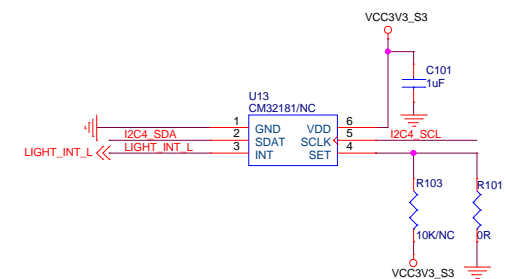
Note 2: The Trace length from C56, C57 to Pin 44,45(VDDREG) must be within 0.5 cm. The trace width from AVDD33 to Pin 44,45 should>40mils



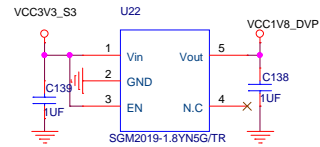
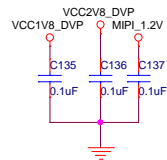
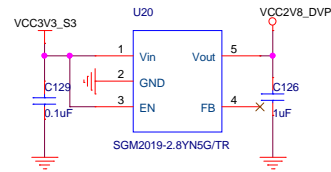
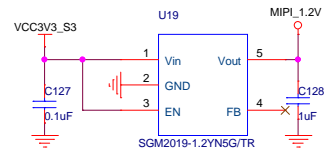
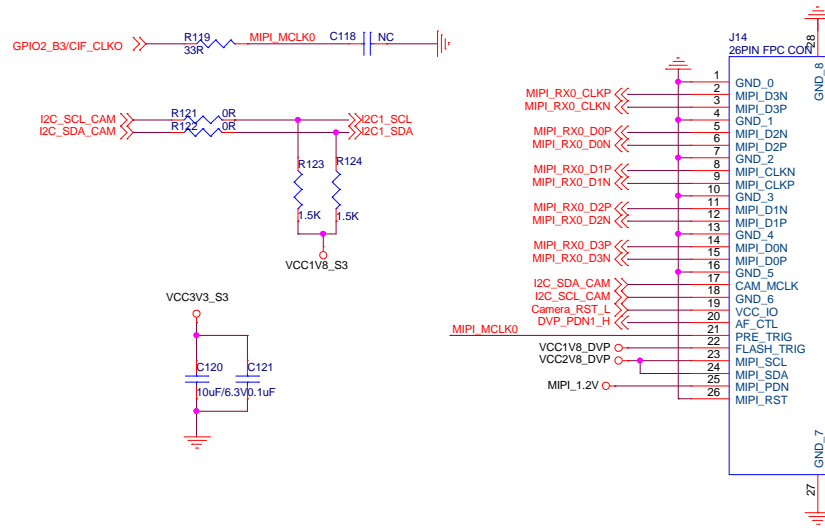
UART&DEBUG



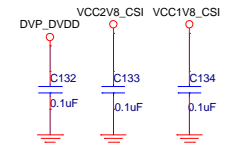
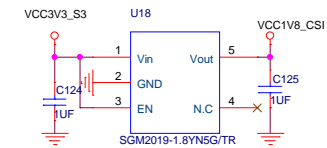
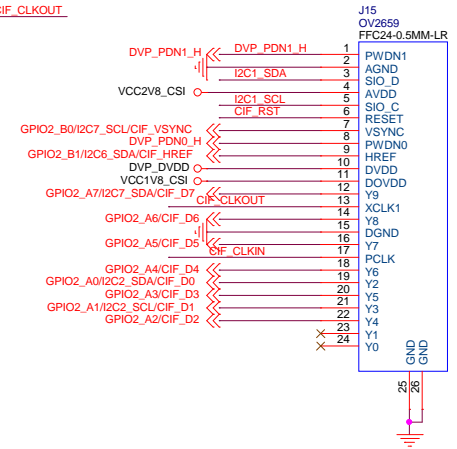
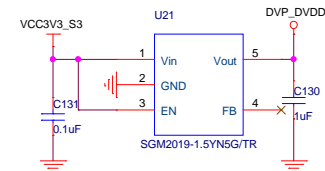
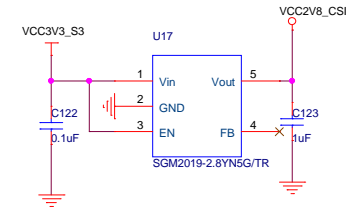
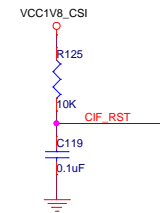
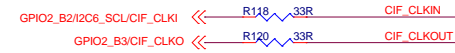
LIGHT-Sensor



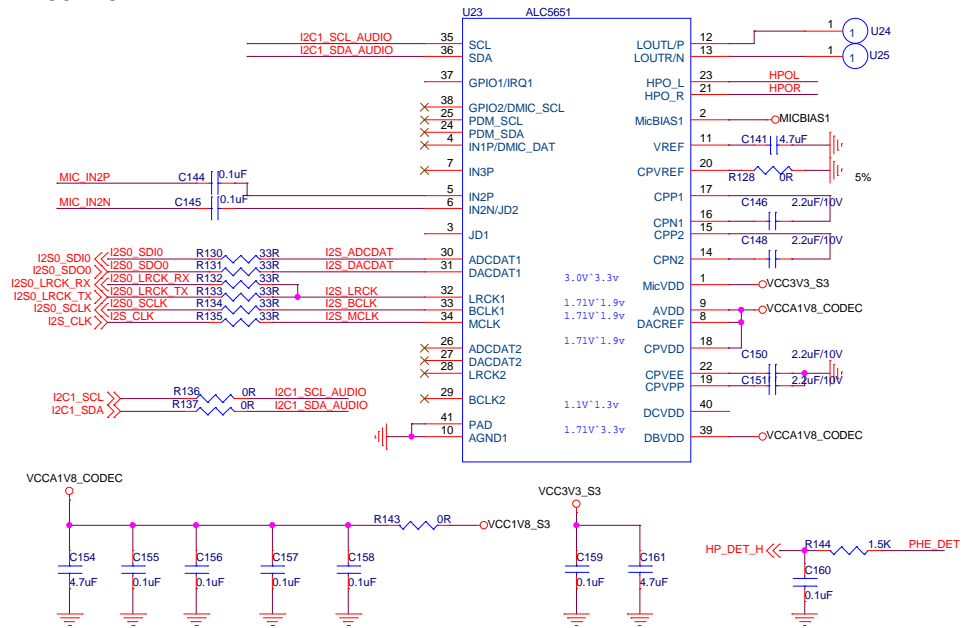
MIPI Camera



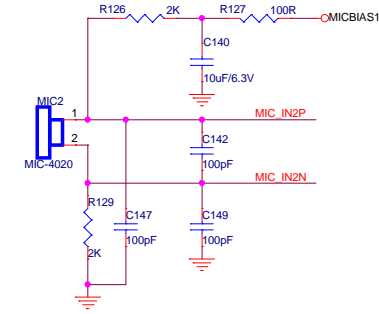
CIF Camera



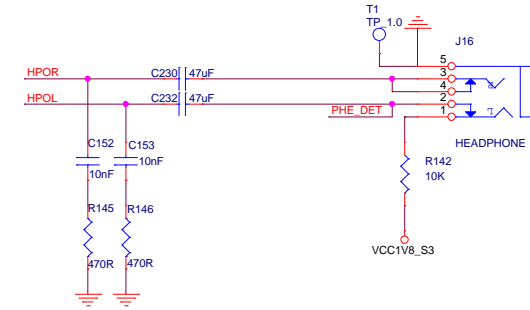
CODEC



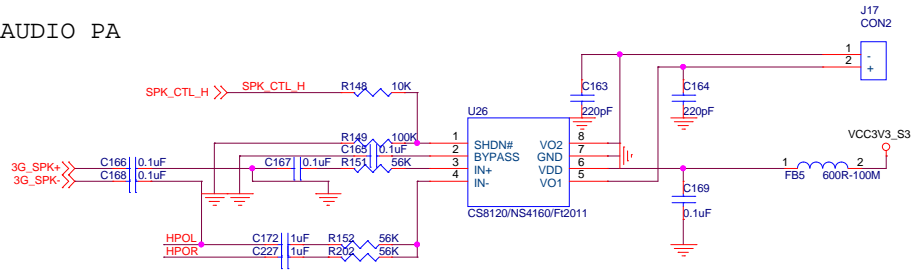
MIC



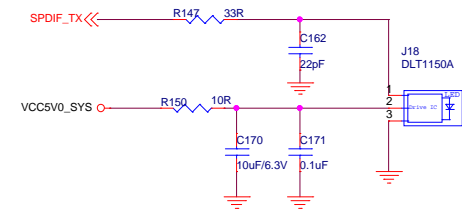
HEADPHONE



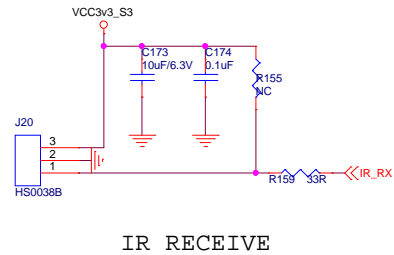
AUDIO PA



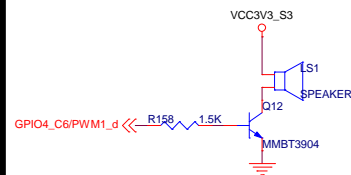
SPDIF OUT



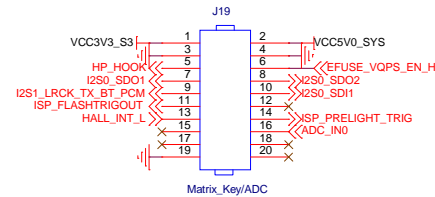
IrDA



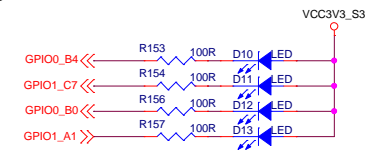
BUZZER



GPIO



LED



	
www.9tripod.com	
Project:	X3399BV4
File:	AUDIO/SPDIF/IR/LED/GPIOBUZZER
Date:	2017-03-31
Design by:	WY
Rev:	2
Sheet	7 of 9

[illegible]

USB2.0 Host

VCC5V0_USB

J22

1 VBUS

2 D-

3 D+

4 GND

5

6

HOST0_DM

HOST0_DP

USB_Host

[illegible]

KEY

ADKEY_IN << ADKEY_IN

R178 10K

VCC1V8_S3

R179 10R

R180 2K

R182 3.3K

R183 6.8K

R184 15K

VOL+RECOVERY

VOL-

ESC

MENU

SW1

SW2

SW3

SW4

SW5

SW6

C197 0.1uF

C201 0.1uF

POWER_KEY << POWER

PMU_RESET << RST

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Project:	X3399BV4		
File:	TYPECUSBSDKEY		
Date:	2017-03-31	Rev:	2
Design by:	WY	Sheet	8 of 9

WIFI/WIFI ac/BT MODULE

