

# Integrated Circuit Design

## Homework #1

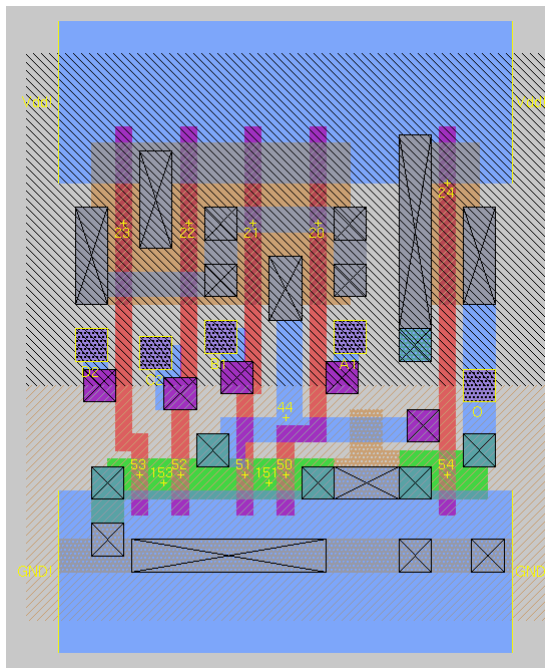
### Layout Reading

***Deadline: Sep. 27***

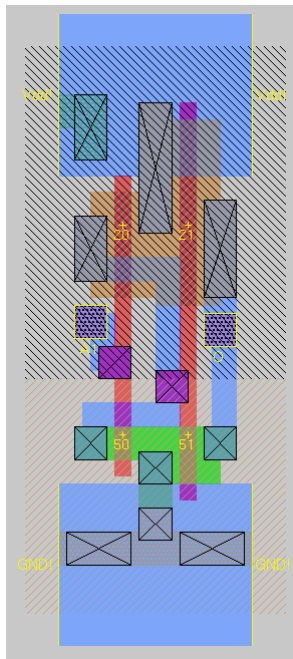
For the following layouts, please

- (a) Draw the associated sticky diagram.
- (b) Draw the circuit in transistor level.
- (c) Describe the function of the circuits. (You can use Boolean equations or other descriptions)

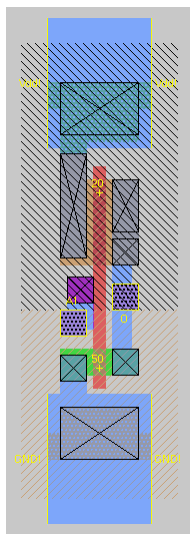
1.



2.

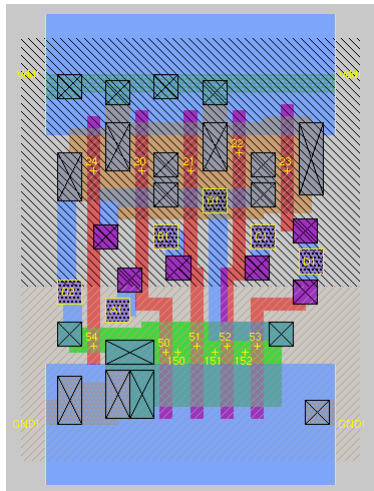


3.

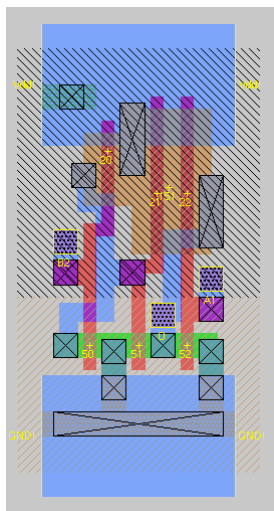




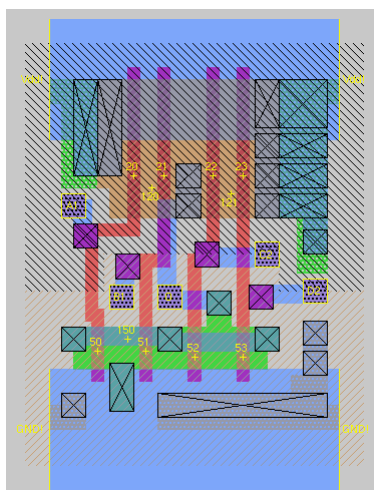
7.



8.



9.



10.

