# William Zhang

#### Education

2018–2022 University of Virginia, B.S., Computer Science & Electrical Engineering, GPA: 3.73.

**Curriculum:** FPGA Design, Artificial Intelligence, Machine Learning, Real-Time Embedded Systems, Operating Systems, Algorithms, Computer Architecture Design, Linear Algebra, Ordinary Differential Equations, Probability

2014–2018 Thomas Jefferson High School for Science and Technology, GPA: 4.27.

### Experience

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2021-Present **Embedded Software Intern**, *Rivian Automotive, Inc.*, Irvine, CA.

2018–Present **Software and Systems Engineering Intern**, *Lancium LLC*, Houston, TX.

- Developed software and infrastructure to support novel distributed, power-aware datacenter technology.

  O Collaborated with team to design consumer-facing REST API for job, container, data, and computing resource management.
- o Co-designed reference Python client library to consume the REST API with another intern.
- o Implemented customer-facing command-line interface (CLI) in Python utilizing the client library and the Click framework.
- o Built testing harnesses and mocking framework for the client library and CLI.
- o Deployed HPC clusters and infrastructure (DNS, ZFS, SLURM) with Ansible.
- o Designed site network architecture.

2018-Present Electrical Team Advisor, Solar Car at UVA, Charlottesville, VA.

Led development of embedded software and hardware for various vehicle subsystems.

- o Created an Arduino library for interfacing with the Kelly KLS brushless DC (BLDC) motor controllers over CAN.
- o Implemented a vehicle dashboard interface using JavaScript, Socket.IO, and Flask.
- o Led embedded systems development and documentation for ARM mbedOS on STM32 microcontrollers.
- o Contributed to vehicle subsystem PCB design in KiCad.
- 2018–2019 Distributed Systems Research Assistant, UVA Dept. of Computer Science, Charlottesville, VA.

Created tools for the GenesisII grid computing platform under Dr. Andrew Grimshaw.

- 2018 Network Engineering Intern, Cypress Consulting, Arlington, VA.
  - Wrote Python scripts for Juniper Junos automation using NetJSON, lxml, and the Juniper pyEZ library.
  - o Simulated virtual IS-IS and OSPF networks from scratch with Juniper vMX and vSRX VMs in KVM/QEMU.

## Projects

2020 **Modular EV BMS**, *UVA ECE Capstone Project*, github.com/willzhang05/modular-ev-bms.

Capstone project with four other students developing a software and board design prototype for a modular battery management system for use in an electric vehicle. Consists of main node and an arbitrary number of cell nodes attached to sets of 18650 batteries

2020 **Shop With Space**, *HooHacks 2020: Prize for Best Health Hack*, devpost.com/software/shop-with-space.

Hackathon project with three other students developing an app to promote social distancing in the COVID-19 pandemic by helping consumers avoid large crowds in stores. Dynamically generated safety ratings based on nearby traffic and location popularity data from Google Maps and TomTom APIs.

2019 KLS Motor Controller Library, Solar Car at UVA, github.com/solarcaratuva/KLS.

Arduino library for interfacing Kelly KLS brushless DC (BLDC) motor controllers over a CAN bus.

2019 **Solar Car TestBench**, *Solar Car at UVA*, github.com/solarcaratuva/TestBench.

Proof-of-concept test bench and dashboard to visualize vehicle status. Interfaces over a serial port with the ECU and uses Flask and socket.io to relay JSON payloads to a web front end.

2018 **Public AMT Relay**, *TJHSST Senior Research Project*, github.com/willzhang05/senior-research.

Deployment of first known public Automatic Multicast Tunneling (AMT) relay on the Multicast backbone (MBONE) to help promote adoption of inter-domain multicast. Presented at IETF 101 in the MBONED-PIM working group.

#### Skills

Programming Python, C++, C, MATLAB, Rust, Go, Java

Misc. Numpy, Keras, Libvirt, SLURM, VHDL

Tools Git, Shell, Ansible, Docker, Jupyter, KiCad

Web HTML5, JavaScript, Flask, Django

Systems Linux, FreeBSD, Juniper Junos, RTOS

Languages English, Chinese, German (B2)