



1. Description

1.1. Project

| | |
|-----------------|-------------------|
| Project Name | human_arm_mcu |
| Board Name | custom |
| Generated with: | STM32CubeMX 6.9.2 |
| Date | 11/16/2023 |

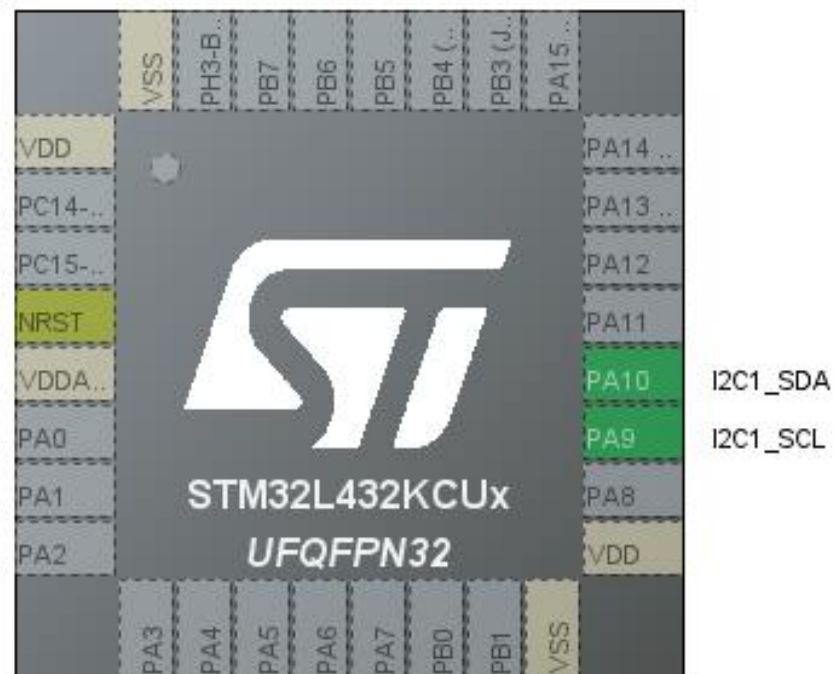
1.2. MCU

| | |
|----------------|---------------|
| MCU Series | STM32L4 |
| MCU Line | STM32L4x2 |
| MCU name | STM32L432KCUx |
| MCU Package | UFQFPN32 |
| MCU Pin number | 32 |

1.3. Core(s) information

| | |
|---------|---------------|
| Core(s) | Arm Cortex-M4 |
|---------|---------------|

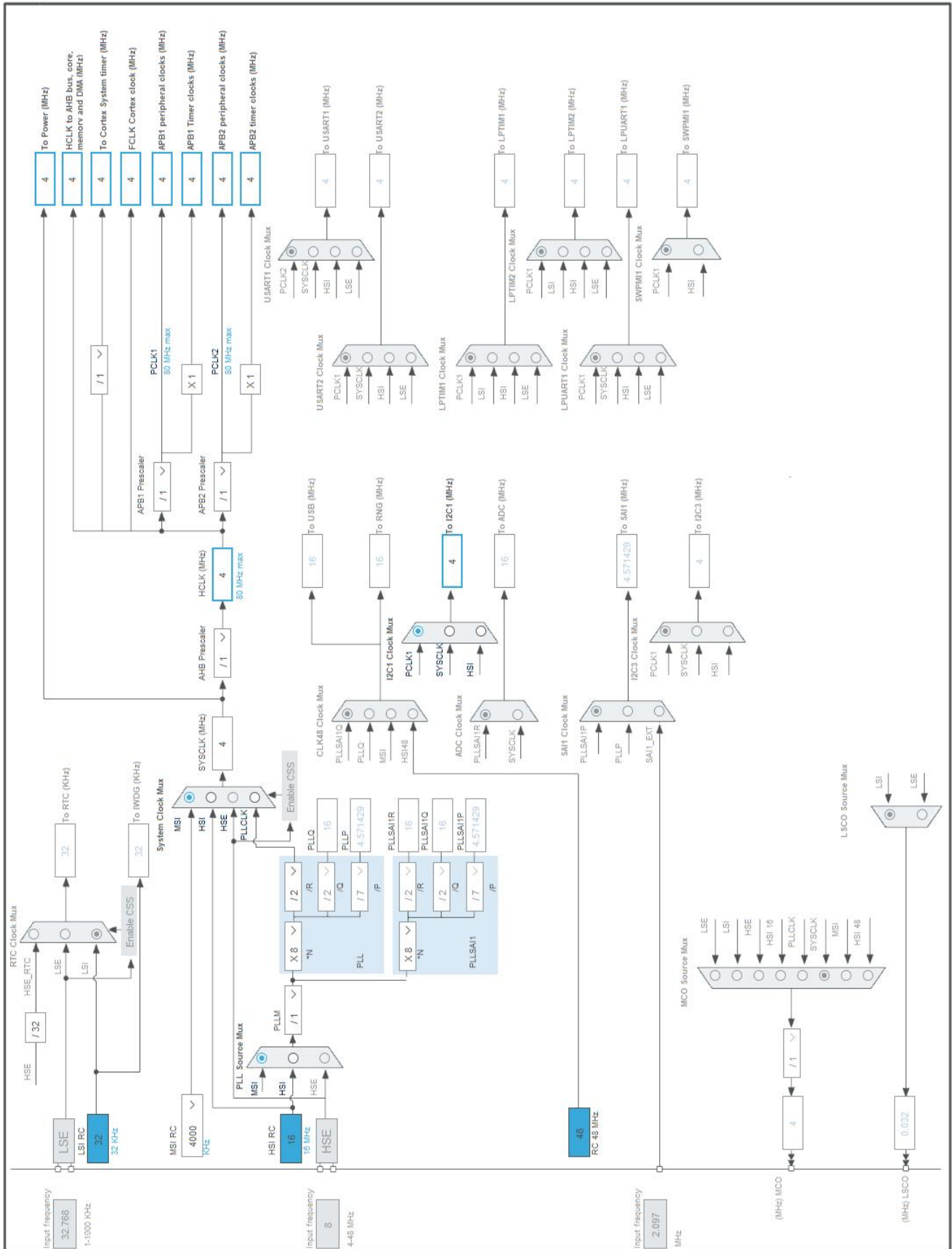
2. Pinout Configuration



3. Pins Configuration

| Pin Number UFQFPN32 | Pin Name (function after reset) | Pin Type | Alternate Function(s) | Label |
|------------------------|---------------------------------------|----------|--------------------------|-------|
| 1 | VDD | Power | | |
| 4 | NRST | Reset | | |
| 5 | VDDA/VREF+ | Power | | |
| 16 | VSS | Power | | |
| 17 | VDD | Power | | |
| 19 | PA9 | I/O | I2C1_SCL | |
| 20 | PA10 | I/O | I2C1_SDA | |
| 32 | VSS | Power | | |

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

| Name | Value |
|-----------------------------------|--|
| Project Name | human_arm_mcu |
| Project Folder | C:\Users\Sashr\STM32CubeIDE\workspace_1.13.2\human_arm_mcu |
| Toolchain / IDE | STM32CubeIDE |
| Firmware Package Name and Version | STM32Cube FW_L4 V1.18.0 |
| Application Structure | Advanced |
| Generate Under Root | Yes |
| Do not generate the main() | No |
| Minimum Heap Size | 0x200 |
| Minimum Stack Size | 0x400 |

5.2. Code Generation Settings

| Name | Value |
|---|---------------------------------------|
| STM32Cube MCU packages and embedded software | Copy only the necessary library files |
| Generate peripheral initialization as a pair of '.c/.h' files | No |
| Backup previously generated files when re-generating | No |
| Keep User Code when re-generating | Yes |
| Delete previously generated files when not re-generated | Yes |
| Set all free pins as analog (to optimize the power consumption) | No |
| Enable Full Assert | No |

5.3. Advanced Settings - Generated Function Calls

| Rank | Function Name | Peripheral Instance Name |
|------|--------------------|--------------------------|
| 1 | SystemClock_Config | RCC |
| 2 | MX_GPIO_Init | GPIO |
| 3 | MX_I2C1_Init | I2C1 |

1. Power Consumption Calculator report

1.1. Microcontroller Selection

| | |
|-----------|---------------|
| Series | STM32L4 |
| Line | STM32L4x2 |
| MCU | STM32L432KCUx |
| Datasheet | DS11451_Rev2 |

1.2. Parameter Selection

| | |
|-------------|-----|
| Temperature | 25 |
| Vdd | 3.0 |

1.3. Battery Selection

| | |
|-------------------|-----------------|
| Battery | Li-SOCL2(A3400) |
| Capacity | 3400.0 mAh |
| Self Discharge | 0.08 %/month |
| Nominal Voltage | 3.6 V |
| Max Cont Current | 100.0 mA |
| Max Pulse Current | 200.0 mA |
| Cells in series | 1 |
| Cells in parallel | 1 |

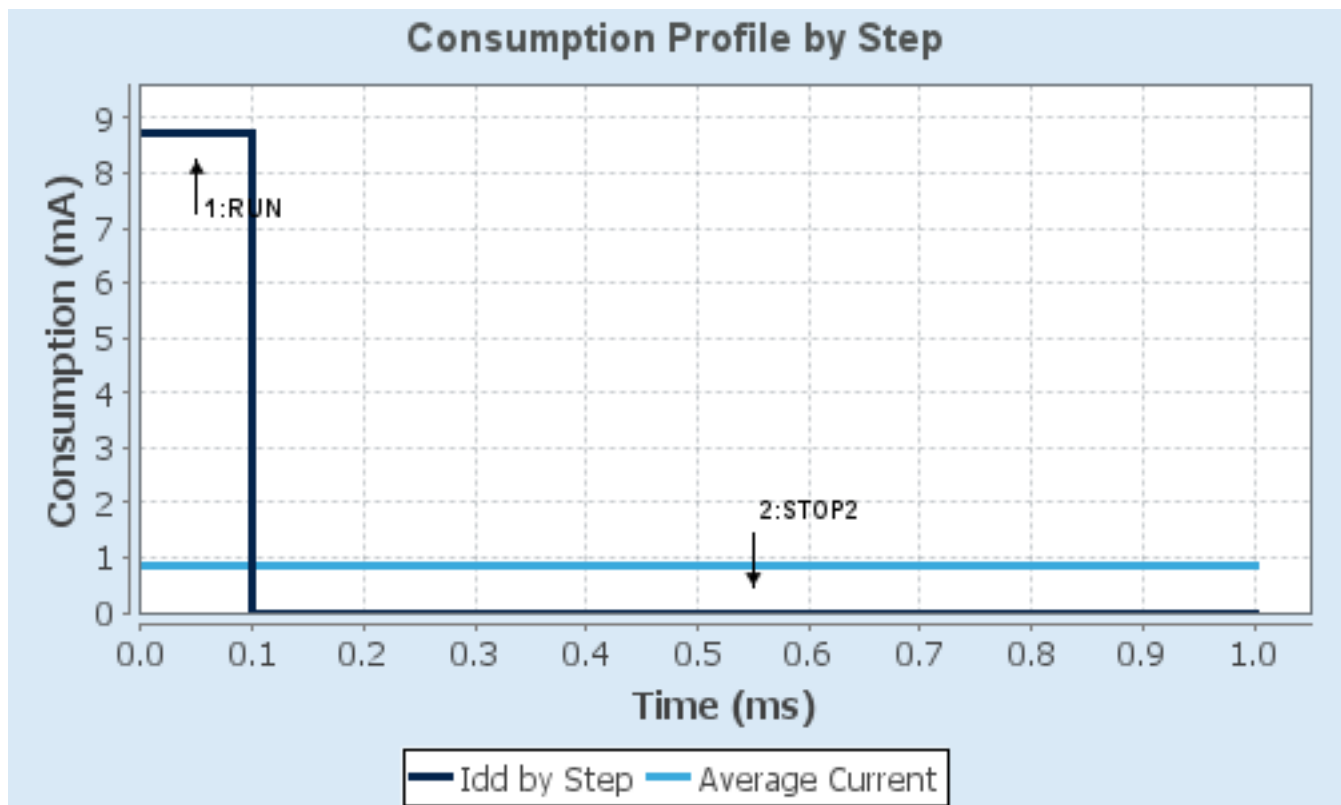
1.4. Sequence

| | | |
|-------------------------------|-------------|----------------|
| Step | Step1 | Step2 |
| Mode | RUN | STOP2 |
| Vdd | 3.0 | 3.0 |
| Voltage Source | Battery | Battery |
| Range | Range1-High | NoRange |
| Fetch Type | SRAM2 | n/a |
| CPU Frequency | 80 MHz | 0 Hz |
| Clock Configuration | HSE BYP PLL | ALL CLOCKS OFF |
| Clock Source Frequency | 4 MHz | 0 Hz |
| Peripherals | | |
| Additional Cons. | 0 mA | 0 mA |
| Average Current | 8.71 mA | 1.06 μ A |
| Duration | 0.1 ms | 0.9 ms |
| DMIPS | 100.0 | 0.0 |
| Ta Max | 103.98 | 105 |
| Category | In DS Table | In DS Table |

1.5. Results

| | | | |
|---------------|-------------------------------|-----------------|----------------|
| Sequence Time | 1 ms | Average Current | 871.95 μ A |
| Battery Life | 5 months, 9 days, 16 hours | Average DMIPS | 100.0 DMIPS |

1.6. Chart



2. Peripherals and Middlewares Configuration

2.1. I2C1

I2C: I2C

2.1.1. Parameter Settings:

Timing configuration:

| | |
|-------------------------------|---------------|
| Custom Timing | Disabled |
| I2C Speed Mode | Standard Mode |
| I2C Speed Frequency (KHz) | 100 |
| Rise Time (ns) | 0 |
| Fall Time (ns) | 0 |
| Coefficient of Digital Filter | 0 |
| Analog Filter | Enabled |
| Timing | 0x00000E14 |

Slave Features:

| | |
|----------------------------------|----------|
| Clock No Stretch Mode | Disabled |
| General Call Address Detection | Disabled |
| Primary Address Length selection | 7-bit |
| Dual Address Acknowledged | Disabled |
| Primary slave address | 0 |

2.2. RCC

2.2.1. Parameter Settings:

System Parameters:

| | |
|-------------------|--------------------|
| VDD voltage (V) | 3.3 |
| Instruction Cache | Enabled |
| Prefetch Buffer | Disabled |
| Data Cache | Enabled |
| Flash Latency(WS) | 0 WS (1 CPU cycle) |

RCC Parameters:

| | |
|--------------------------------|----------|
| HSI Calibration Value | 16 |
| MSI Calibration Value | 0 |
| MSI Auto Calibration | Disabled |
| HSE Startup Timeout Value (ms) | 100 |
| LSE Startup Timeout Value (ms) | 5000 |

Power Parameters:

| | |
|-------------------------------|---------------------------------|
| Power Regulator Voltage Scale | Power Regulator Voltage Scale 1 |
|-------------------------------|---------------------------------|

*** User modified value**

3. System Configuration

3.1. GPIO configuration

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull down | Max Speed | User Label |
|------|------|----------|-------------------------------|-----------------------------|-----------------------|------------|
| I2C1 | PA9 | I2C1_SCL | Alternate Function Open Drain | No pull-up and no pull-down | Very High * | |
| | PA10 | I2C1_SDA | Alternate Function Open Drain | No pull-up and no pull-down | Very High * | |

3.2. DMA configuration

nothing configured in DMA service

3.3. NVIC configuration

3.3.1. NVIC

| Interrupt Table | Enable | Preenmption Priority | SubPriority |
|--|--------|----------------------|-------------|
| Non maskable interrupt | true | 0 | 0 |
| Hard fault interrupt | true | 0 | 0 |
| Memory management fault | true | 0 | 0 |
| Prefetch fault, memory access fault | true | 0 | 0 |
| Undefined instruction or illegal state | true | 0 | 0 |
| System service call via SWI instruction | true | 0 | 0 |
| Debug monitor | true | 0 | 0 |
| Pendable request for system service | true | 0 | 0 |
| System tick timer | true | 15 | 0 |
| PVD/PVM1/PVM2/PVM3/PVM4 interrupts through EXTI lines 16/35/36/37/38 | unused | | |
| Flash global interrupt | unused | | |
| RCC global interrupt | unused | | |
| I2C1 event interrupt | unused | | |
| I2C1 error interrupt | unused | | |
| FPU global interrupt | unused | | |

3.3.2. NVIC Code generation

| Enabled interrupt Table | Select for init sequence ordering | Generate IRQ handler | Call HAL handler |
|---|-----------------------------------|----------------------|------------------|
| Non maskable interrupt | false | true | false |
| Hard fault interrupt | false | true | false |
| Memory management fault | false | true | false |
| Prefetch fault, memory access fault | false | true | false |
| Undefined instruction or illegal state | false | true | false |
| System service call via SWI instruction | false | true | false |
| Debug monitor | false | true | false |
| Pendable request for system service | false | true | false |
| System tick timer | false | true | true |

* User modified value

4. System Views

4.1. Category view

4.1.1. Current

Middleware

System Core

Analog

Timers

Connectivity

Multimedia

Security

Computing

DMA

I2C1 

GPIO 

NVIC 

RCC 

5. Docs & Resources

| Type | Link |
|-------------------------|---|
| BSDL files | https://www.st.com/resource/en/bsdl_model/stm32l4_bsd1.zip |
| IBIS models | https://www.st.com/resource/en/ibis_model/stm32l4_ibis.zip |
| System View Description | https://www.st.com/resource/en/svd/stm32l4_svd.zip |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-stm8_functional-safety-packages.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32l4_marketing-pres.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf |
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| Brochures | https://www.st.com/resource/en/brochure/brstm32ulp.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstm32l4.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstm32nucleo.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstmcsuite.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstm32trust.pdf |
| Magazine Articles | https://www.st.com/resource/en/magazine/design-elektronik_august2017.pdf |
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| Product Certifications | https://www.st.com/resource/en/certification_document/sesip-2000002-01-cert.pdf |
| Product Certifications | https://www.st.com/resource/en/certification_document/sesip-2000002-01-st2.pdf |

| | |
|-------------------|---|
| Product | https://www.st.com/resource/en/certification_document/psa-certificate_stm32l4.pdf |
| Certifications | |
| Application Notes | https://www.st.com/resource/en/application_note/an1181-electrostatic-discharge-sensitivity-measurement-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an2834-how-to-get-the-best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an2867-oscillator-design-guide-for-stm8afals-stm32-mcus-and-mpus-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an3154-can-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an3155-uart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an3156-usb-dfu-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an3236-increase-the-number-of-touchkeys-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an3960-esd-considerations-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an4013-stm32-crossseries-timer-overview-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an4221-i2c-protocol- |

used-in-the-stm32-bootloader-stmicroelectronics.pdf

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Application Notes https://www.st.com/resource/en/application_note/an4299-improve-conducted-noise-robustness-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4310-sampling-capacitor-selection-guide-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4312-design-with-surface-sensors-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf

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Application Notes https://www.st.com/resource/en/application_note/an4555-getting-started-with-stm32l4-series-and-stm32l4-series-hardware-development-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4566-extending-the-dac-performance-of-stm32-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4612-migrating-from-stm32l1-series-to-stm32l4-series-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4616-migrating-from-stm32f401-and-stm32f411-lines-to-stm32l4-series-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4621-stm32l4-and-stm32l4-ultralowpower-features-overview-stmicroelectronics.pdf

- Application Notes https://www.st.com/resource/en/application_note/an4629-adc-hardware-oversampling-for-microcontrollers-of-the-stm32-l0-and-l4-series-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4635-minimization-of-power-consumption-using-lpuart-for-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4649-migrating-from-stm32f1-series-to-stm32l4-series--stm32l4-series-microcontrollers-stmicroelectronics.pdf
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& Software

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Application Notes https://www.st.com/resource/en/application_note/an4435-guidelines-for-for_related_Tools_obtaining-ulcsaiec-607301603351-class-b-certification-in-any-stm32-application-stmicroelectronics.pdf
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Application Notes https://www.st.com/resource/en/application_note/an4666-parallel-synchronous-transmission-using-gpio-and-dma-stmicroelectronics.pdf
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& Software

Application Notes https://www.st.com/resource/en/application_note/an4701-proprietary-code-readout-protection-on-microcontrollers-of-the-stm32f4-series-stmicroelectronics.pdf
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