

Low Loss DuoPack : IGBT in TRENCHSTOP™ and Fieldstop technology with soft, fast recovery anti-parallel Emitter Controlled HE diode









#### Features:

- Very low V<sub>CE(sat)</sub> 1.5V (typ.)
- Maximum Junction Temperature 175°C
- Short circuit withstand time 5μs
- Designed for :
  - Frequency Converters
  - Uninterrupted Power Supply
- TRENCHSTOP™ and Fieldstop technology for 600V applications offers :
  - very tight parameter distribution
  - high ruggedness, temperature stable behavior
  - very high switching speed
  - low V<sub>CE(sat)</sub>
- Positive temperature coefficient in V<sub>CE(sat)</sub>
- Low EMI
- Low Gate Charge
- Very soft, fast recovery anti-parallel Emitter Controlled HE diode
- Qualified according to JEDEC¹ for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <a href="http://www.infineon.com/igbt/">http://www.infineon.com/igbt/</a>

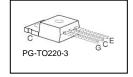
Туре	<b>V</b> <sub>CE</sub>	<b>I</b> C	V <sub>CE(sat),Tj=25°C</sub>	$ all_{ extsf{j,max}}$	Marking	Package
IKP20N60T	600V	20A	1.5V	175°C	K20T60	PG-TO220-3

#### **Maximum Ratings**

Parameter	Symbol	Value	Unit
Collector-emitter voltage, <i>T</i> <sub>j</sub> ≥ 25°C	V <sub>CE</sub>	600	V
DC collector current, limited by $T_{\text{jmax}}$ $T_{\text{C}} = 25^{\circ}\text{C}$	1	41	
$T_{\rm C} = 100^{\circ}{\rm C}$	I <sub>C</sub>	28	
Pulsed collector current, $t_p$ limited by $T_{jmax}$	I <sub>Cpuls</sub>	60	_
Turn off safe operating area, $V_{CE} = 600\text{V}$ , $T_j = 175^{\circ}\text{C}$ , $t_p = 1 \mu\text{s}$	-	60	A
Diode forward current, limited by $T_{jmax}$ $T_{C} = 25^{\circ}C$		41	
$T_{\rm C} = 100^{\circ}{\rm C}$	I <sub>F</sub>	28	
Diode pulsed current, $t_p$ limited by $T_{jmax}$	I <sub>Fpuls</sub>	60	
Gate-emitter voltage	V <sub>GE</sub>	±20	V
Short circuit withstand time <sup>2)</sup>	1	F	
$V_{\rm GE} = 15  \text{V}, \ V_{\rm CC} \le 400  \text{V}, \ T_{\rm j} \le 150  ^{\circ} \text{C}$	$t_{ t SC}$	5	μS
Power dissipation $T_C = 25^{\circ}C$	P <sub>tot</sub>	166	W
Operating junction temperature	T <sub>j</sub>	-40+175	
Storage temperature	$T_{\rm stg}$	-55+150	°C
Soldering temperature, 1.6mm (0.063 in.) from case for 10s	-	260	

<sup>&</sup>lt;sup>1</sup> J-STD-020 and JESD-022





<sup>&</sup>lt;sup>2)</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.



#### **Thermal Resistance**

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				•
IGBT thermal resistance,	P		0.0	K/W
junction – case	$R_{thJC}$		0.9	
Diode thermal resistance,	Б		4.5	
junction – case	$R_{thJCD}$		1.5	
Thermal resistance,	D		62	
junction – ambient	$R_{thJA}$		02	

## **Electrical Characteristic,** at $T_j$ = 25 °C, unless otherwise specified

Danamatan	Cumbal	Conditions		I Incia		
Parameter	Symbol		min.	Тур.	max.	Unit
Static Characteristic	•					
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{\rm GE} = 0  \text{V}, I_{\rm C} = 0.2  \text{mA}$	600	-	-	
Collector-emitter saturation voltage		$V_{\rm GE} = 15 \rm V, \ I_{\rm C} = 20 \rm A$				
	$V_{\text{CE(sat)}}$	<i>T</i> <sub>j</sub> =25°C	-	1.5	2.05	
		<i>T</i> <sub>j</sub> =175°C	-	1.9	-	V
Diode forward voltage		$V_{GE} = 0V, I_{F} = 20A$				7 v
	$V_{F}$	<i>T</i> <sub>j</sub> =25°C	-	1.65	2.05	
		<i>T</i> <sub>j</sub> =175°C	-	1.6	-	
Gate-emitter threshold voltage	$V_{\rm GE(th)}$	$I_{\rm C} = 290  \mu  {\rm A}, V_{\rm CE} = V_{\rm GE}$	4.1	4.9	5.7	
Zero gate voltage collector current		$V_{\text{CE}}=600\text{V}, \ V_{\text{GE}}=0\text{V}$				
	I <sub>CES</sub>	<i>T</i> <sub>j</sub> =25°C	-	-	40	μA
		<i>T</i> <sub>j</sub> =175°C	-	-	1500	
Gate-emitter leakage current	I <sub>GES</sub>	$V_{\text{CE}}=0\text{V}, V_{\text{GE}}=20\text{V}$	-	-	100	nA
Transconductance	$g_{fs}$	$V_{CE} = 20 \text{V}, I_{C} = 20 \text{A}$	-	11	-	S
Integrated gate resistor	R <sub>Gint</sub>			-		Ω

### **Dynamic Characteristic**

Input capacitance	C <sub>iss</sub>	V <sub>CE</sub> =25V,	-	1100	-	
Output capacitance	Coss	$V_{GE}=0V$ ,	-	71	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f=1MHz	-	32	-	
Gate charge	Q <sub>Gate</sub>	$V_{CC} = 480 \text{ V}, I_{C} = 20 \text{ A}$ $V_{GE} = 15 \text{ V}$	-	120	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	LE	PG-TO220-3	-	7	-	nH
Short circuit collector current <sup>1)</sup>	I <sub>C(SC)</sub>	$V_{\text{GE}} = 15 \text{V}, t_{\text{SC}} \le 5 \mu \text{s}$ $V_{\text{CC}} = 400 \text{V},$ $T_{\text{j}} \le 150^{\circ} \text{C}$	-	183.3	-	А

<sup>&</sup>lt;sup>1)</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.





## Switching Characteristic, Inductive Load, at $T_j$ =25 °C

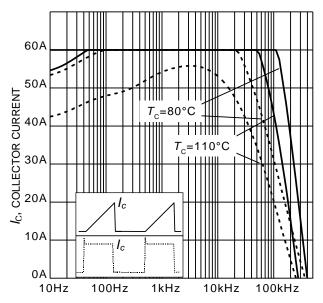
Danamatan	Cymbol	O a maliti a ma	Value			11:4:4
Parameter	Symbol	Conditions	min.	Тур.	max.	Unit
IGBT Characteristic	•					
Turn-on delay time	$t_{d(on)}$	T <sub>j</sub> =25°C,	-	18	-	
Rise time	$t_{\rm r}$	$V_{\text{CC}} = 400 \text{ V}, I_{\text{C}} = 20 \text{ A},$ $V_{\text{GE}} = 0/15 \text{ V}, I_{\text{C}} = 12 \Omega,$	-	14	-	
Turn-off delay time	$t_{d(off)}$	$L_{\sigma}$ =131nH, $C_{\sigma}$ =31pF	-	199	-	ns
Fall time	$t_{f}$	1	-	42	-	
Turn-on energy	Eon	$L_{\sigma}$ , $C_{\sigma}$ from Fig. E Energy losses include	-	0.31	-	
Turn-off energy	E <sub>off</sub>	"tail" and diode reverse	-	0.46	-	mJ
Total switching energy	Ets	recovery.	-	0.77	-	
Anti-Parallel Diode Characteristic				•		
Diode reverse recovery time	$t_{rr}$	T <sub>j</sub> =25°C,	-	41	-	ns
Diode reverse recovery charge	Q <sub>rr</sub>	$V_{R}$ =400V, $I_{F}$ =20A,	-	0.31	-	μC
Diode peak reverse recovery current	I <sub>rrm</sub>	di <sub>F</sub> /dt=880A/μs	-	13.3	-	Α
Diode peak rate of fall of reverse recovery current during $t_{\rm b}$	di <sub>rr</sub> /dt		-	711	-	A/μs

### Switching Characteristic, Inductive Load, at $T_j$ =175 °C

Davamatav	Symbol	Conditions	Value			11:0:4
Parameter	Symbol	Conditions	min.	Тур.	max.	Unit
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	T <sub>j</sub> =175°C,	-	18	-	
Rise time	t <sub>r</sub>	$V_{CC}=400V, I_{C}=20A, V_{GE}=0/15V, I_{C}=12\Omega,$	-	18	-	1
Turn-off delay time	$t_{d(off)}$	$L_{\sigma}$ =131nH, $C_{\sigma}$ =31pF	-	223	-	ns
Fall time	t <sub>f</sub>	]	-	76	-	1
Turn-on energy	Eon	$L_{\sigma}$ , $C_{\sigma}$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	0.51	-	mJ
Turn-off energy	E <sub>off</sub>		-	0.64	-	
Total switching energy	Ets		-	1.15	-	
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	$t_{rr}$	T <sub>j</sub> =175°C	-	176	-	ns
Diode reverse recovery charge	Q <sub>rr</sub>	$V_{R}$ =400V, $I_{F}$ =20A,	-	1.46	-	μC
Diode peak reverse recovery current	I <sub>rrm</sub>	$di_{\rm F}/dt$ =880A/ $\mu$ s	-	18.9	-	Α
Diode peak rate of fall of reverse recovery current during $t_{\rm b}$	di <sub>rr</sub> /dt		-	467	-	A/μs

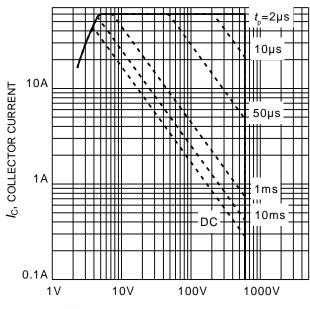






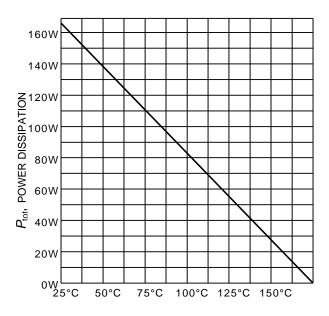
f, SWITCHING FREQUENCY

Figure 1. Collector current as a function of switching frequency  $(T_j \le 175^{\circ}\text{C}, D = 0.5, V_{\text{CE}} = 400\text{V}, V_{\text{GE}} = 0/15\text{V}, r_{\text{G}} = 12\Omega)$ 



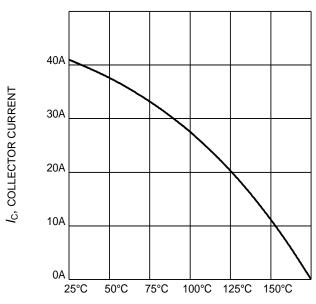
 $V_{\text{CE}}$ , COLLECTOR-EMITTER VOLTAGE

Figure 2. Safe operating area  $(D=0, T_C=25^{\circ}\text{C}, T_j \leq 175^{\circ}\text{C}; V_{GE}=0/15\text{V})$ 



 $T_{\rm C}$ , CASE TEMPERATURE Figure 3. Power dissipation as a function of case temperature

 $(T_{j} \le 175^{\circ}C)$ 



 $T_{\rm C}$ , CASE TEMPERATURE

Figure 4. Collector current as a function of case temperature

( $V_{GE} \ge 15V$ ,  $T_j \le 175^{\circ}C$ )





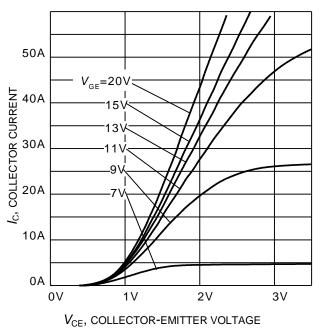


Figure 5. Typical output characteristic  $(T_i = 25^{\circ}C)$ 

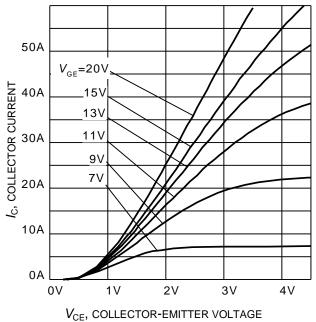


Figure 6. Typical output characteristic  $(T_j = 175^{\circ}\text{C})$ 

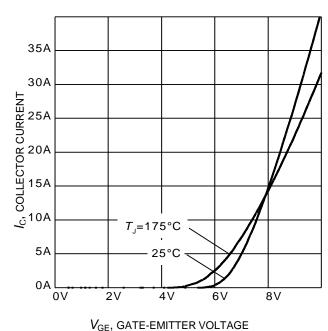


Figure 7. Typical transfer characteristic  $(V_{CE}=10V)$ 

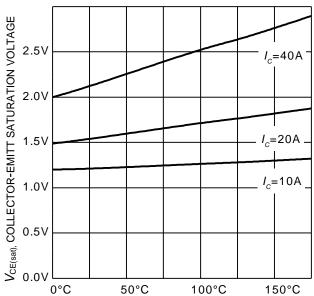
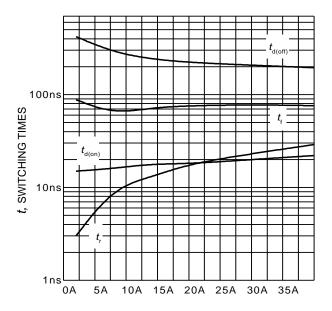


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature  $(V_{GE} = 15 \text{V})$ 

 $T_{\rm J}$ , JUNCTION TEMPERATURE

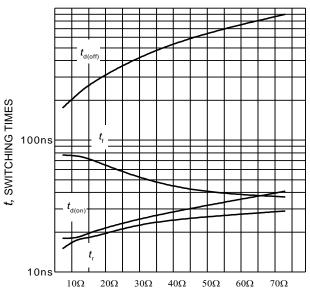






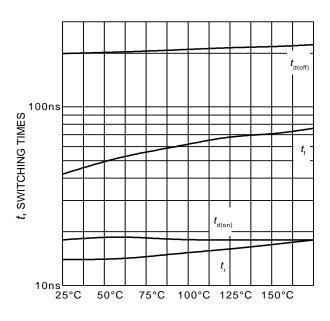
 $I_{\rm C}$ , COLLECTOR CURRENT

Figure 9. Typical switching times as a function of collector current (inductive load,  $T_J$ =175°C,  $V_{CE}$  = 400V,  $V_{GE}$  = 0/15V,  $r_G$  = 12 $\Omega$ , Dynamic test circuit in Figure E)



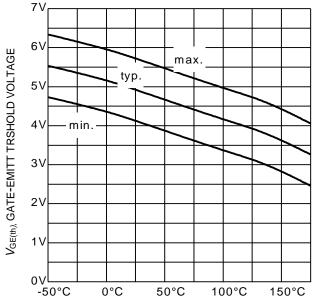
R<sub>G</sub>, GATE RESISTOR

Figure 10. Typical switching times as a function of gate resistor (inductive load,  $T_J$  = 175°C,  $V_{CE}$  = 400V,  $V_{GE}$  = 0/15V,  $I_C$  = 20A, Dynamic test circuit in Figure E)



 $T_{\rm J}$ , JUNCTION TEMPERATURE

Figure 11. Typical switching times as a function of junction temperature (inductive load,  $V_{\text{CE}} = 400\text{V}$ ,  $V_{\text{GE}} = 0/15\text{V}$ ,  $I_{\text{C}} = 20\text{A}$ ,  $I_{\text{C}} = 20\text{A}$ , Dynamic test circuit in Figure E)

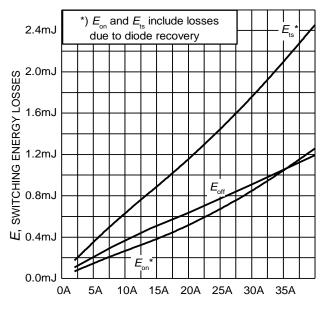


 $T_{\rm J}$ , JUNCTION TEMPERATURE

Figure 12. Gate-emitter threshold voltage as a function of junction temperature  $(I_C = 0.29 \text{mA})$ 







\*)  $E_{on}$  and  $E_{ts}$  include losses 2.4m due to diode recovery E<sub>ts</sub> 2.0mJ **ENERGY LOSSES** 1.6mJ  $E_{\text{off}}$ 1.2mJ **SWITCHING** I 0.8mJ Ш́ 0.4mJ 0.0mJ  $\Omega$ 0 15Ω  $30\Omega$ 45Ω 60Ω

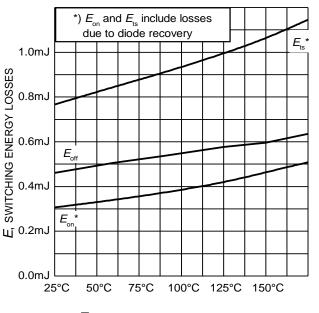
R<sub>G</sub>, GATE RESISTOR

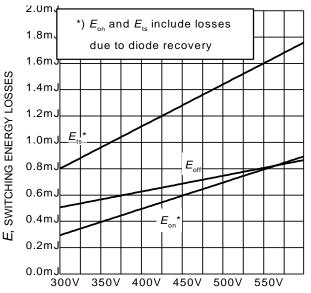
 $I_{C}$ , COLLECTOR CURRENT

Figure 13. Typical switching energy losses as a function of collector current (inductive load,  $T_J = 175$ °C,  $V_{CE} = 400$ V,  $V_{GE} = 0/15$ V,  $r_G = 12\Omega$ , Dynamic test circuit in Figure E)

Figure 14. Typical switching energy losses as a function of gate resistor (inductive load,  $T_J = 175$ °C,

 $V_{\text{CE}} = 400\text{V}, V_{\text{GE}} = 0/15\text{V}, I_{\text{C}} = 20\text{A},$ Dynamic test circuit in Figure E)





 $T_{\rm J}$ , JUNCTION TEMPERATURE

Figure 15. Typical switching energy losses as a function of junction temperature

(inductive load,  $V_{\rm CE}$  = 400V,  $V_{\rm GE}$  = 0/15V,  $I_{\rm C}$  = 20A,  $r_{\rm G}$  = 12 $\Omega$ , Dynamic test circuit in Figure E)

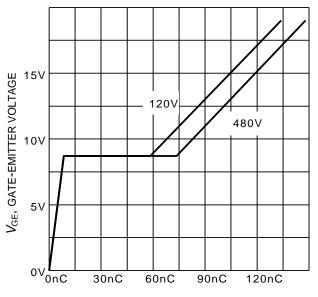
 $V_{CE}$ , COLLECTOR-EMITTER VOLTAGE

Figure 16. Typical switching energy losses as a function of collector emitter voltage

(inductive load,  $T_J$  = 175°C,  $V_{GE}$  = 0/15V,  $I_C$  = 20A,  $r_G$  = 12 $\Omega$ , Dynamic test circuit in Figure E)

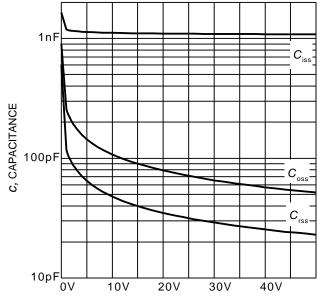






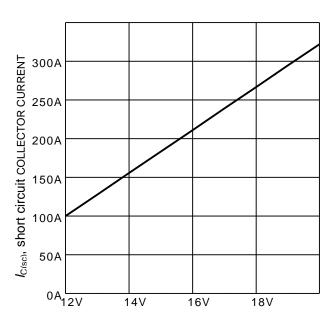
 $Q_{\text{GE}}$ , GATE CHARGE

Figure 17. Typical gate charge  $(I_c=20 \text{ A})$ 



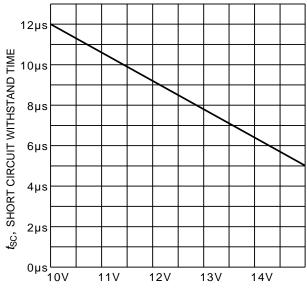
 $V_{\rm CE}$ , COLLECTOR-EMITTER VOLTAGE

Figure 18. Typical capacitance as a function of collector-emitter voltage  $(V_{GE}=0V, f=1 \text{ MHz})$ 



 $V_{\rm GE}$ , gate-emittetr voltage

Figure 19. Typical short circuit collector current as a function of gate-emitter voltage  $(V_{CE} \le 400 \text{V}, T_i \le 150 ^{\circ}\text{C})$ 



 $V_{\rm GE}$ , gate-emitetr voltage

Figure 20. Short circuit withstand time as a function of gate-emitter voltage ( $V_{\rm CE}$ =400V, start at  $T_{\rm J}$ =25°C,  $T_{\rm Jmax}$ <150°C)





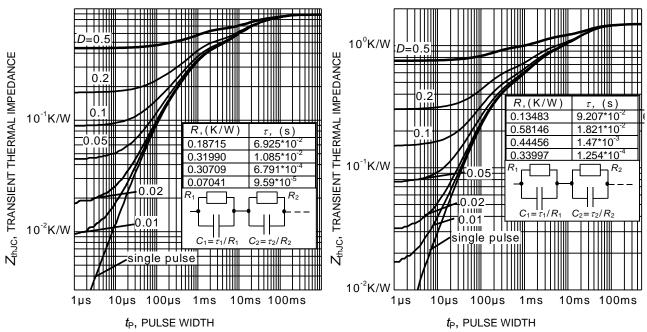


Figure 21. IGBT transient thermal impedance  $(D = t_p / T)$ 

Figure 22. Diode transient thermal impedance as a function of pulse width  $(D=t_P/T)$ 

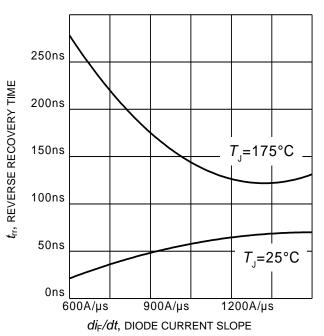
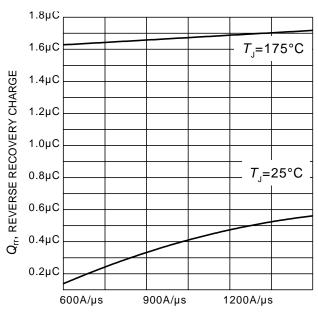


Figure 23. Typical reverse recovery time as a function of diode current slope  $(V_R=400V, I_F=20A,$  Dynamic test circuit in Figure E)



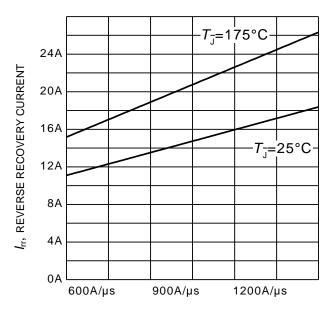
 $di_{\rm F}/dt$ , DIODE CURRENT SLOPE

Figure 24. Typical reverse recovery charge as a function of diode current slope

 $(V_R = 400V, I_F = 20A,$ Dynamic test circuit in Figure E)







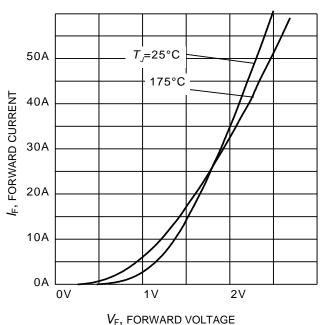
-750A/μs

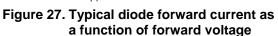
di<sub>F</sub>/dt, DIODE CURRENT SLOPE

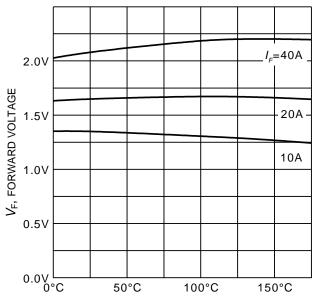
Figure 25. Typical reverse recovery current as a function of diode current slope

( $V_R = 400V$ ,  $I_F = 20A$ , Dynamic test circuit in Figure E) di<sub>F</sub>/dt, DIODE CURRENT SLOPE

Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope (V<sub>R</sub>=400V, I<sub>F</sub>=20A, Dynamic test circuit in Figure E)





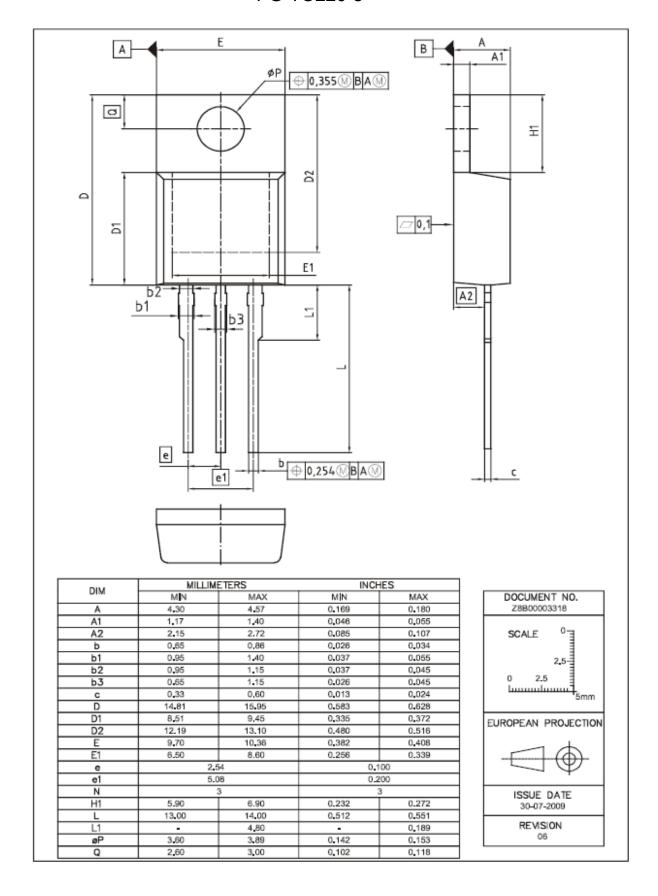


T<sub>J</sub>, JUNCTION TEMPERATURE

Figure 28. Typical diode forward voltage as a function of junction temperature

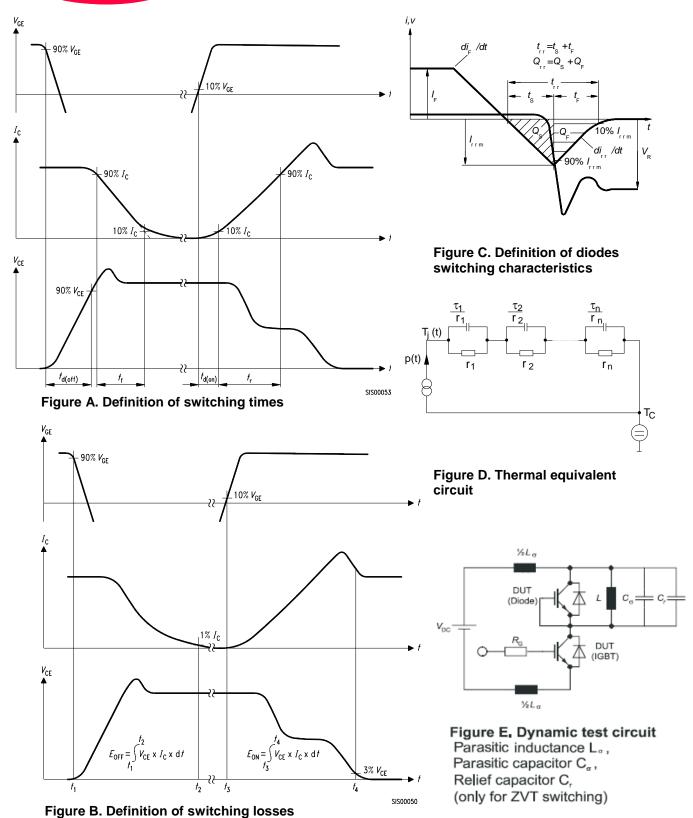


### PG-TO220-3











# IKP20N60T

#### TRENCHSTOP™ Series

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