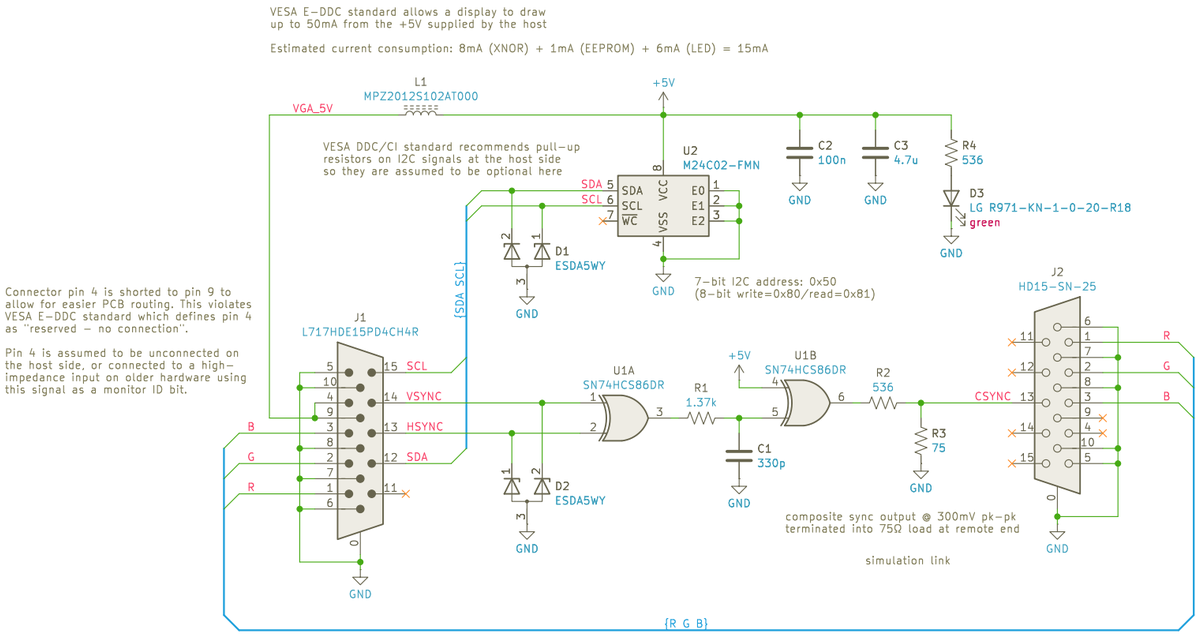
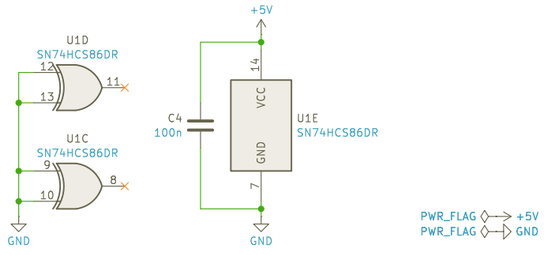
Notes:

- All resistors rated 1% tolerance or better unless noted otherwise
- All capacitors rater 10V or higher unless noted otherwise



Connector pin 4 is shorted to pin 9 to allow for easier PCB routing. This violates VESA E-DDC standard which defines pin 4 as "reserved - no connection".

Pin 4 is assumed to be unconnected on the host side, or connected to a high-impedance input on older hardware using this signal as a monitor ID bit.



PWR_FLAG +5V
PWR_FLAG GND

Source location: https://github.com/windsorschmidt/vga-csync-xnor

Sheet: /
File: vga-csync-xnor.kicad_sch

Title: VGA H/V to Composite Sync Generator (XNOR)

Size: B	Date: 2025-08-03	**Rev: 1**
KiCad E.D.A. 9.0.3		Id: 1/1