ECE375 Lab 2C -> Assembler -> Machine Code

TA:

School of Electrical Engineering and Computer Science Oregon State University

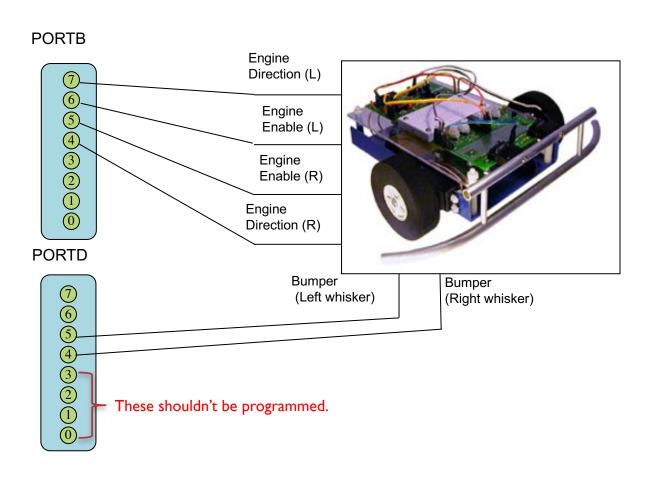
Goal of this Lab

- Understand BumpBot behaviors through LEDs.
- Learn how to control with registers.

Learn how to avoid switch debouncing.

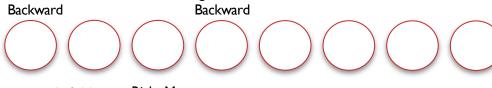
 Understand the difference between C and Assmebly.

Connection Guides



Bumpbot Behaviors

On(1)Forward Right Motor Left Motor **Forward Forward** Bit Bit Bit Bit Bit Bit Bit Bit 3 2 5 0 Left Motor Right Motor **Enabled Enabled** • Halt Left Motor Right Motor Forward **Forward** Left Motor Right Motor Disabled Disabled Backward Left Motor Right Motor

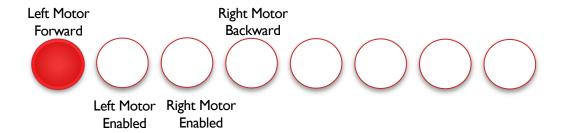


Left Motor Right Motor Enabled Enabled

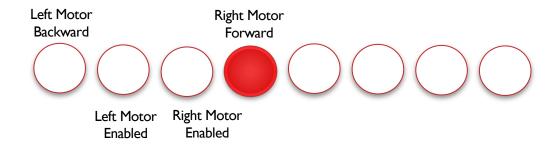
Bumpbot Behaviors

• Turn Right





Turn Left



Controlling Registers

- Register Types
 - DDRx is a <u>Data Direction Register for port x</u>
 - PORTx is a Port Output Register for port x
 - PINx is a Port Input Register for port x
- Output Port Settings

```
    DDRB = 0b11111111
```

PORTB = 0b11110000

; set bit 0-7 as output

; assign output data to bit 4-7

; LEDs are turned on

Input Port Settings

DDRD = 0b00000000

PORTD = 0b11111111

IN mpr, PIND

; set 7-0 bits as inputs

; enable pull up resistors

; Read input data to mpr

AVR Ports

Figure 30. General Digital I/O⁽¹⁾ **DDRx** DDxn Q cur WDx **Pull-up** RESET RDx resistor DATA BUS **PORT**x PORTxn Q CLR WPx RESET SLEEP RRx SYNCHRONIZER **PIN**x WDx: RDx: WPx: RRx: RPx: WRITE DDRx READ DDRx WRITE PORTx READ PORTx REGISTER READ PORTx PIN PULLUP DISABLE SLEEP CONTROL I/O CLOCK PUD: SLEEP:

Note: 1. WPx, WDx, RRx, RPx, and RDx are common to all pins within the same port. clk_{I/O}, SLEEP, and PUD are common to all ports.

Atmega I 28 Datasheet 66p

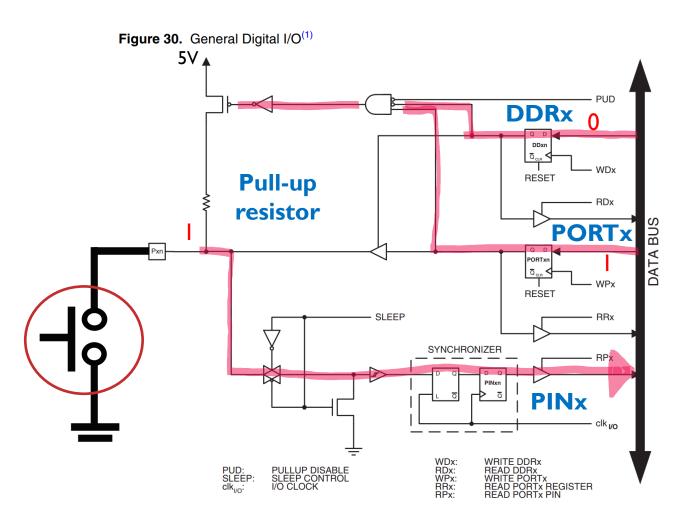
AVR Ports – Configure output

Figure 30. General Digital I/O⁽¹⁾ **DDRx** DDxn Q_a WDx **Pull-up** RESET RDx resistor DATA BUS **PORT**x Q cLF WPx RESET SLEEP RRx SYNCHRONIZER **PIN**x WDx: WRITE DDRx PULLUP DISABLE SLEEP CONTROL I/O CLOCK RDx: WPx: RRx: RPx: PUD: SLEEP: READ DDRx WRITE PORTX READ PORTX REGISTER READ PORTX PIN

Note: 1. WPx, WDx, RRx, RPx, and RDx are common to all pins within the same port. clk_{I/O}, SLEEP, and PUD are common to all ports.

Atmega I 28 Datasheet 66p

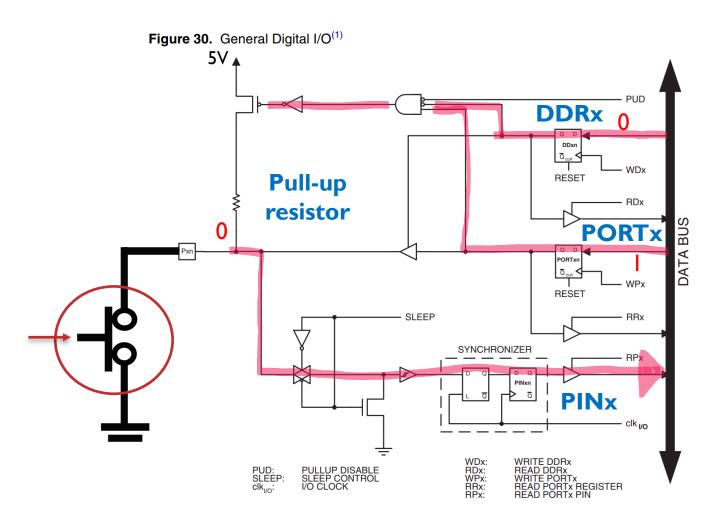
AVR Ports – Configure input



Note: 1. WPx, WDx, RRx, RPx, and RDx are common to all pins within the same port. clk_{I/O}, SLEEP, and PUD are common to all ports.

Atmega 128 Datasheet 66p

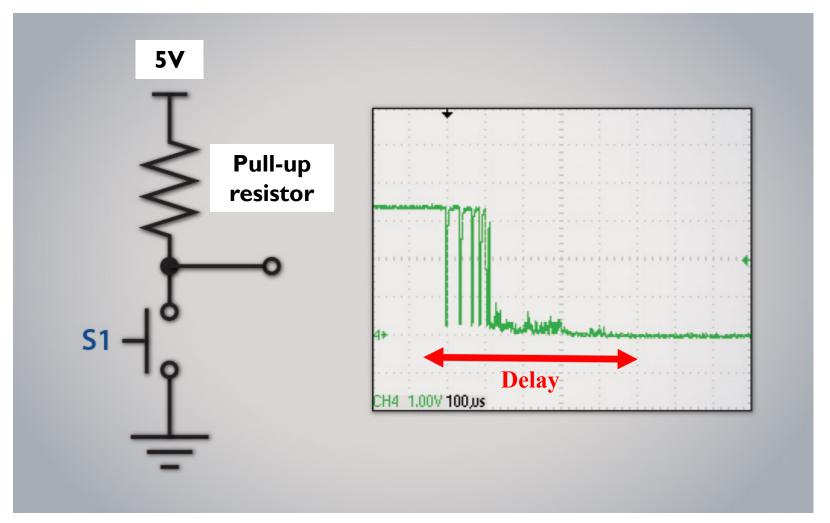
AVR Ports – Configure input



Note: 1. WPx, WDx, RRx, RPx, and RDx are common to all pins within the same port. clk_{I/O}, SLEEP, and PUD are common to all ports.

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Switch Debouncing



C vs Assembly

In C

```
DDRB = 0b11110000 // set bit 7-4 as output
PORTB = 0b01100000 // turn on LEDs connected to bit 6-5
```

In Assembly

```
LDI mpr, 0b11110000
```

OUT DDRB, mpr; set bit 7-4 as output

LDI mpr, 0b01100000

OUT PORTB, mpr; turn on LEDs connected to bit 6-5

C vs Assembly

In C

In Assembly

```
IN
                       PIND
                                           ;read input values
             mpr,
                       0600110000
                                           ;extract only 4,5th bit
ANDI
             mpr,
CPI
                       0600100000
                                           ;check if right whisker is hit
             mpr,
BRNE
            NEXT
                                           ;if no, go to NEXT
             BotAction
RCALL
                                           ; if yes, call BotAction
NEXT:
```

Check-off Lists

- Correct LED behaviors based on the switch buttons.
 - LEDs represent the Bot behaviors
 - Switch buttons represent whisker hits.

Compilation for Mac and Ubuntu users

I. Install avr-gcc toolchain.

2. Download Makefile from lab webpage.

- 3. Open the file with a text editor, set PRG variable to a file name of your source code without the file extension(.c)
 - e.g., PRG = DanceBot

Questions?

