# TSPi Plan Summary - Form SUMP

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|---|---|---|---|
| Name | นางสาวปรีชญา ชูศรีทอง (PM)<br>นางสาววรรัตน์ กะเสริม (QM) | Date | 04 ก.ย. 2564 |
| Team | 4 | Instructor | อ.อภิสิทธิ์ แสงใส |
| Part/Level | System | Cycle | 1 |

| **Product Size** | **Plan** | **Actual** |
|---|---|---|
| Requirements pages (SRS) | 100 | 230 |
| เอกกสารการประชุม | 39 | 48 |
| High-level design pages (SDS) | 30 | 30 |
| Base LOC (B) (measured) | 0 | 0 |
| Deleted LOC (D) | 0 | 0 |
| | (Estimated) | (Counted) |
| Modified LOC (M) | 0 | 0 |
| | (Estimated) | (Counted) |
| Added LOC (A) | 0 | 8,991 |
| | (N-M) | (T-B+D-R) |
| Reused LOC (R) | 0 | 0 |
| | (Estimated) | (Counted) |
| Total New & Changed LOC (N) | 0 | 0 |
| | (Estimated) | (A+M) |
| Total LOC (T) | 0 | 8,991 |
| | (N+B-M-D+R) | (Measured) |
| Total New Reuse LOC | 0 | 0 |
| Estimated Object LOC (E) | | |
| Upper Prediction Interval (70%) | | |
| Lower Prediction Interval (70%) | | |

| **Time in Phase (hours)** | **Plan** | **Actual** | **Actual %** |
|---|---|---|---|
| Management and miscellaneous | 12 | 4 | 33.33 |
| Launch and strategy | 10 | ไม่ทราบข้อมูล | ไม่ทราบข้อมูล |
| Planning | 40 | 66 | 165 |
| Requirements | 82 | 40 | 48.8 |
| Test plan | 20 | - | ไม่ทราบข้อมูล |
| Requirements review | 20 | 15.1 | 75.5 |
| High-level design | 100 | 48 | 48 |
| High-level design review | 10 | 7 | 70 |
| Implementation planning | 103 | 84 | 81.55 |
| Code | 277 | 203.6 | 73.5 |
| Code review | 52 | 41.3 | 79.42 |
| Compile | ไม่ทราบข้อมูล | ไม่ทราบข้อมูล | ไม่ทราบข้อมูล |
| Unit test | 18.5 | 16.2 | 87.57 |
| Build and integration | 55.5 | 37.8 | 68.11 |
| System test | 30 | - | ไม่ทราบข้อมูล |
| Documentation | 226.8 | 263.3 | 116.1 |
| Postmortem | 100 | - | ไม่ทราบข้อมูล |
| Total | 1056.8 | 826.3 | 78.2 |
| Total Time UPI (70%) | | | |
| Total Time LPI (70%) | | | |

# TSPi Plan Summary - Form SUMP (continued)

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| Name | นางสาวปรีชญา ชูศรีทอง (PM)<br>นางสาววรรัตน์ กะเสริม (QM) | Date | 04 ก.ย. 2564 |
| Team | 4 | Instructor | อ.อภิสิทธิ์ แสงใส |
| Part/Level | System | Cycle | 1 |

| **Defects Injected** | **Plan** | **Actual** | **Actual %** |
|---|---|---|---|
| Strategy and Planning | 6 | 0 | 0 |
| Requirements | 0 | 0 | 0 |
| System test plan | 0 | 0 | 0 |
| Requirements inspection | 0 | 0 | 0 |
| High-level design | 0 | 0 | 0 |
| Integration test plan | 0 | 0 | 0 |
| High-level design inspection | 0 | 0 | 0 |
| Detailed design | 0 | 3 | 0 |
| Detailed design review | 15 | 0 | 0 |
| Test development | 0 | 0 | 0 |
| Detailed design inspection | 0 | 0 | 0 |
| Code | 15 | 98 | 653.33 |
| Code review | 9 | 0 | 0 |
| Compile | 1 | 2 | 200 |
| Code inspection | 0 | 0 | 0 |
| Unit Test | 0 | 0 | 0 |
| Build and integration | 0 | 0 | 0 |
| System test | 0 | 0 | 0 |
| Total Development | 46 | 103 | |
| **Defects Removed** | **Plan** | **Actual** | **Actual %** |
| Strategy and Planning | 0 | 0 | 0 |
| Requirements | 0 | 0 | 0 |
| System test plan | 0 | 0 | 0 |
| Requirements inspection | 0 | 0 | 0 |
| High-level design | 0 | 0 | 0 |
| Integration test plan | 0 | 0 | 0 |
| High-level design inspection | 0 | 0 | 0 |
| Detailed design | 0 | 0 | 0 |
| Detailed design review | 0 | 0 | 0 |
| Test development | 6 | 11 | 183.33 |
| Detailed design inspection | 0 | 0 | 0 |
| Code | 2 | 3 | 150 |
| Code review | 10 | 0 | 0 |
| Compile | 69 | 117 | 169 |
| Code inspection | 0 | 0 | 0 |
| Unit Test | 0 | 0 | 0 |
| Build and integration | 0 | 0 | 0 |
| System test | 0 | 0 | 0 |
| Total Development | 79 | 128 | |

# TSPi Plan Summary Instructions - Form SUMP

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|---|---|
| **Purpose** | - This form holds plan and actual data for program parts or assemblies. |
| **General** | - An assembly could be a system with multiple products, a product with multiple components, or a component with multiple modules.<br>- A part could be a module, component, or product.<br>- Note: the lowest-level parts or modules typically have no system-level data, such as requirements, high-level design, or system test. |
| **Using the TSPi Tool** | When using the TSPi tool, the plan values are automatically generated.<br>- The time and size data are computed from the TASK and SUMS forms.<br>- The defect values are automatically generated during the quality planning process (SUMQ).<br>The actual values are also automatically generated by the TSPi tool.<br>- Time and size values come from the LOGT, TASK, and SUMS forms.<br>- Defect data come from the LOGD forms.<br>When not using the TSPi tool, follow the instructions below. |
| **Header** | - Enter your name, date, team name, and instructor's name.<br>- Name the part or assembly and its level.<br>- Enter the cycle number. |
| **Columns** | - Plan: This column holds the part or assembly plan data.<br>- Actual: For assemblies, this column holds the sum of the actual data for the parts of the assembly (at the lowest level, the modules). |
| **Product Size** | - For text and designs, enter only the new and changed size data.<br>- For program parts or assemblies, enter all the indicated LOC data.<br>- Obtain the data from the SUMS form. |
| **Time in Phase** | - Enter estimated and actual time by phase.<br>- For parts, obtain these data from the TASK forms for those parts.<br>- For assemblies, obtain the part-level time data from the totals on the SUMT form and the assembly-level data from the assembly-level TASK form.<br>- For example, HLD time would come from the assembly TASK form while total part unit test time would come from the SUMT form.<br>- Actual %: Enter the percent of the actual development time by phase. |
| **Defects Injected** | - Enter estimated and actual defects injected by phase.<br>- Enter the defect estimates while producing the quality plan.<br>- For parts, obtain actual data from the LOGD forms for those parts.<br>- For assemblies, get part-level defect data from the totals of the SUMDI form and assembly-level data from the assembly LOGD form.<br>- For example, HLD defects would come from the assembly LOGD form while the total part coding defects would come from the SUMDI form.<br>- Actual %: Enter the percent of the actual defects injected by phase. |
| **Defects Removed** | - Enter estimated and actual defects removed by phase.<br>- Enter the defect estimates while producing the quality plan.<br>- For parts, obtain actaul data from the LOGD forms for those parts.<br>- For assemblies, obtain part-level defect data from the totals of the SUMDR form and assembly-level data from the assembly LOGD form.<br>- For example, HLD review defects would come from the assembly LOGD form while the total part code review defects would come from the SUMDR form.<br>- Actual %: Enter the percent of the actual defects removed by phase. |