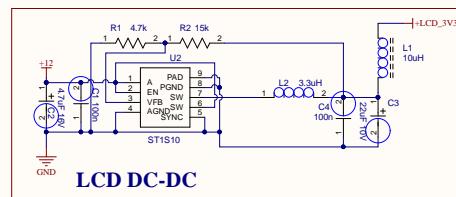




7Inch version



A

A

B

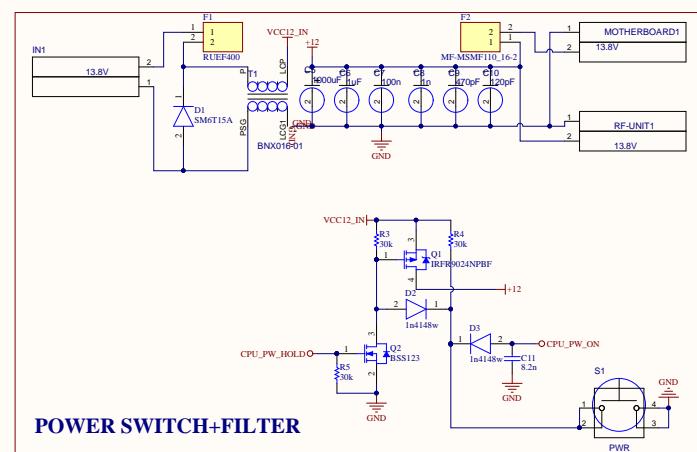
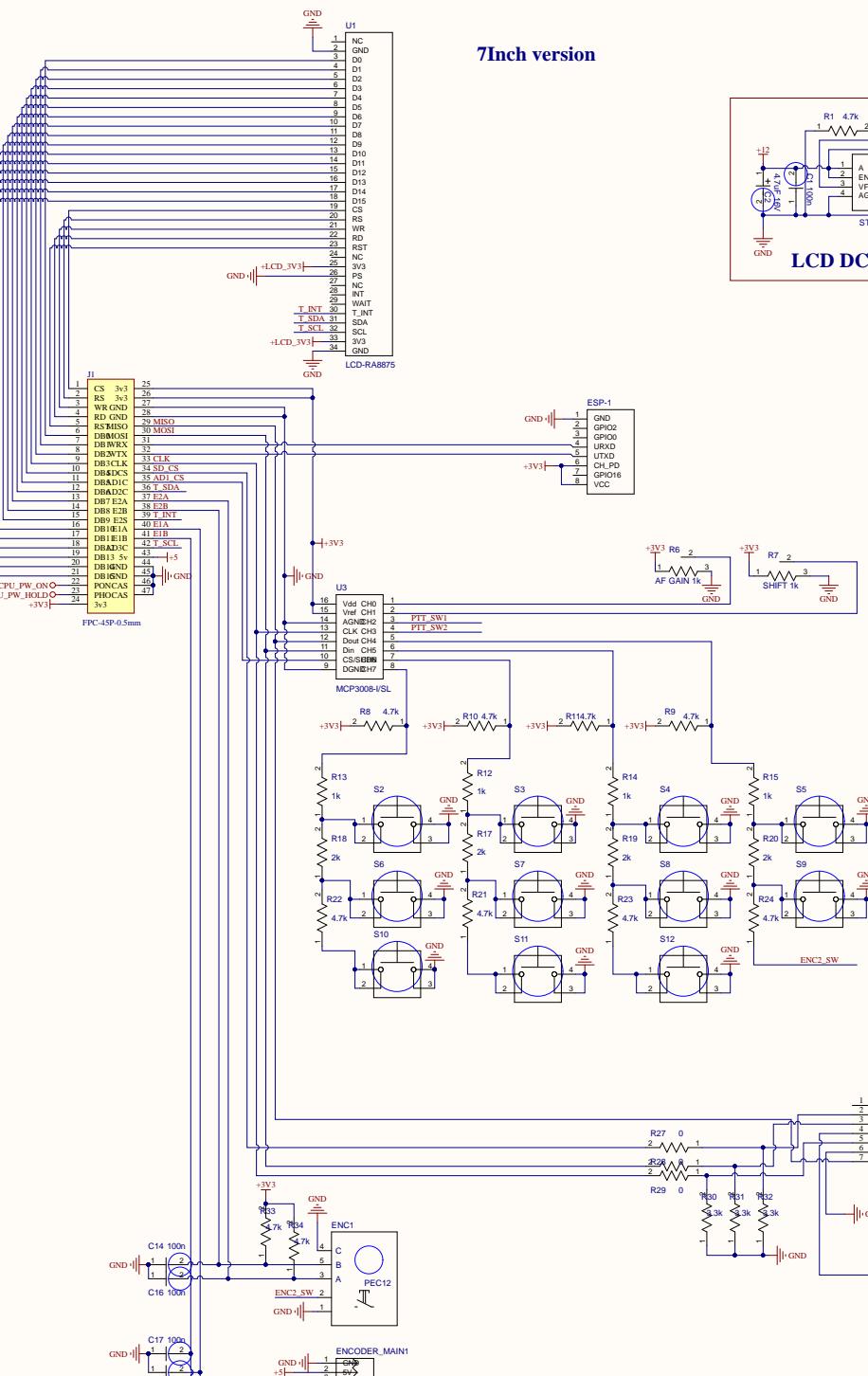
B

C

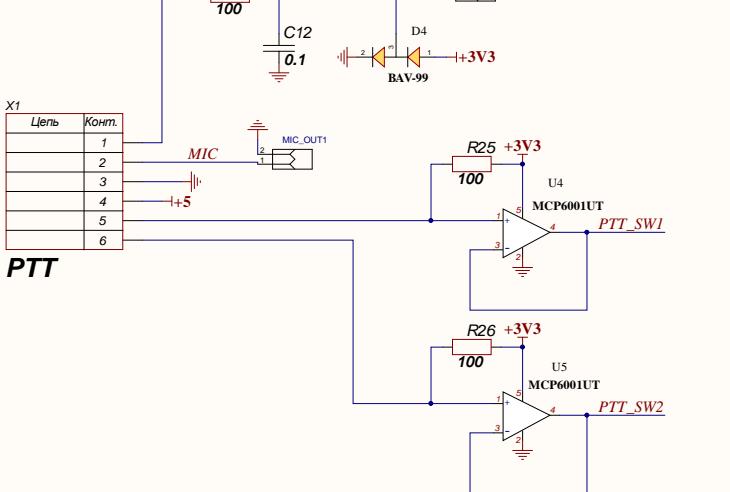
C

D

D

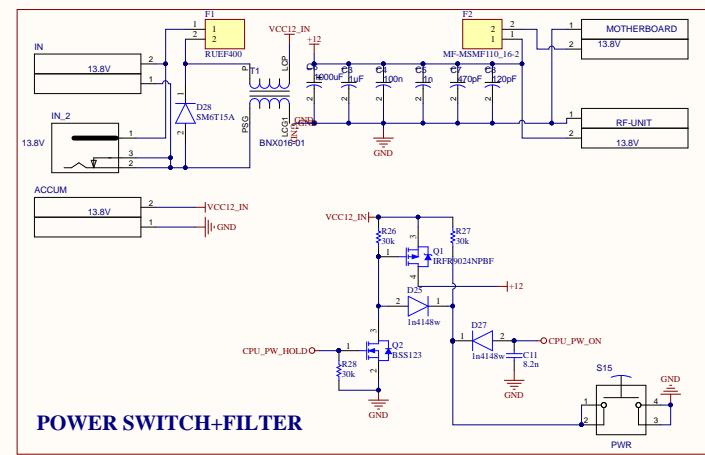
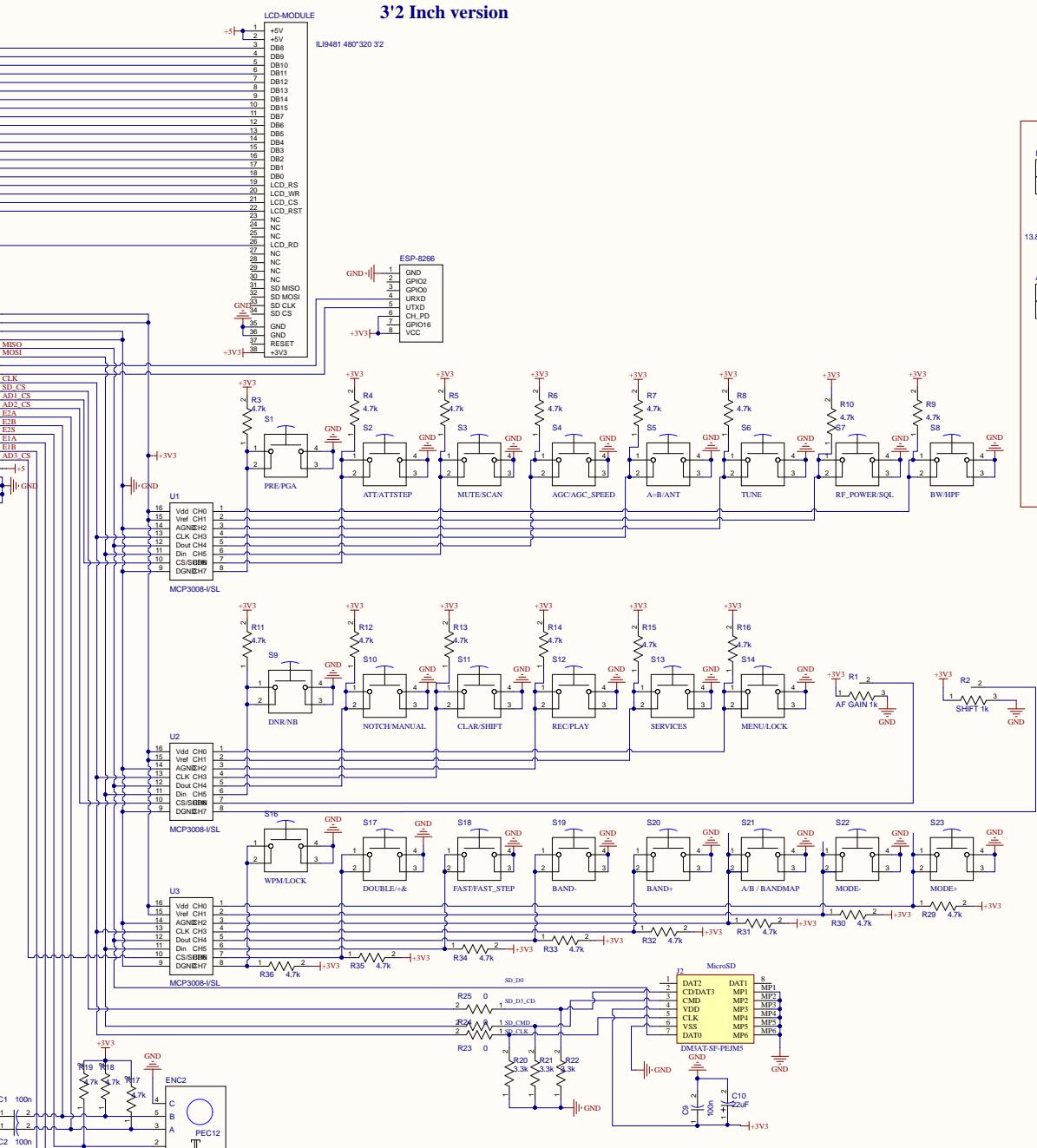


TANGENT



3'2 Inch version

ILI9481 480*320 3'2

**POWER SWITCH+FILTER**

A

U_POWER
POWER.SchDoc



U_CLOCK_GENERATOR
CLOCK_GENERATOR.SchDoc



A

B

U_CONNECTIONS
CONNECTIONS.SchDoc



U_FPGA
FPGA.SchDoc



U_STM32
STM32.SchDoc



U_AUDIO_CODEC
AUDIO_CODEC.SchDoc



B

C

U_DAC
DAC.SchDoc



U_ADC
ADC.SchDoc



C

D

Title **MOTHERBOARD**

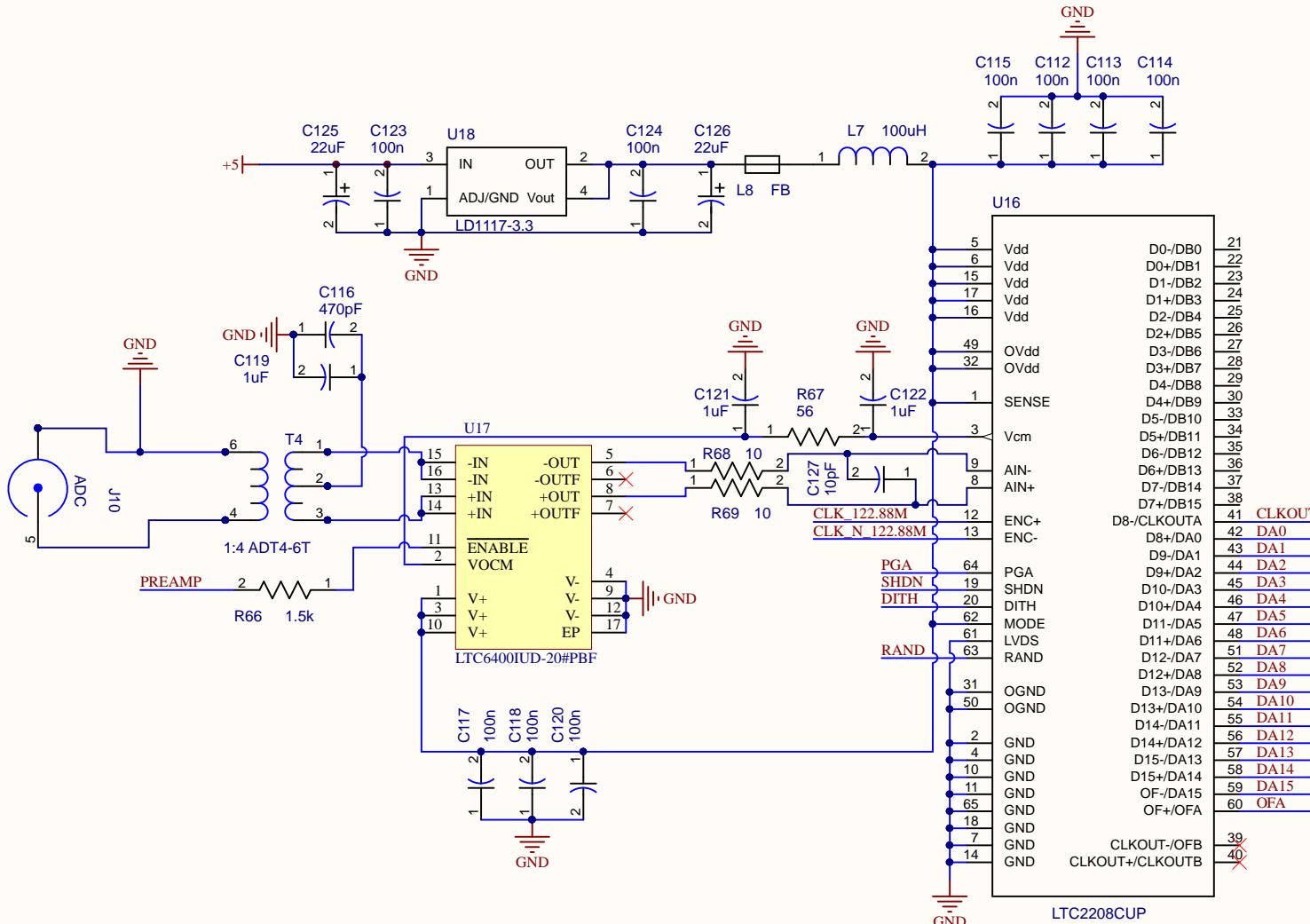
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Date: 01.12.2020	Time: 14:44:44	Sheet 2 of 18
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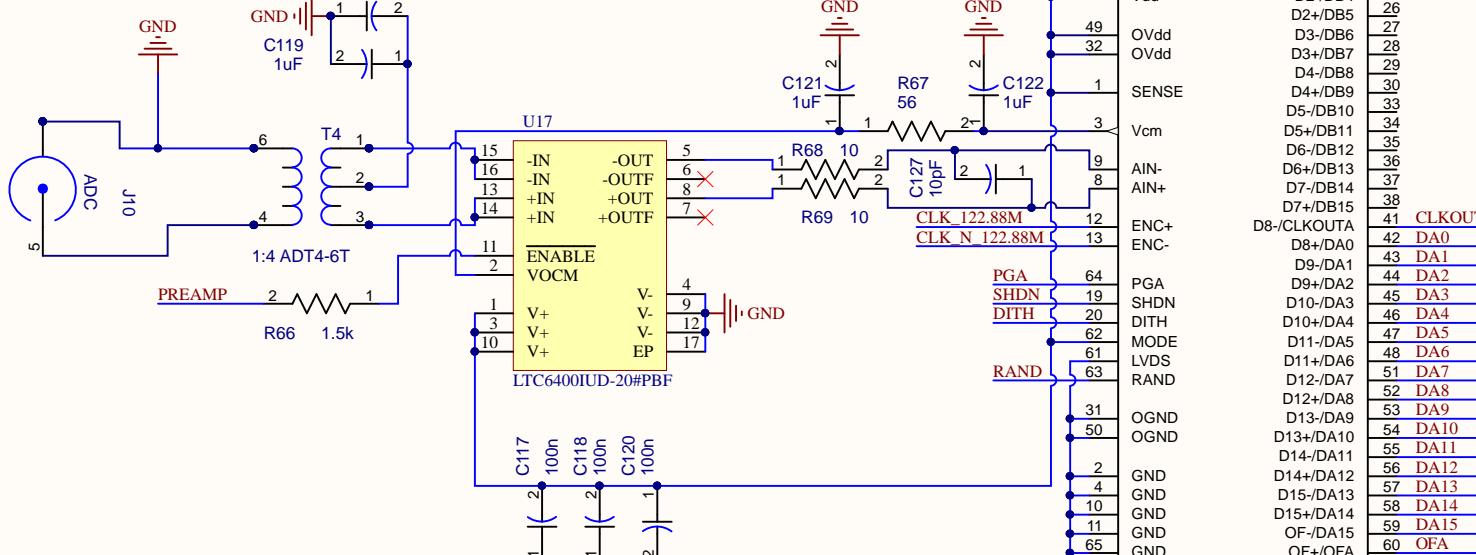


A



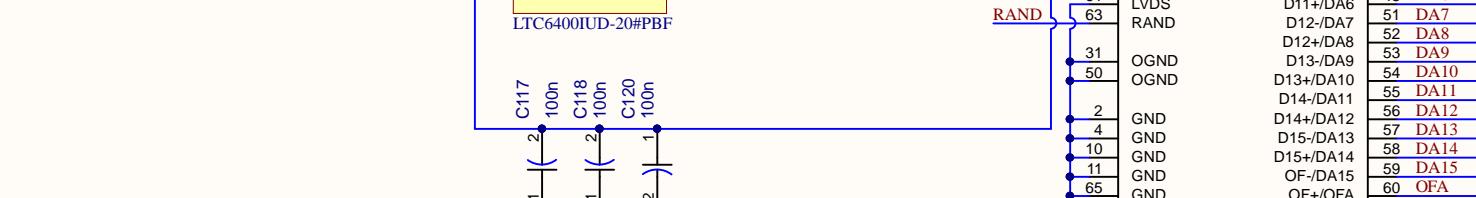
A

B



B

C



C

D

Title ADC			*
Size: A4	Number:3	Revision:2.0	*
Date: 01.12.2020	Time: 14:44:44	Sheet 3 of 18	*
File: C:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\MOTHERBOARD\ADC.SchDoc			*

A

A

B

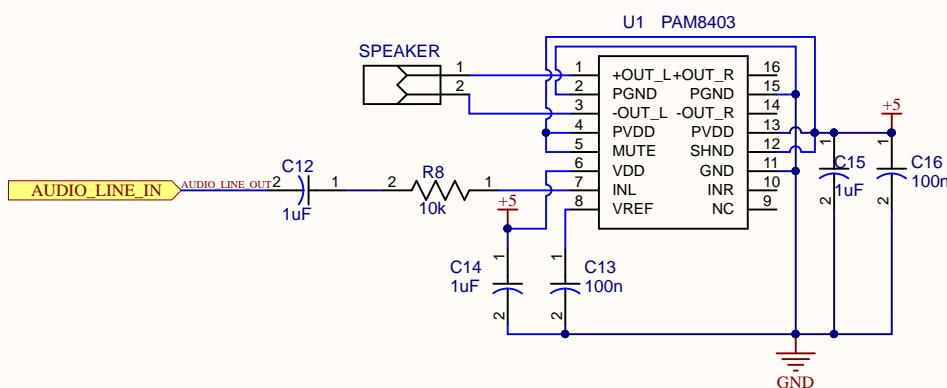
B

C

C

D

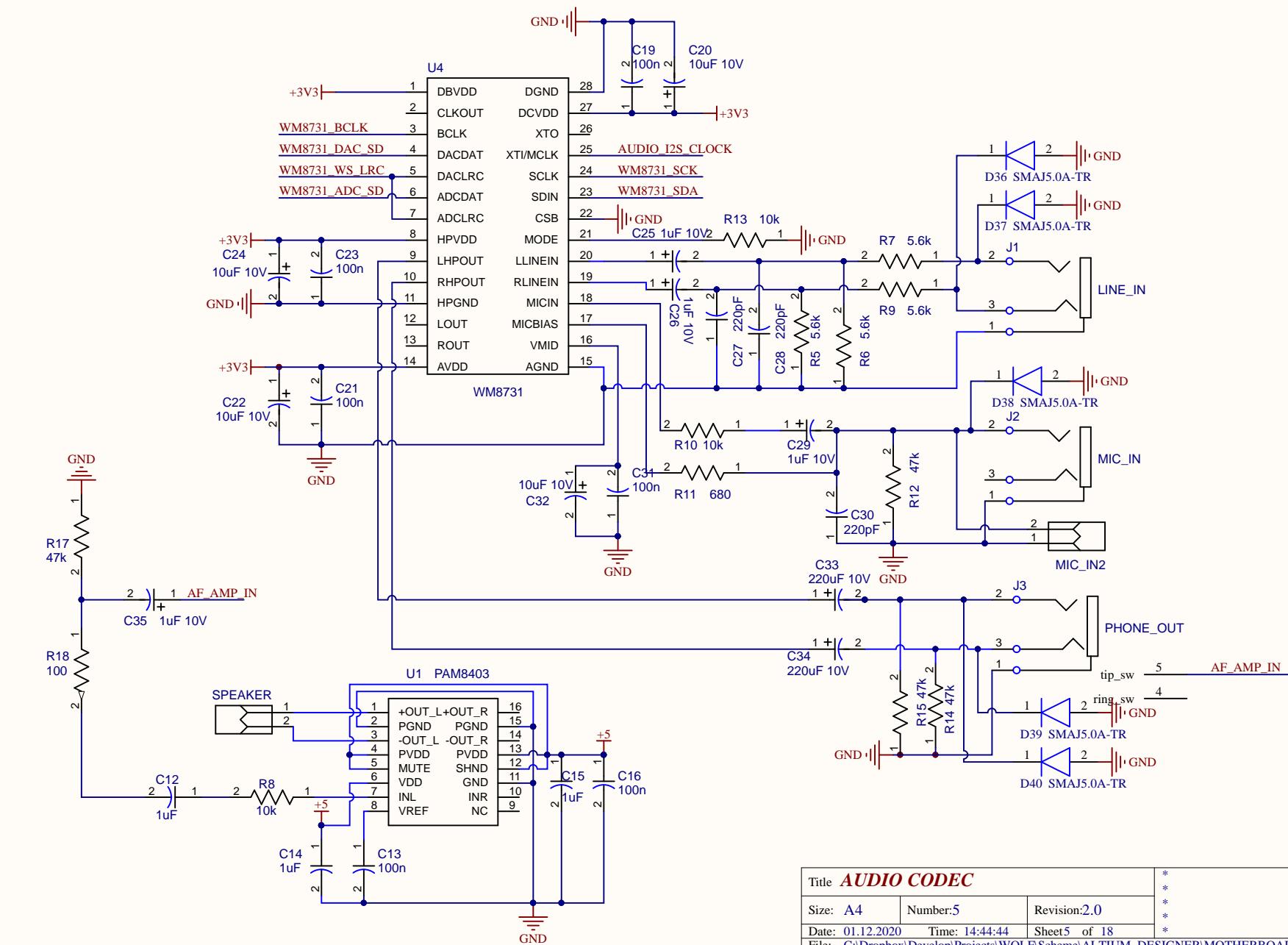
D

Title ***AF AMP***Size: **A4** Number: **4** Revision: **2.0**Date: **01.12.2020** Time: **14:44:44** Sheet **4** of **18**File: **C:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\AF AMP.SchDoc**

*
UA3REO
BH8CGJ
*

Altium

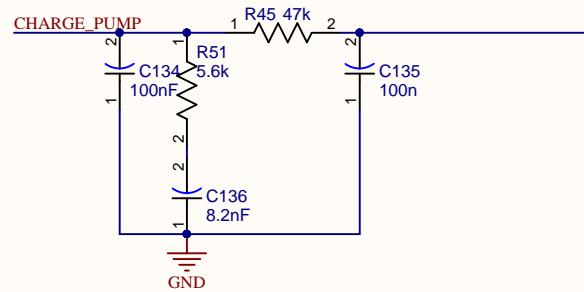
A

Title **AUDIO CODEC**

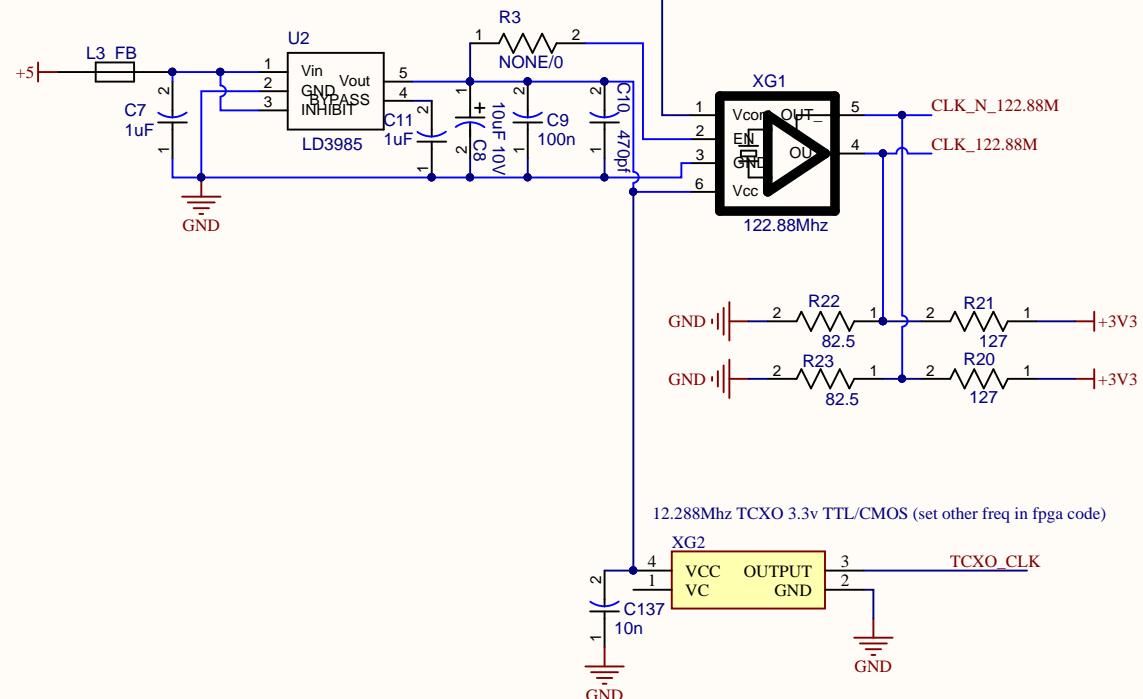
Size: A4	Number: 5	Revision: 2.0
Date: 01.12.2020	Time: 14:44:44	Sheet 5 of 18
File: C:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\MOTHERBOARD\AUDIO_CODEC.SchDoc		*



A



B



C

D

Title **CLOCK_GENERATOR**

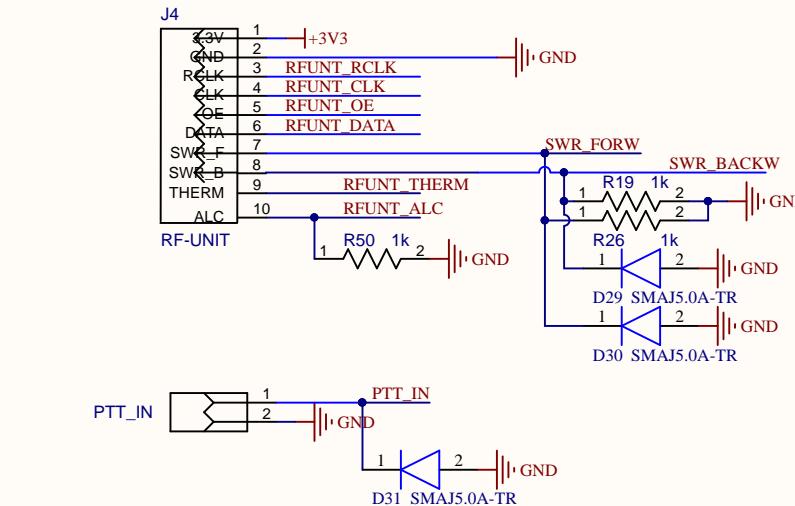
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Date: 01.12.2020 Time: 14:44:45 Sheet 6 of 18

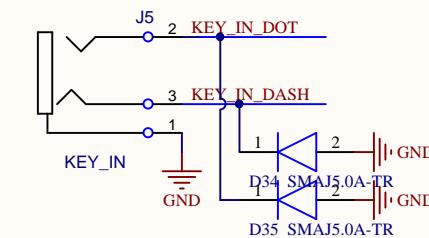
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Altium

A



B



C

J6

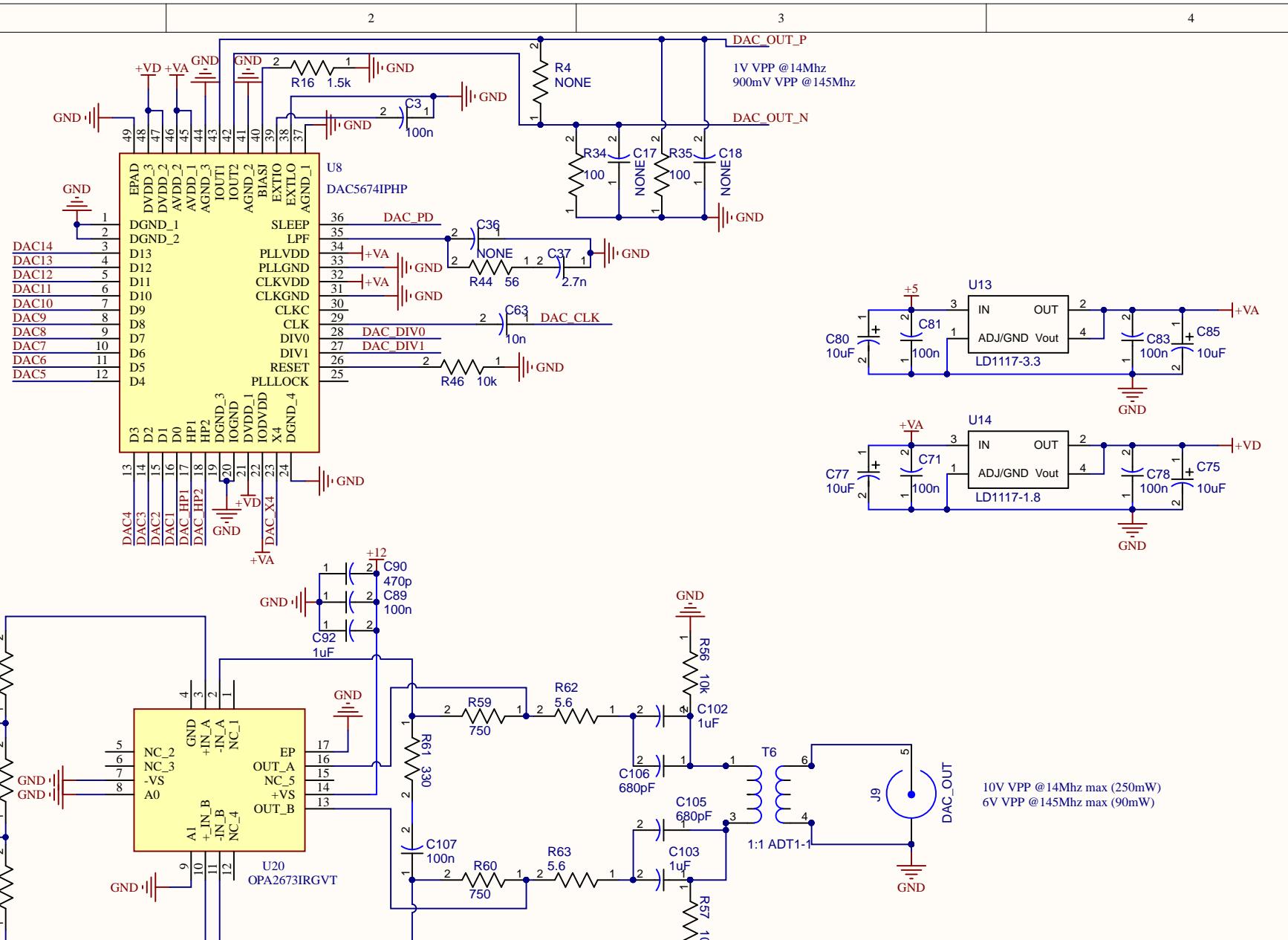
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FMC_NWE	3
FMC_NOE	4
STM_NRST	5
FMC_D0	6
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FMC_D2	8
FMC_D3	9
FMC_D4	10
FMC_D5	11
FMC_D6	12
FMC_D7	13
FMC_D8	14
FMC_D9	15
FMC_D10	16
FMC_D11	17
FMC_D12	18
FMC_D13	19
FMC_D14	20
FMC_D15	21
PWR_ON	22
PWR_HOLD	23
+3V3	24
	25 +3V3
	26 GND
	27 GND
	28 GND
	29 PERI_MISO
	30 PERI_MOSI
	31 ESP_1_TX
	32 ESP_1_RX
	33 PERI_SCK
	34 SD_CS
	35 AD1_CS
	36 AD2_CS
	37 ENC2_DT
	38 ENC2_CLK
	39 ENC2_SW
	40 ENC_DT
	DB1E1A
	DB1E1B
	DBAD3C
	DB13 5v
	DB1GND
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	PONCAS
	PHOCAS
	3v3

FPC-45P-0.5mm

D

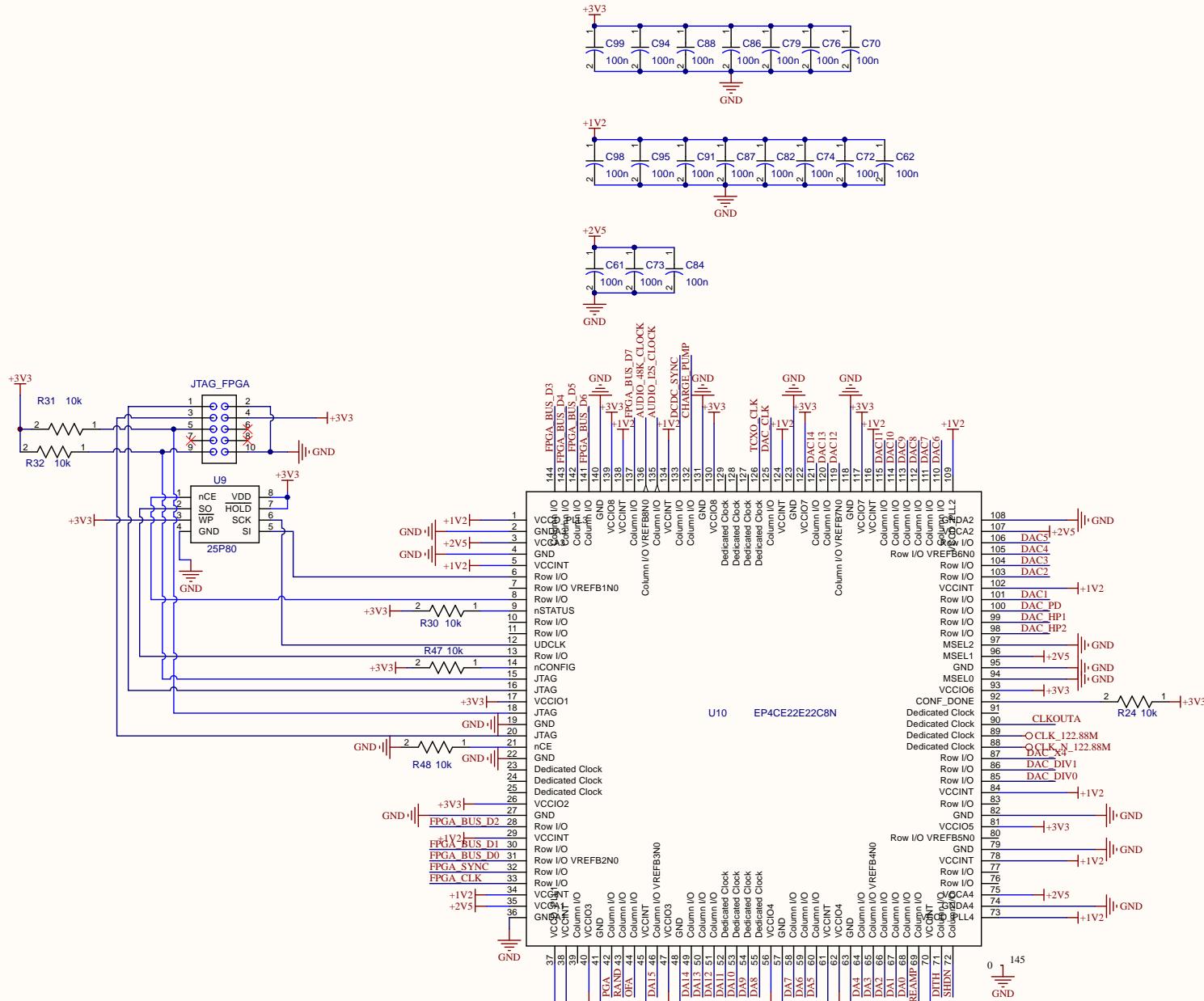
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Size: A4	Number: 7	Revision: 2.0
Date: 01.12.2020	Time: 14:44:45	Sheet 7 of 18
File: C:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\MOTHERBOARD\CONNECTIONS.SchDoc		*

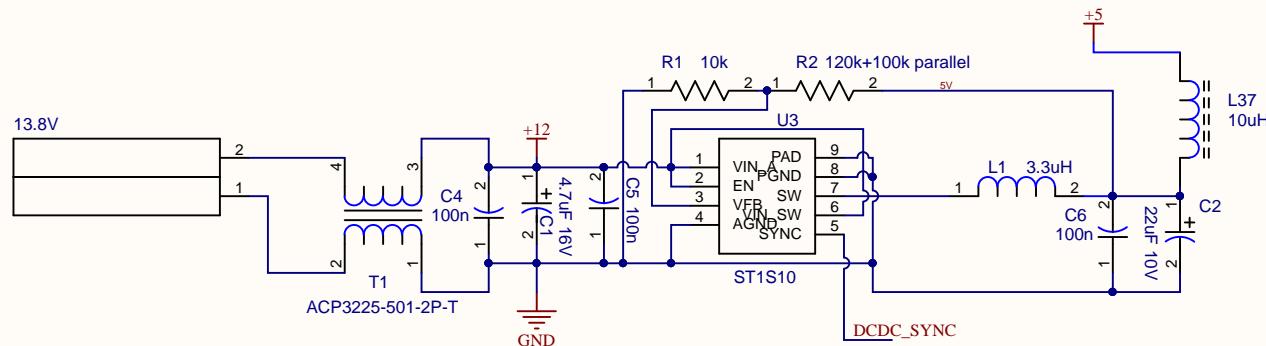


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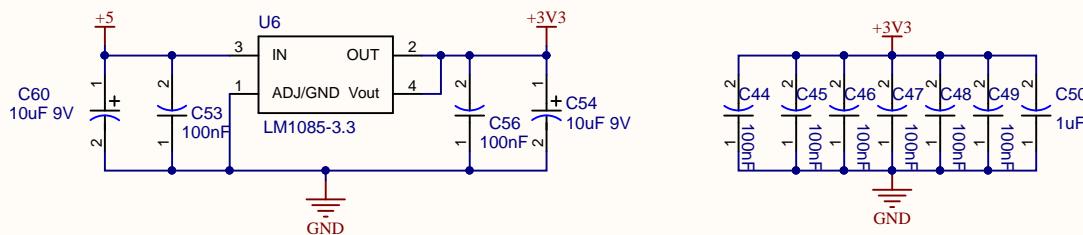
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 Date: **01.12.2020** Time: **14:44:45** Sheet **8** of **18** *
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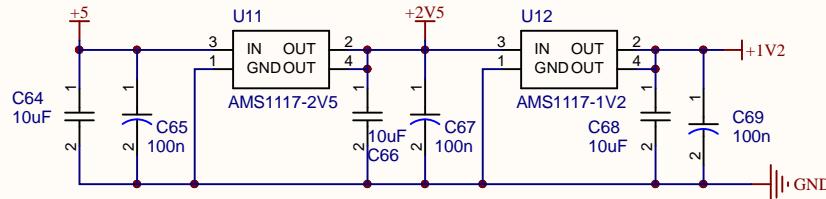
A



B



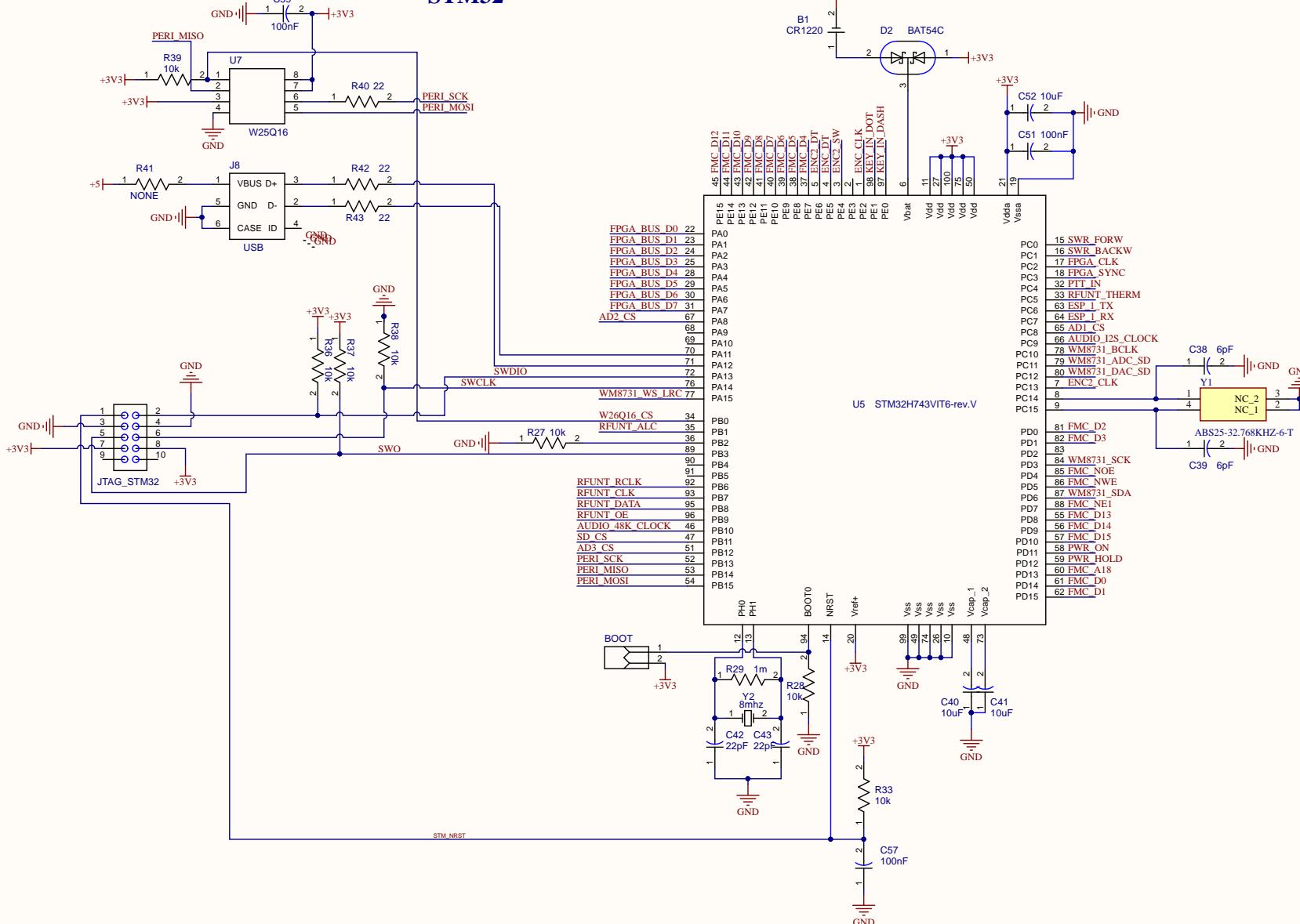
C

Title **POWER**

Size: A4	Number: 10	Revision: 2.0
Date: 01.12.2020	Time: 14:44:46	Sheet 10 of 18
File: C:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\MOTHERBOARD\POWER.SchDoc		*

Altium

STM32



Title **STM32**

Size: A3 Number:11 Revision:2.0
Date: 01.12.2020 Time: 14:44:47 Sheet 11 of 18

File: C:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM DESIGNER\MOTHERBOARD\STM32.SchDoc

Altium

A

A

B

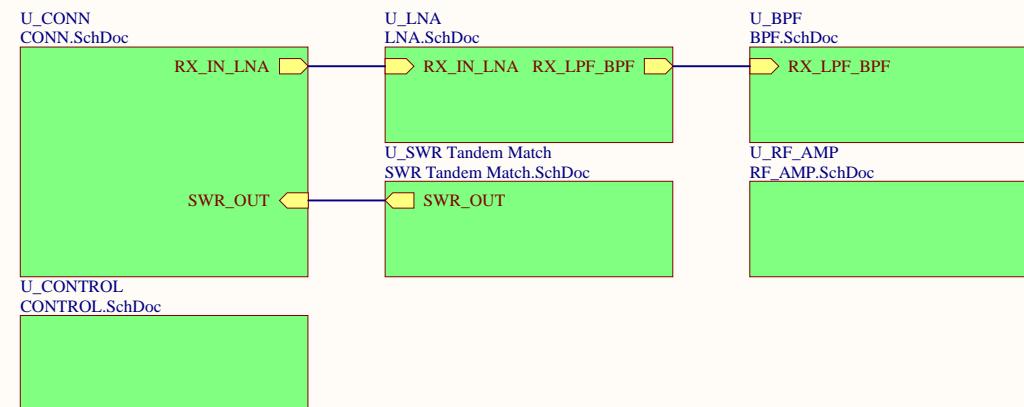
B

C

C

D

D

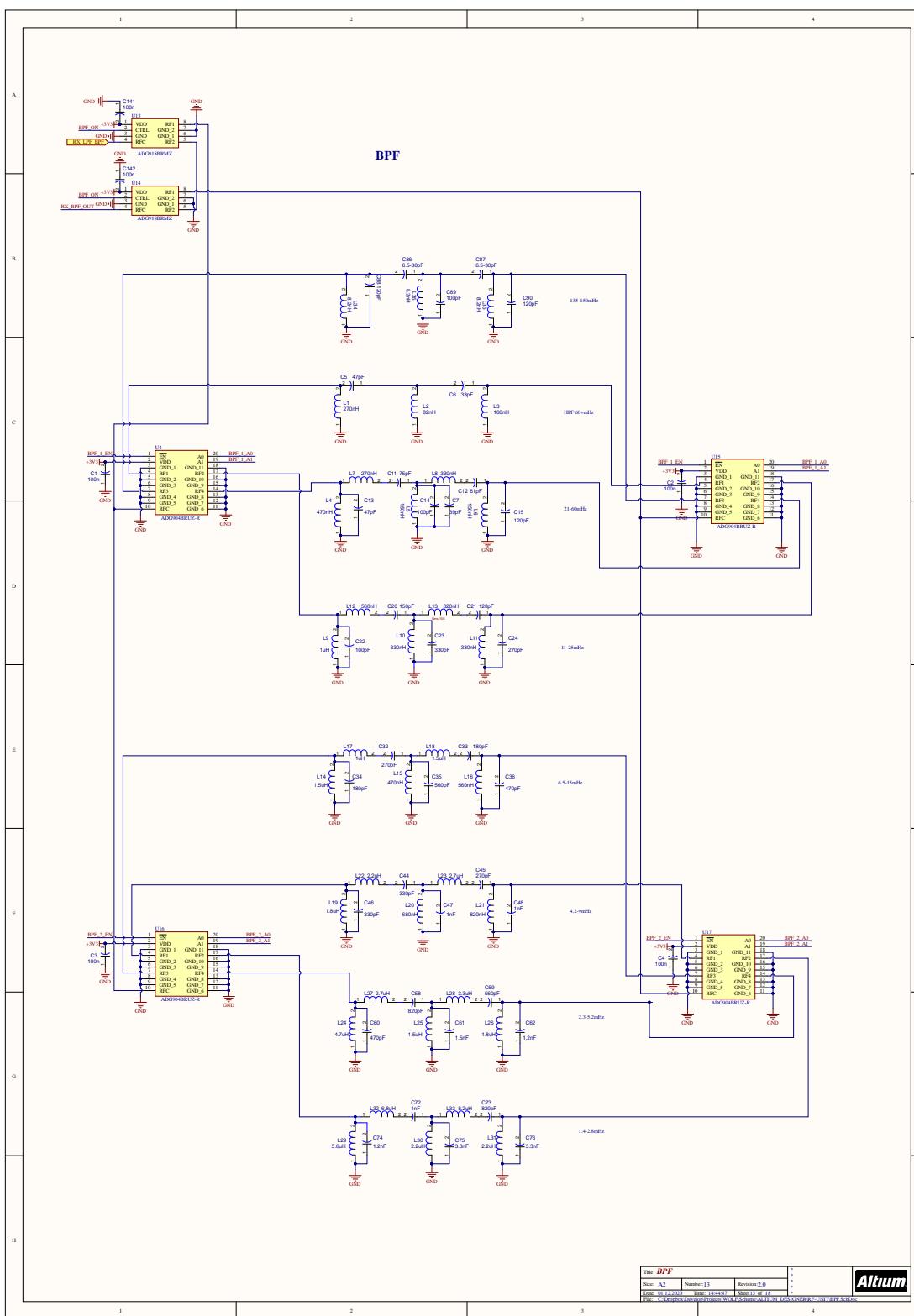
Title ***RF-UNIT***

Size: A4 Number:12 Revision:2.0

Date: 01.12.2020 Time: 14:44:47 Sheet 12 of 18

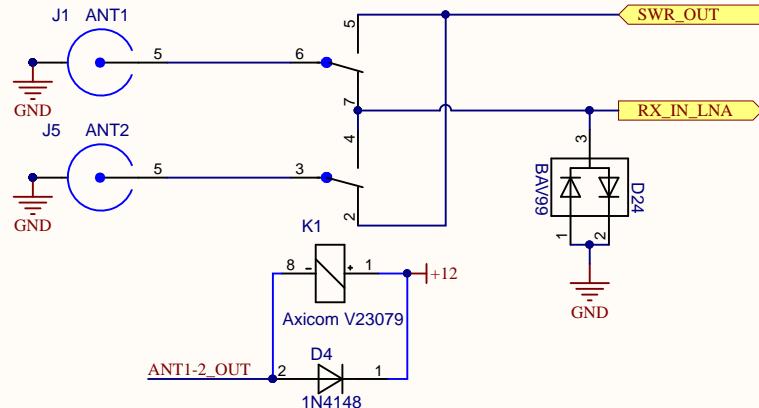
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Altium

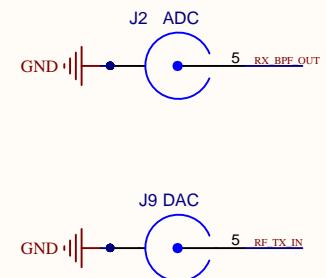


A

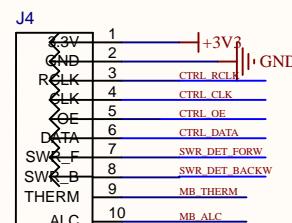
TXRX_COMUTATOR



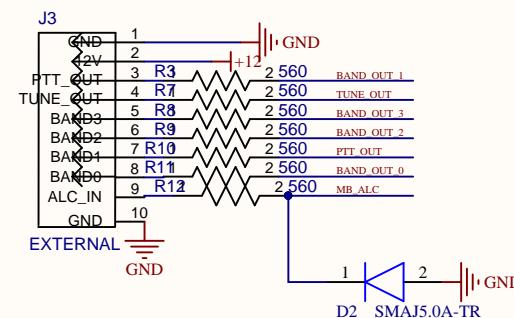
CONN



B



MOTHERBOARD

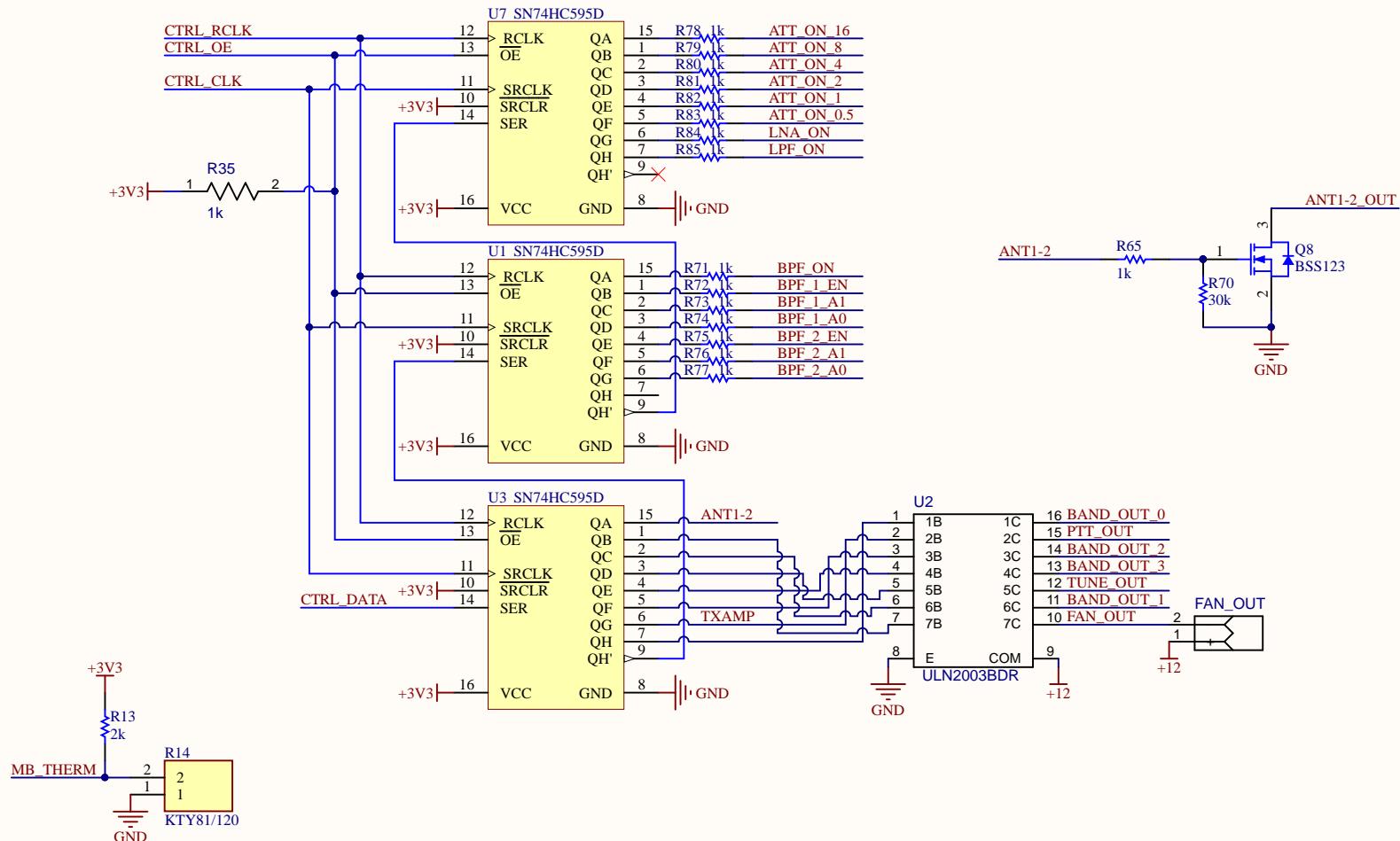


C

Title CONN

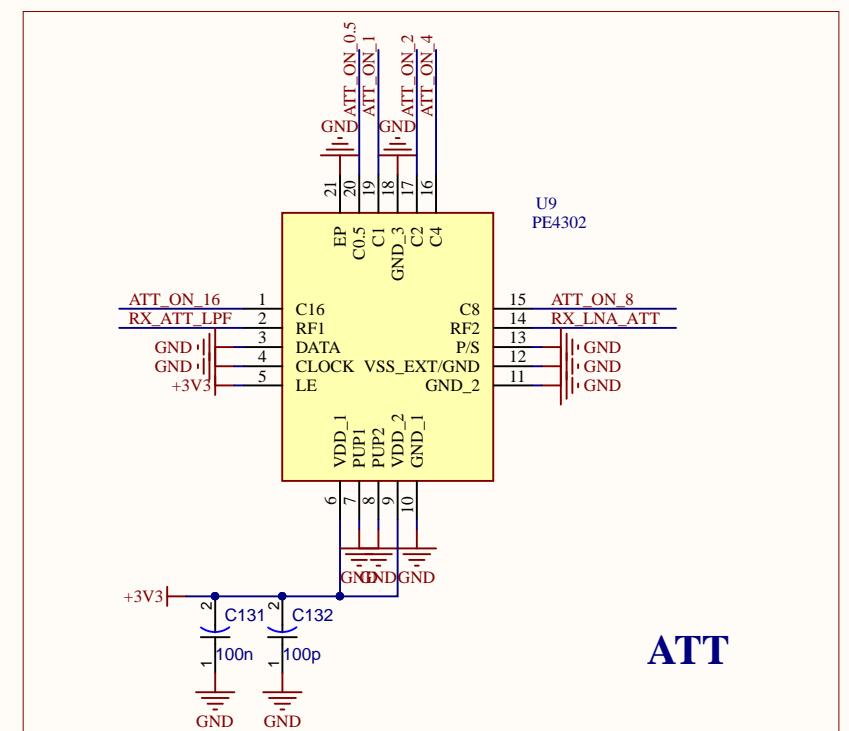
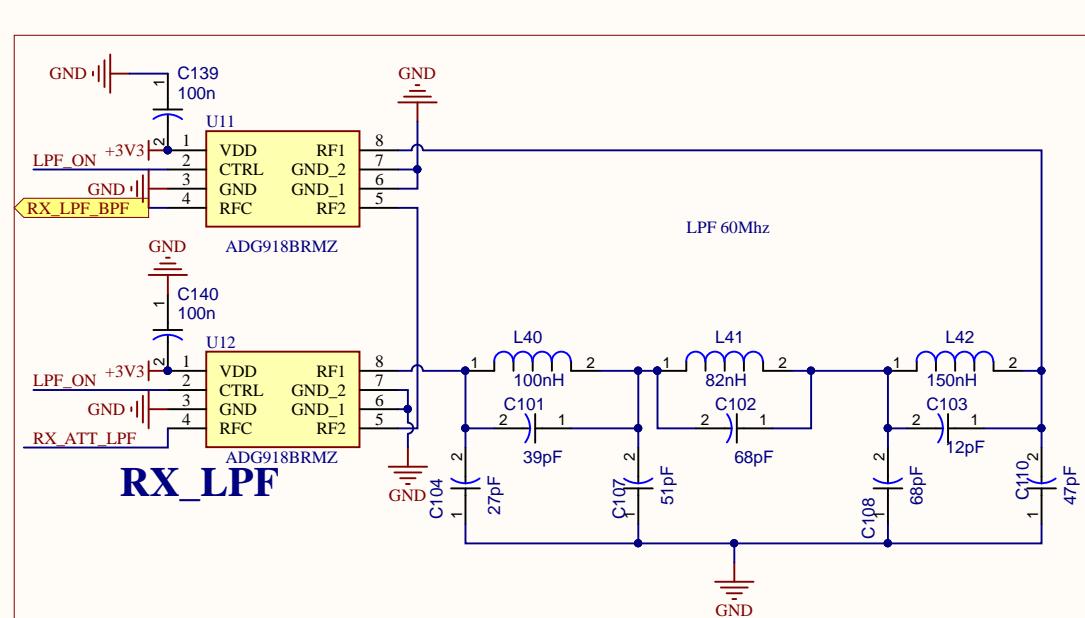
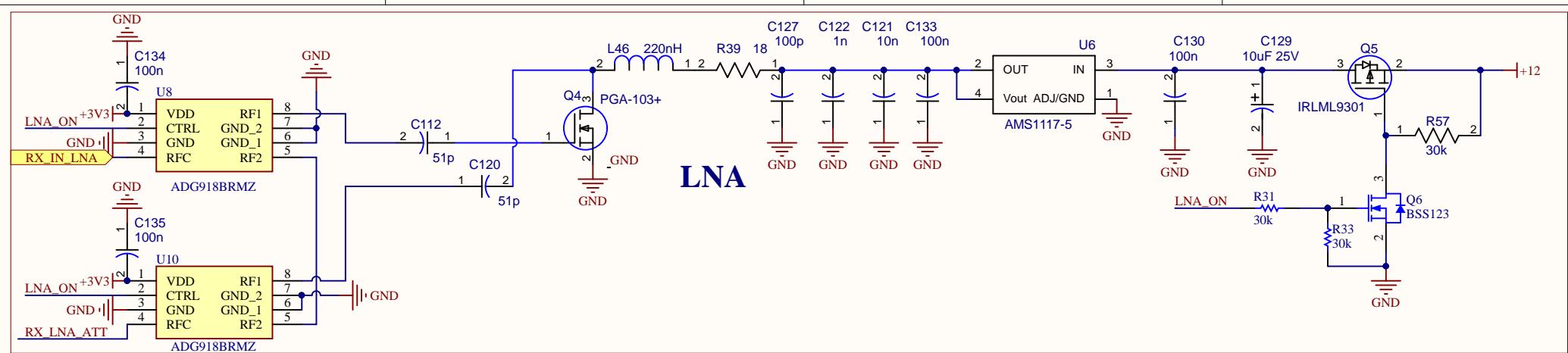
Size: A4	Number: 14	Revision: 2.0
Date: 01.12.2020	Time: 14:44:48	Sheet 14 of 18
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A

Title **CONTROL**

Size: A4	Number: 15	Revision: 2.0
Date: 01.12.2020	Time: 14:44:48	Sheet 15 of 18
File: C:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONTROL.SchDoc		*

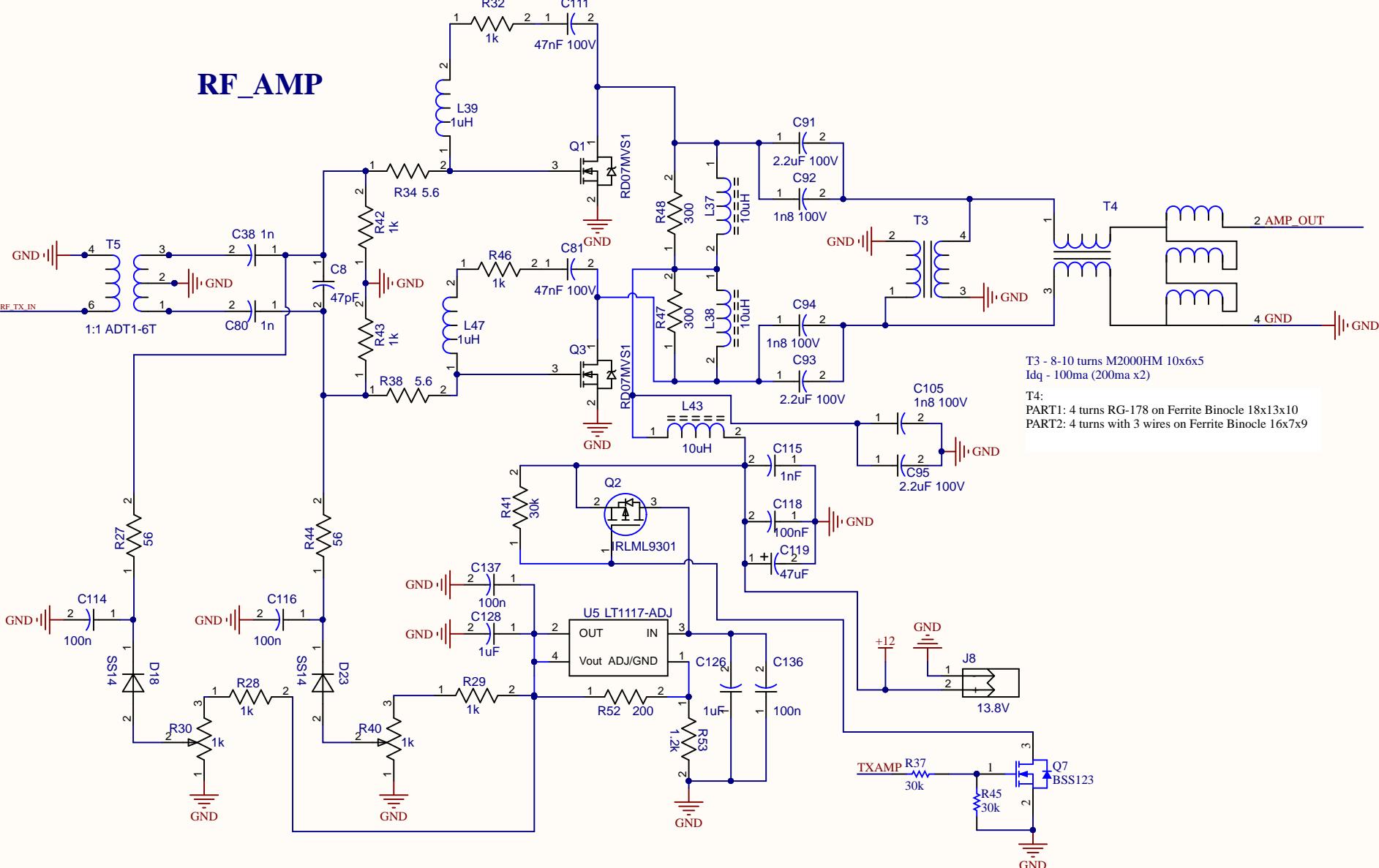




Title LNA			*
Size: A4	Number: 16	Revision: 2.0	*
Date: 01.12.2020	Time: 14:44:48	Sheet 16 of 18	*
File: C:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\RF-UNIT\LNA.SchDoc			

Altium

RF_AMP



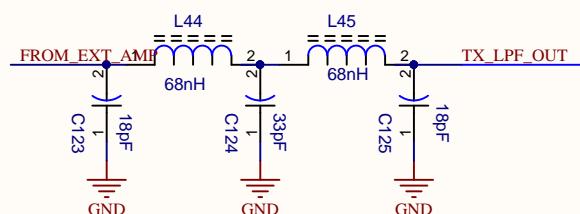
Title **RF_AMP**

Size: A4	Number: 17	Revision: 2.0
Date: 01.12.2020	Time: 14:44:49	Sheet 17 of 18
File: C:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\RF-UNIT\RF_AMP.SchDoc		*

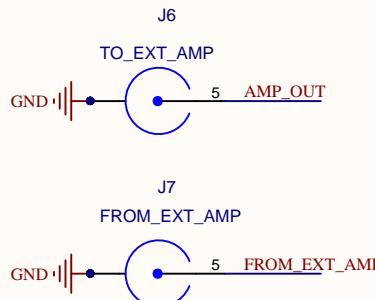
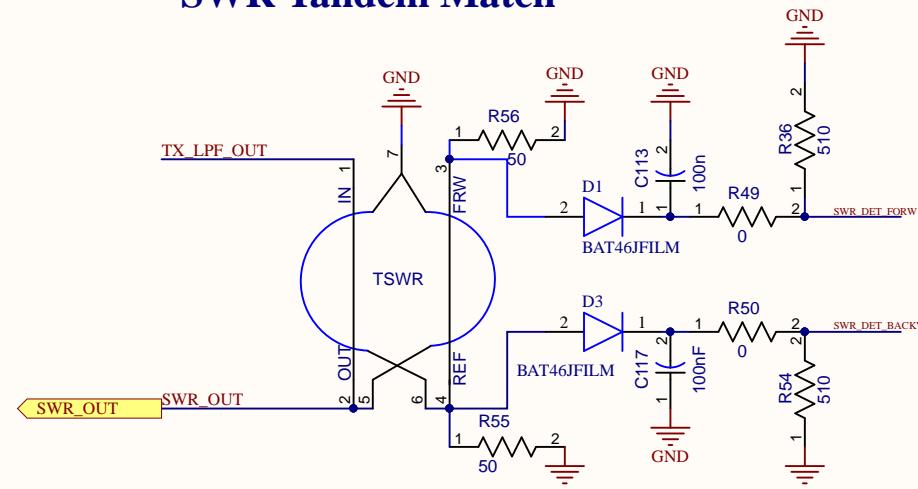
Altium

TX_LPF

LPF 170Mhz



Катушки намотаны проводом 0.5мм на оправке диаметром 5мм, 3.5 витка

**SWR Tandem Match**

2 провода 0.9мм пропущено через бинокль

по диагонали пропущено и намотано по 10 витков 0.5мм

Title **SWR Tandem Match**

Size: A4 Number:18 Revision:2.0

Date: 01.12.2020 Time: 14:44:49 Sheet 18 of 18

File: C:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\RF-UNIT\SWR Tandem Match.SchDoc

Altium