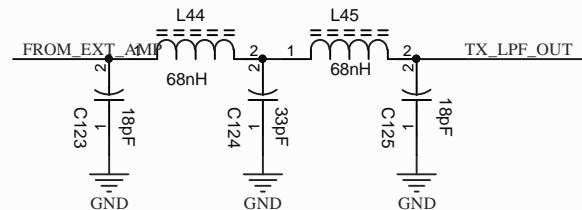
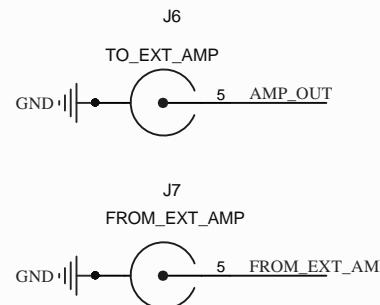
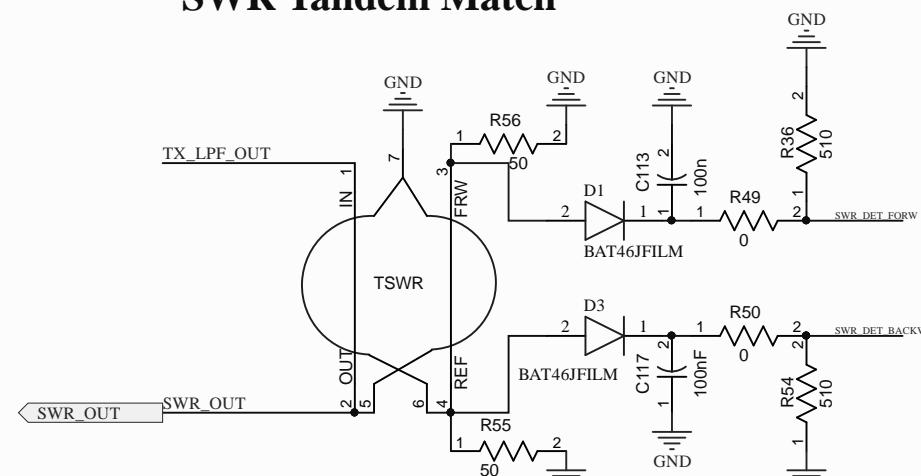


TX_LPF

LPF 170Mhz



Катушки намотаны проводом 0.5мм на оправке диаметром 5мм, 3.5 витка

**SWR Tandem Match**

2 провода 0.9мм пропущено через бинокль

по диагонали пропущено и намотано по 10 витков 0.5мм

Title **SWR Tandem Match**

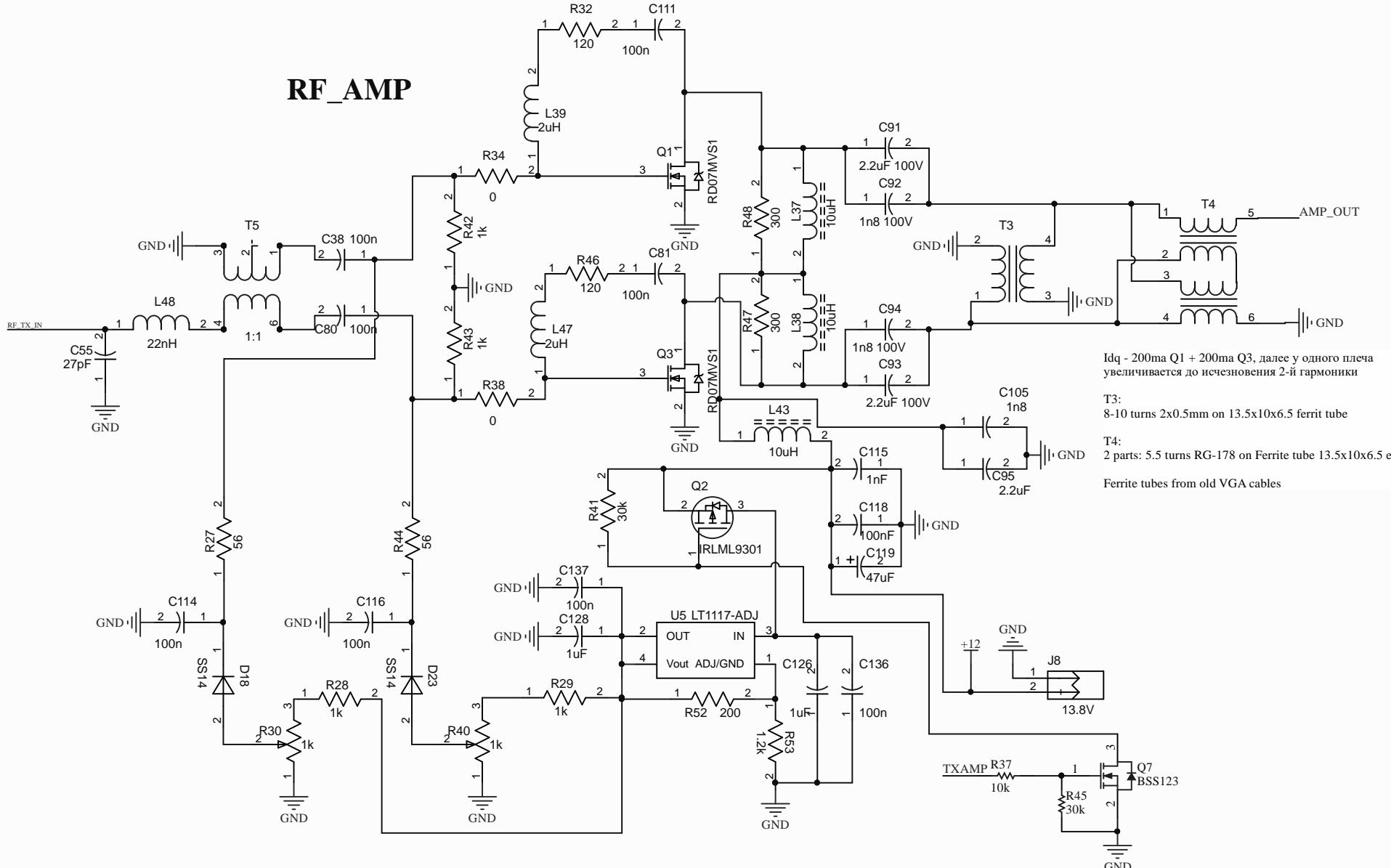
Size: A4 Number:18 Revision:2.0

Date: 08.01.2021 Time: 2:15:34 Sheet:18 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\SWR Tandem Match.SchDoc

Altium

A

RF_AMP

Idq - 200mA Q1 + 200mA Q3, далее у одного плеча
увеличивается до исчезновения 2-й гармоники

T3:
8-10 turns 2x0.5mm on 13.5x10x6.5 ferrite tube

T4:
2 parts: 5.5 turns RG-178 on Ferrite tube 13.5x10x6.5 each

Ferrite tubes from old VGA cables

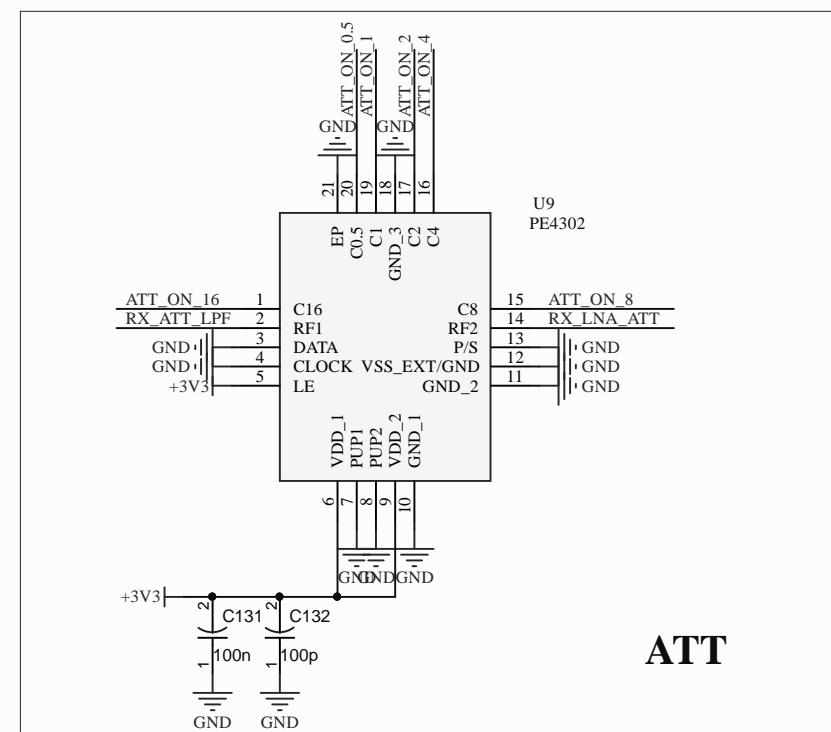
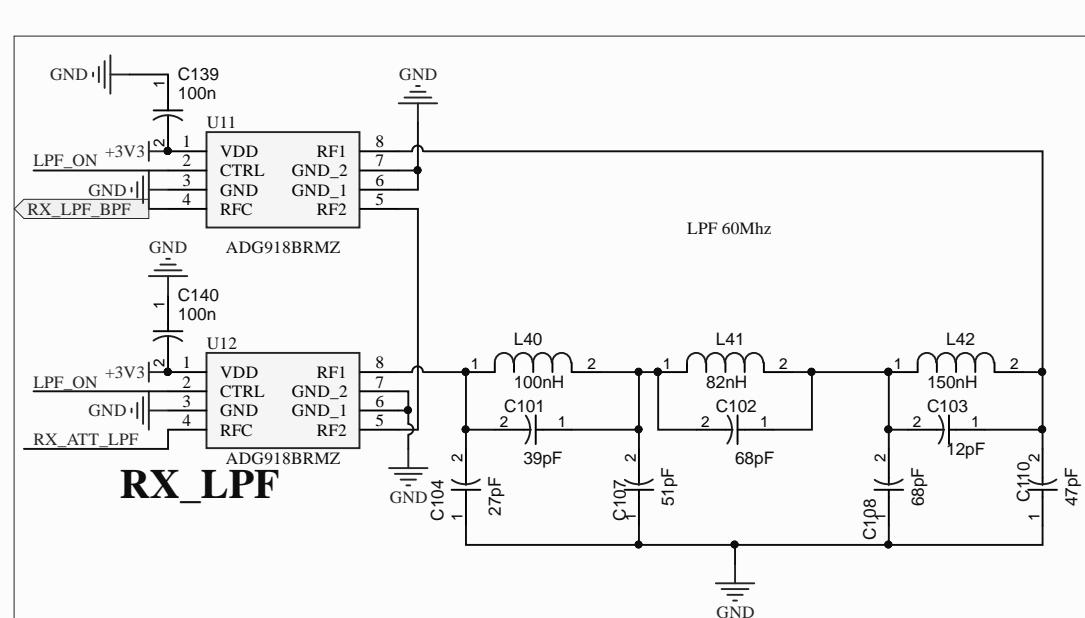
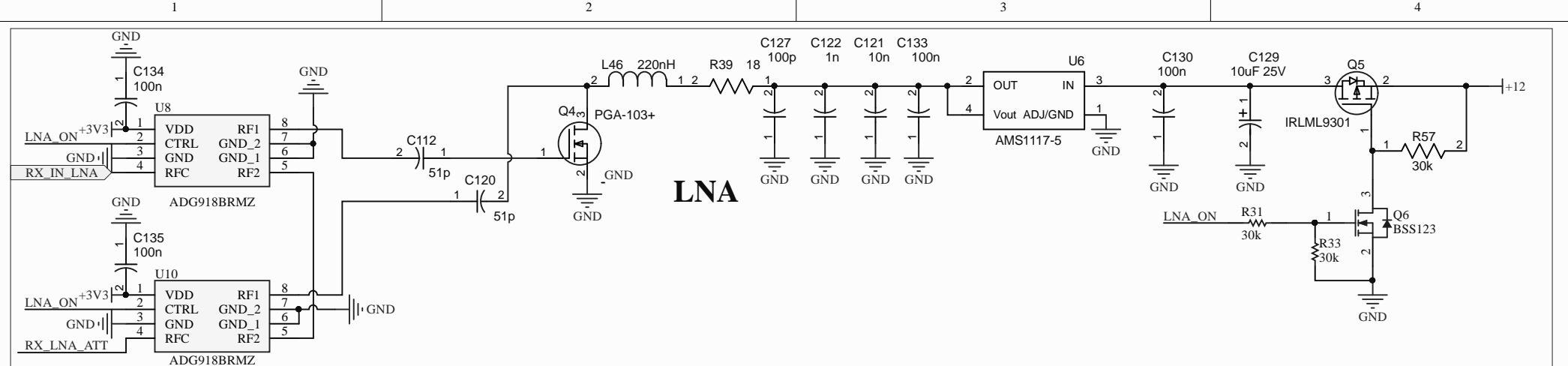
Title **RF_AMP**

Size: A4 Number:17 Revision:2.0

Date: 08.01.2021 Time: 2:15:36 Sheet:17 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\RF_AMP.SchDoc

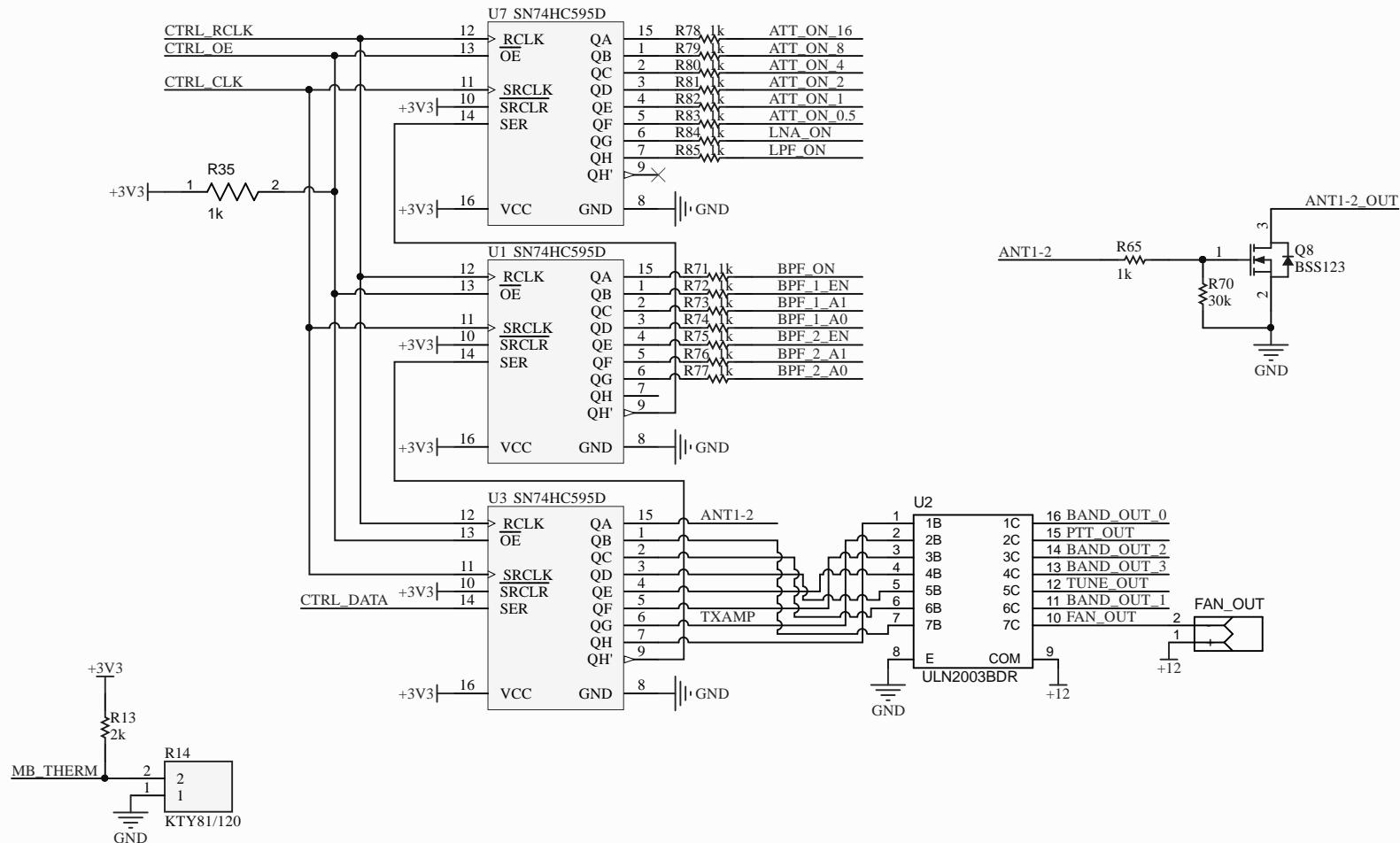
Altium



Title LNA			*
Size: A4	Number: 16	Revision: 2.0	*
Date: 08.01.2021	Time: 2:15:38	Sheet 16 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\LNA.SchDoc			

Altium

A

Title **CONTROL**

Size: A4 Number:15 Revision:2.0

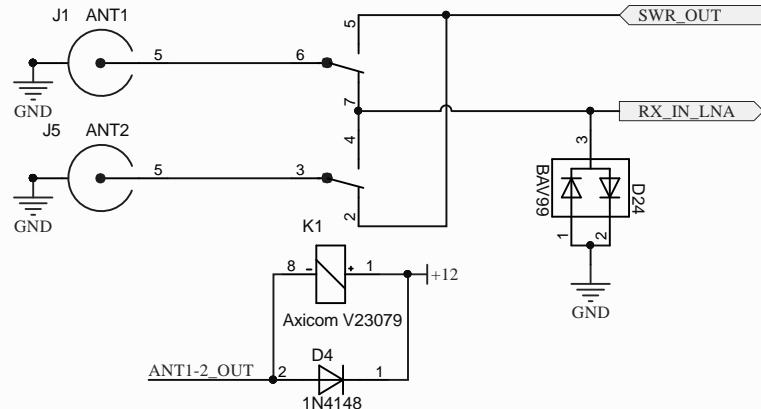
Date: 08.01.2021 Time: 2:15:40 Sheet:15 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONTROL.SchDoc

Altium

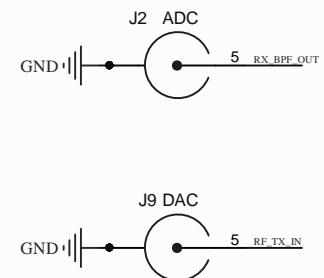
A

TXRX_COMUTATOR

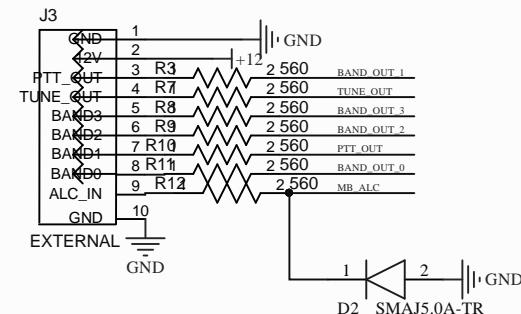
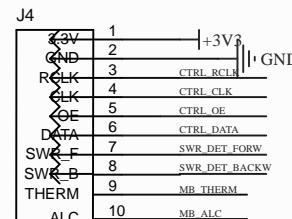


B

CONN



C

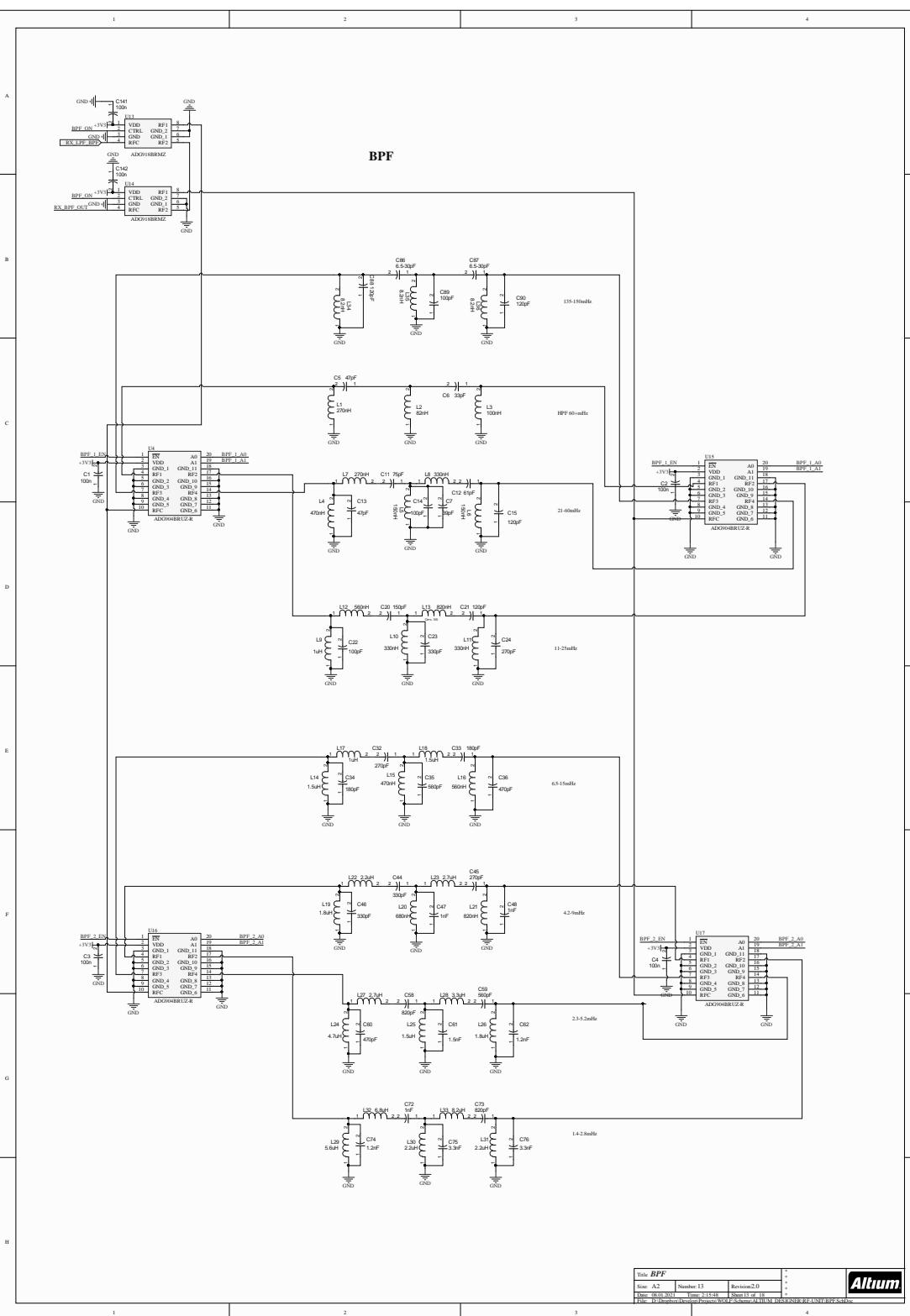


D

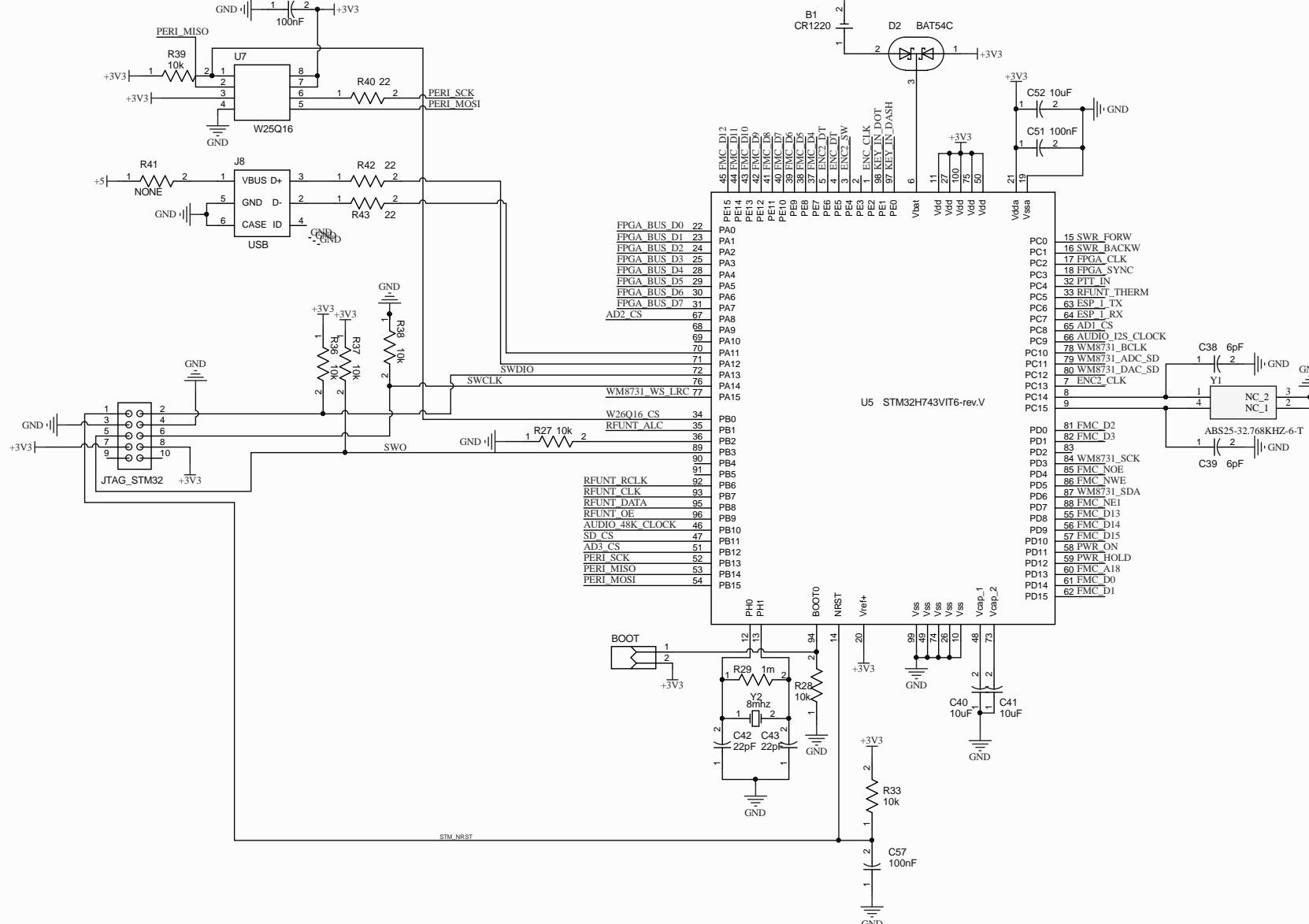
Title **CONN**

Size: A4	Number:14	Revision2.0
Date: 08.01.2021	Time: 2:15:44	Sheet14 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONN.SchDoc		*

Altium



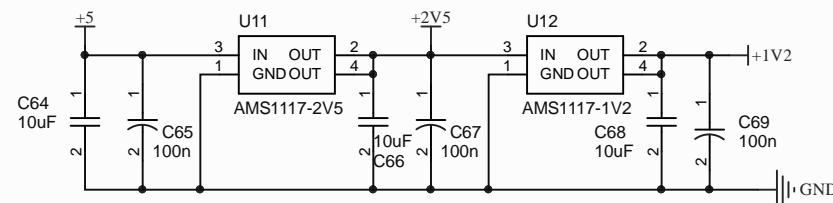
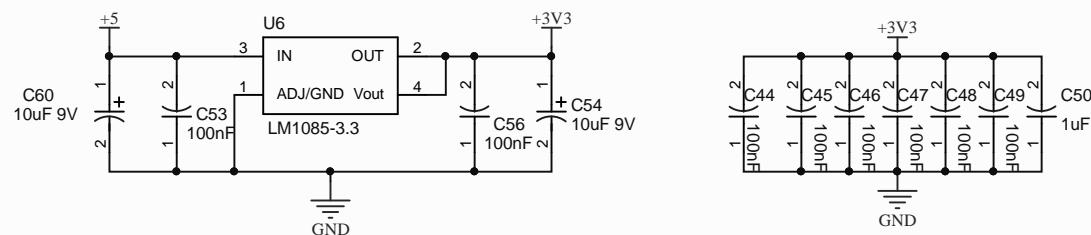
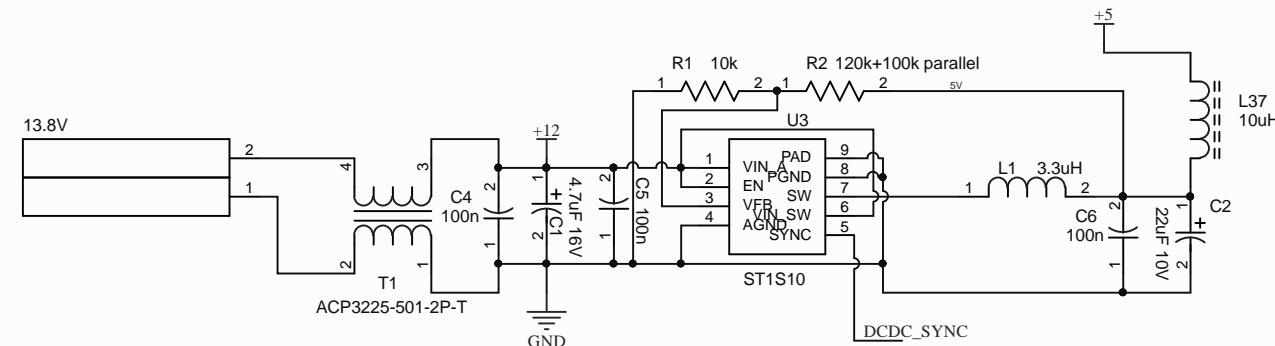
STM32



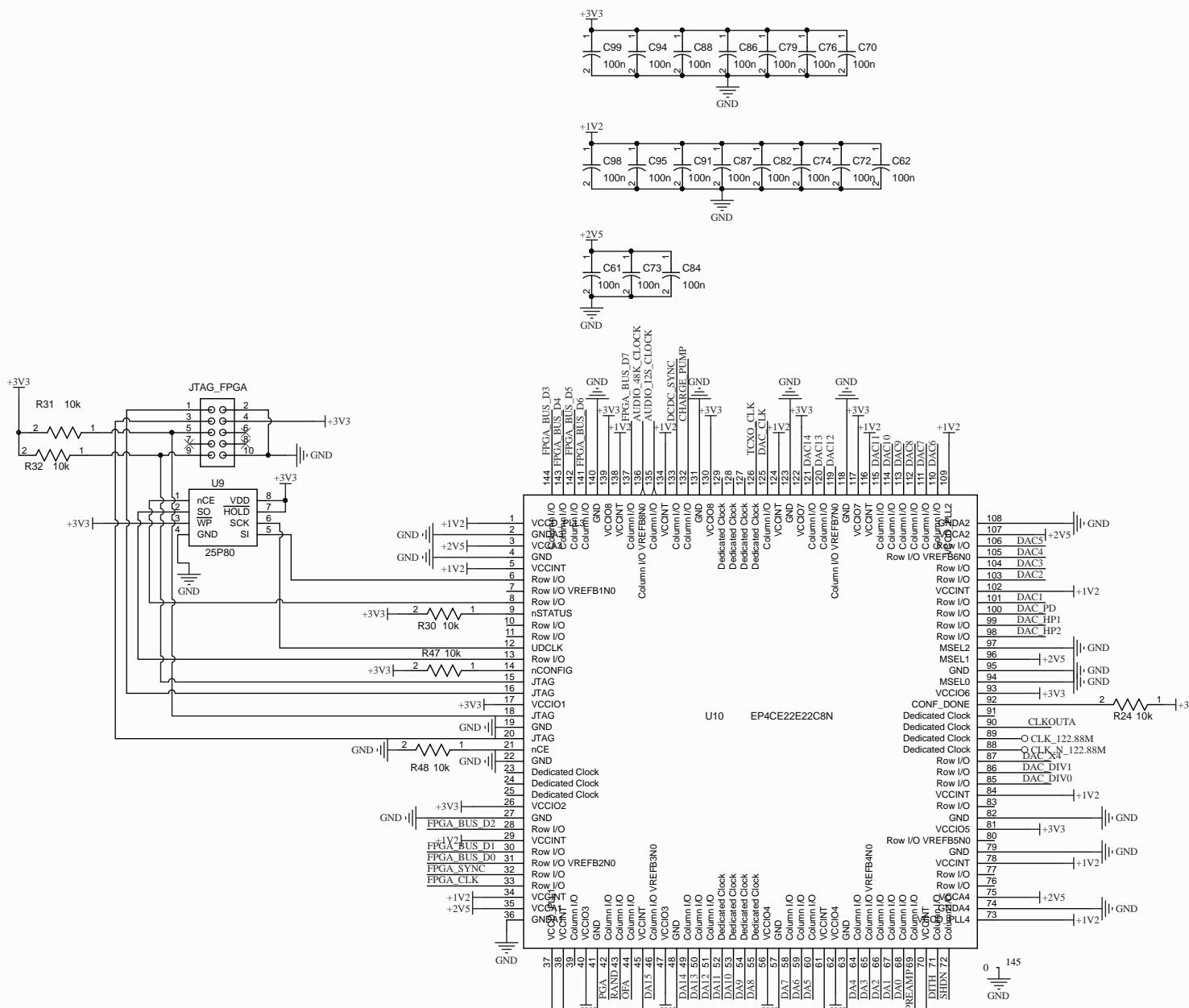
Title STM32

Size: A3 Number: 11 Revision 2.0
Date: 08.01.2021 Time: 2:15:53 Sheet 11 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM DES

Altium



Title POWER	*		
Size: A4	Number:10	Revision2.0	*
Date: 08.01.2021	Time: 2:15:56	Sheet10 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\POWER.SchDoc			

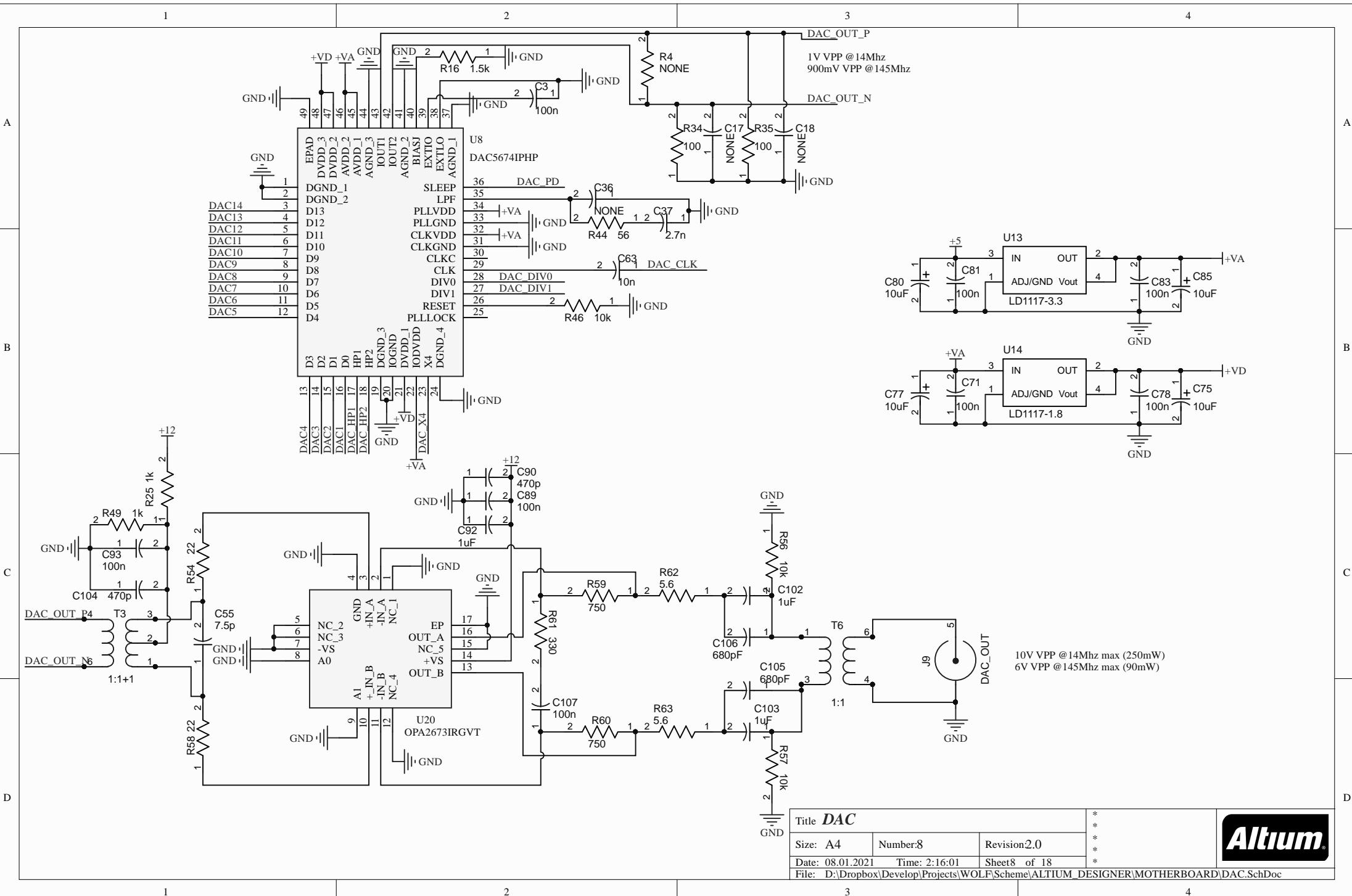


Title *FPGA*

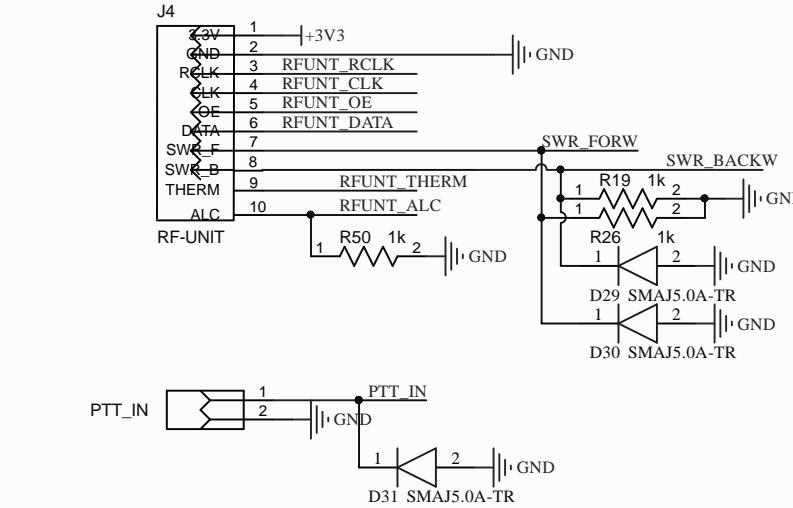
Size: A3
Date: 08.01.20
File: D:\Drop

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*
*
*
Number:9 Revision:2.0
21 Time: 2:15:58 Sheet 9 of 18
box\Develop\Projects\WOLF\Scheme\ALTIUM DESIG

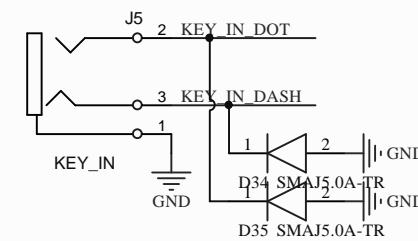
Altium



A



B



C

J6

FMC_NE1	1	CS 3v3
FMC_A18	2	RS 3v3
FMC_NWE	3	WR GND
FMC_NOE	4	RD GND
STM_NRST	5	RSTMISO
FMC_D0	6	DB0MOSI
FMC_D1	7	DB1WRX
FMC_D2	8	DB2WTX
FMC_D3	9	DB3CLK
FMC_D4	10	DB\$DCS
FMC_D5	11	DB\$AD1C
FMC_D6	12	DB\$AD2C
FMC_D7	13	DB7E2A
FMC_D8	14	DB8E2B
FMC_D9	15	DB9E2S
FMC_D10	16	DB10E1A
FMC_D11	17	DB11E1B
FMC_D12	18	DBAD3C
FMC_D13	19	DB135v
FMC_D14	20	DB1GND
FMC_D15	21	DB1GND
PWR_ON	22	PONCAS
PWR_HOLD	23	PHOCAS
	24	3v3

FPC-45P-0.5mm

A

B

D

D

Title **CONNECTIONS**

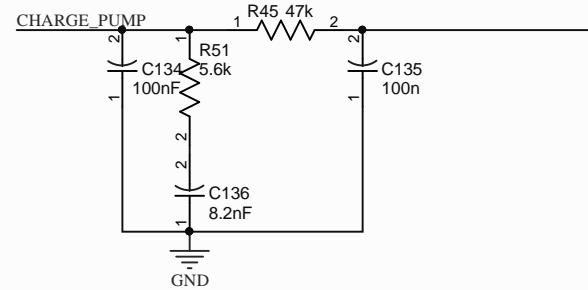
Size: A4 Number:7 Revision 2.0

Date: 08.01.2021 Time: 2:16:04 Sheet 7 of 18

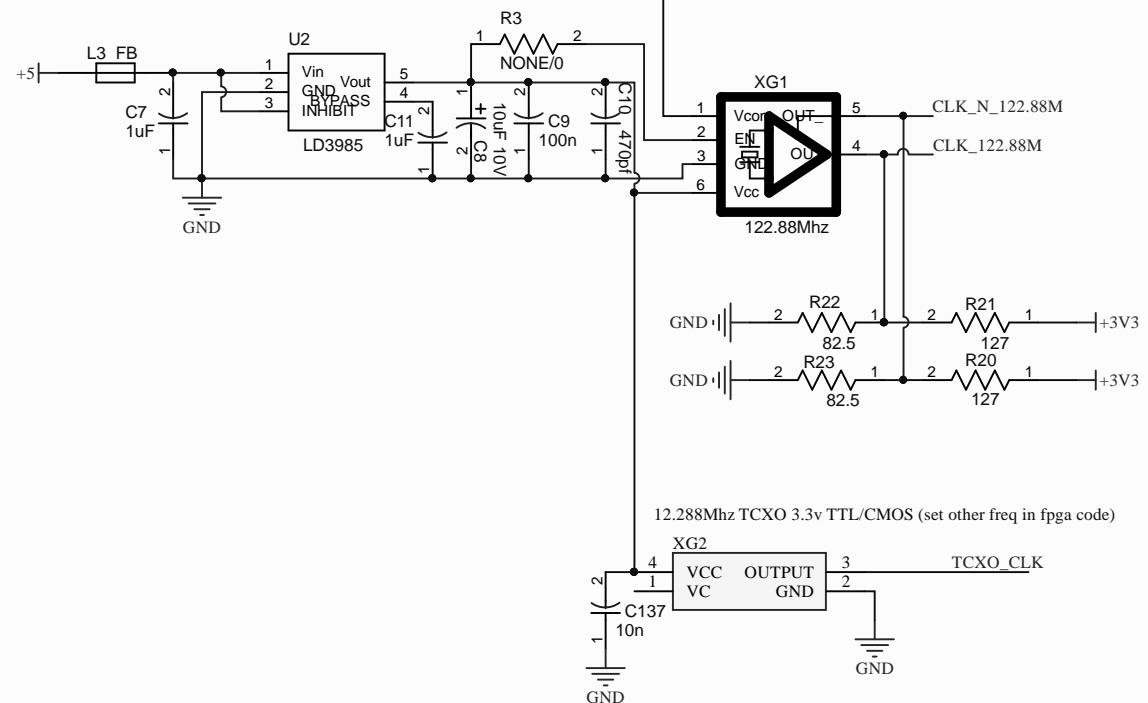
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\CONNECTIONS.SchDoc

Altium

A



B



C

D

Title **CLOCK_GENERATOR**

Size: A4 Number:6 Revision 2.0

Date: 08.01.2021 Time: 2:16:06 Sheet 6 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\MOTHERBOARD\CLOCK_GENERATOR.SchDoc

Altium

A

B

C

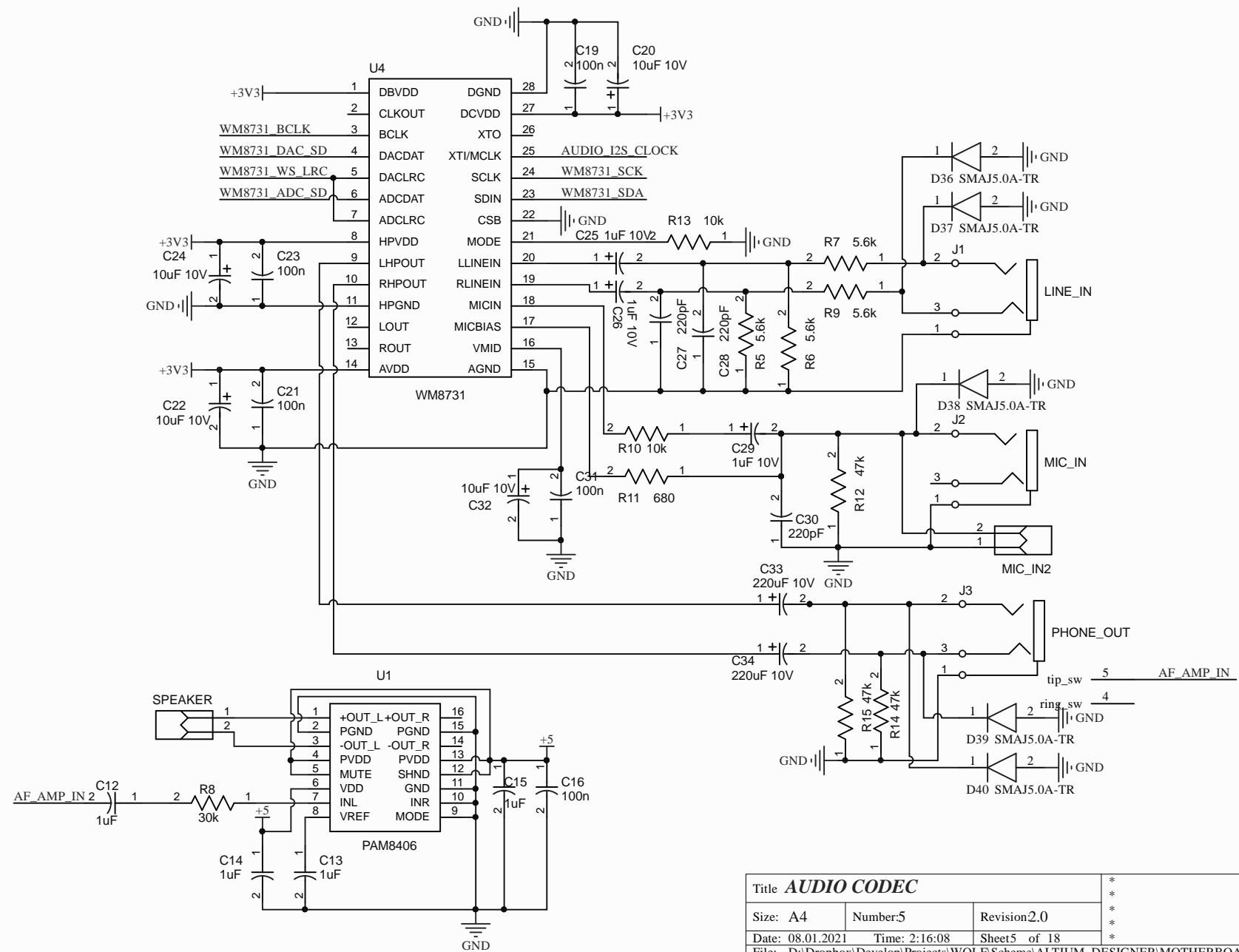
D

A

B

C

D

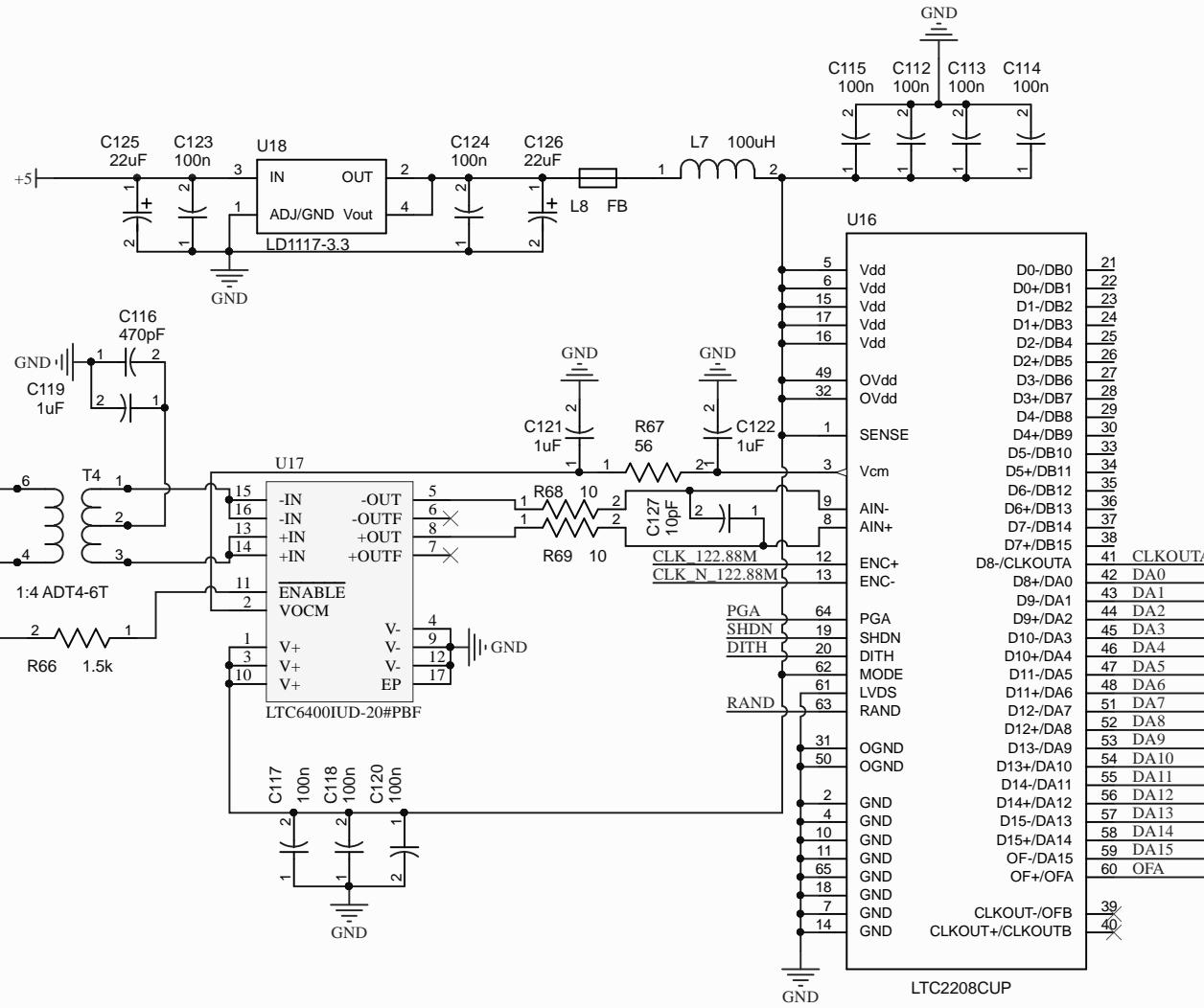
Title **AUDIO CODEC**

Size: A4 Number:5 Revision:2.0

Date: 08.01.2021 Time: 2:16:08 Sheet 5 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\AUDIO_CODEC.SchDoc

Altium

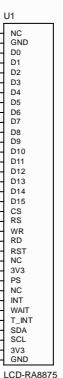
Title **ADC**

Size: A4 Number:3 Revision 2.0

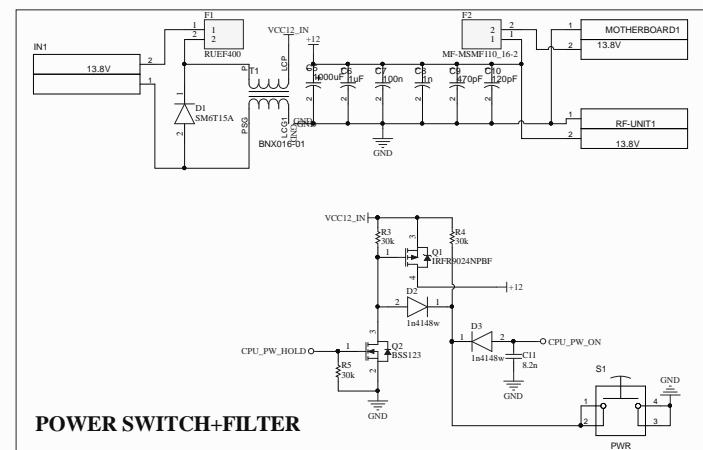
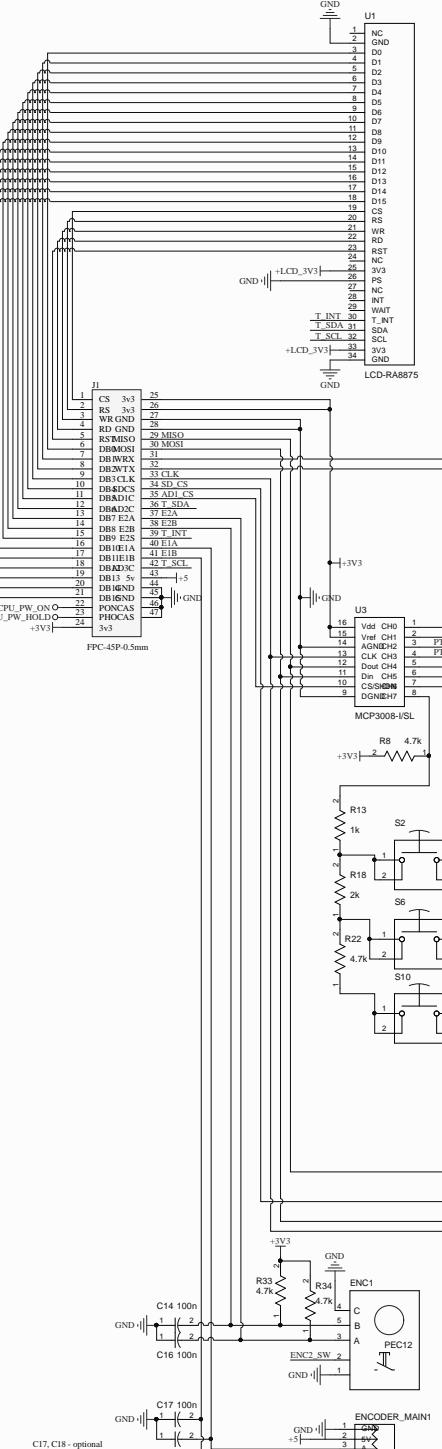
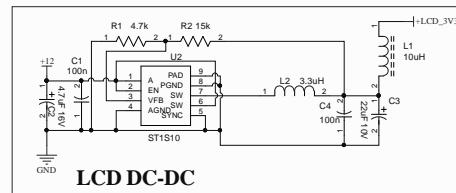
Date: 08.01.2021 Time: 2:16:11 Sheet 3 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\ADC.SchDoc

Altium



7Inch version



TANGENT

