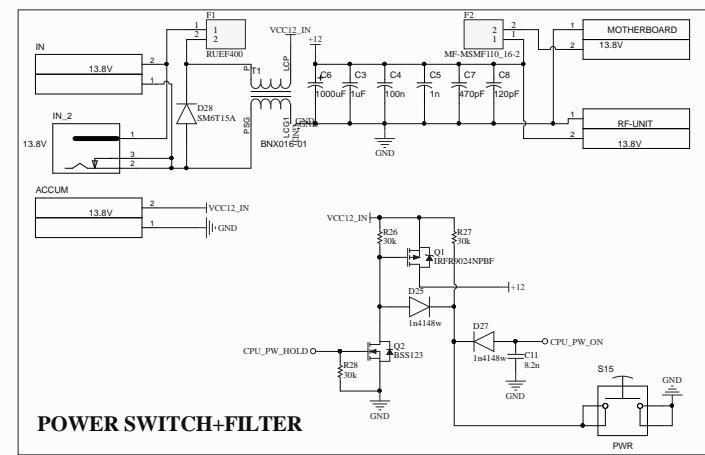
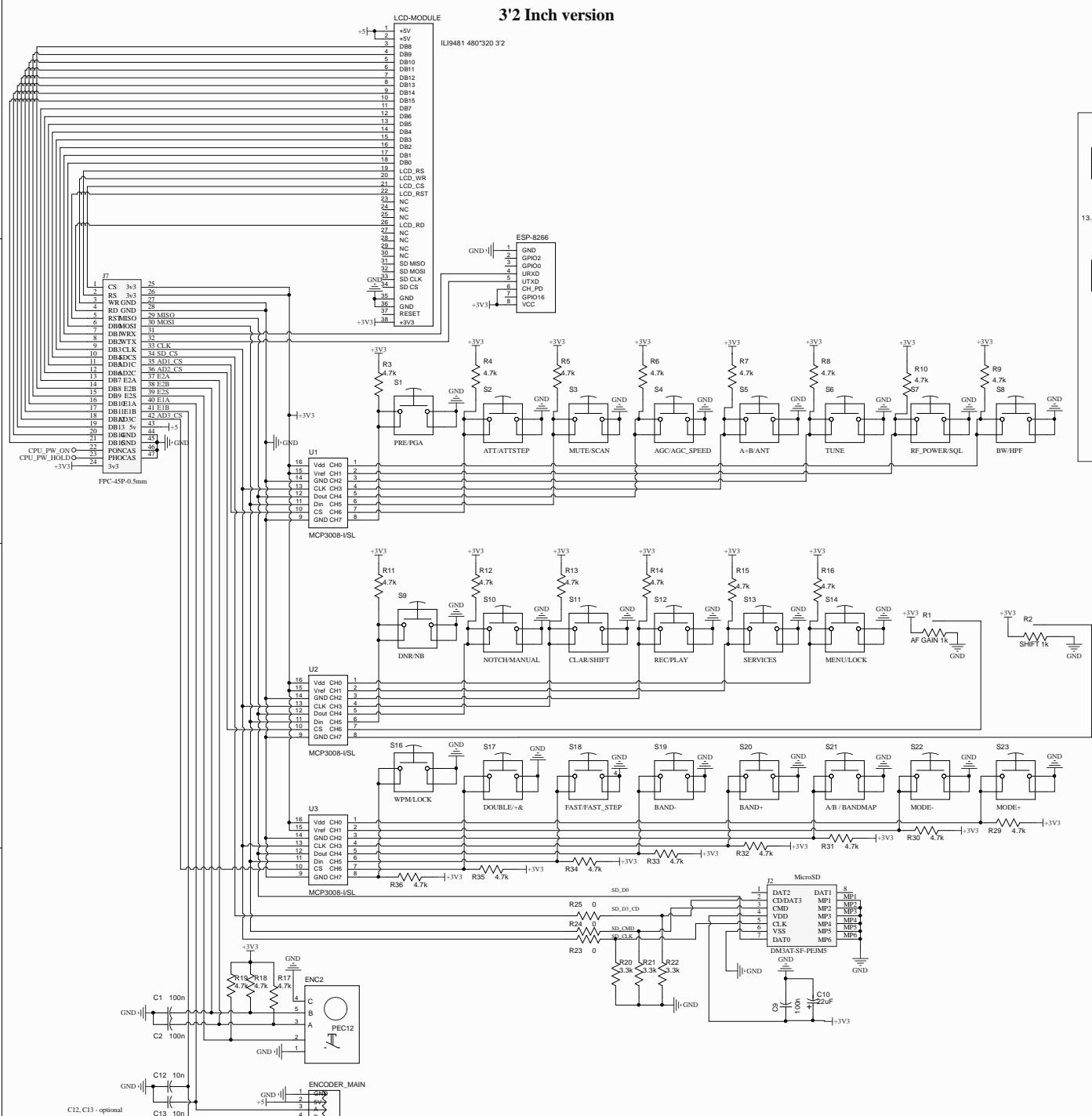


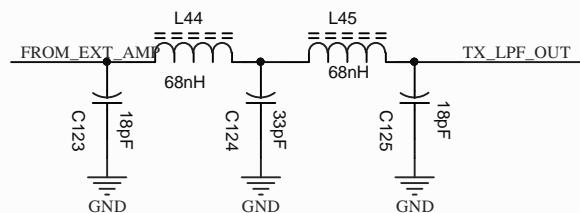
3'2 Inch version



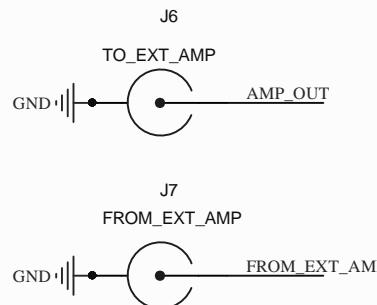
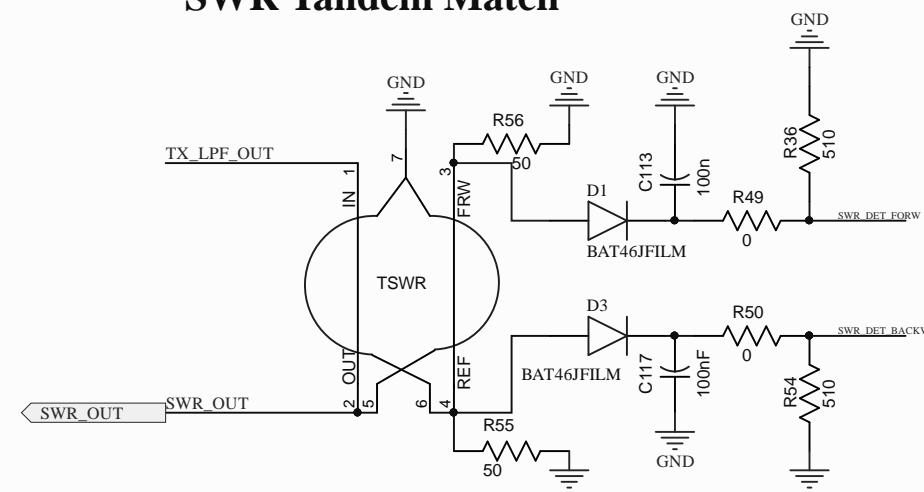
POWER SWITCH+FILTER

TX_LPF

LPF 170Mhz



Катушки намотаны проводом 0.5мм на оправке диаметром 5мм, 3.5 витка

**SWR Tandem Match**Title **SWR Tandem Match**

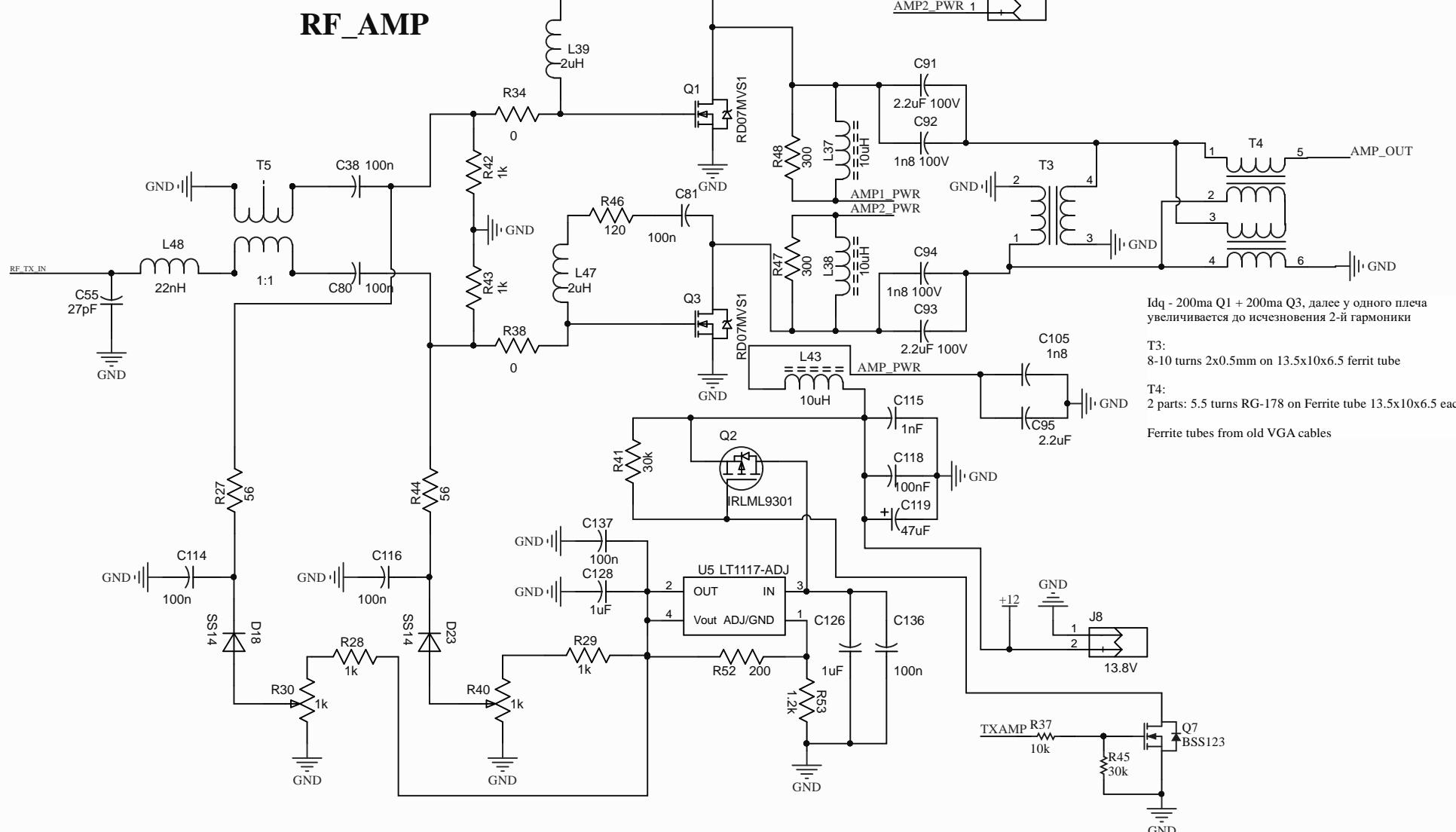
Size: A4 Number:18 Revision:2.0

Date: 21.05.2021 Time: 13:38:08 Sheet:18 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\SWR Tandem Match.SchDoc

Altium

A

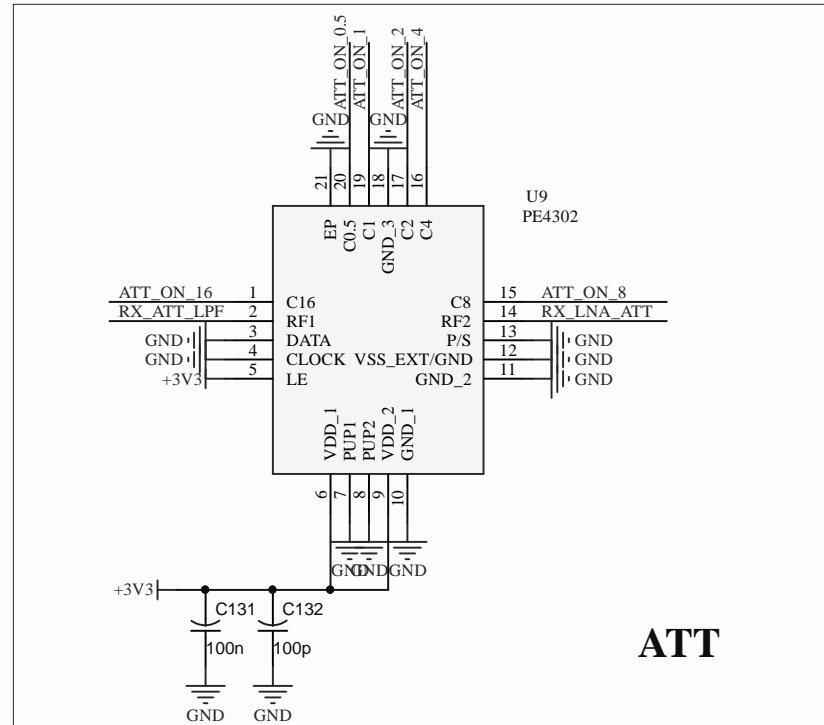
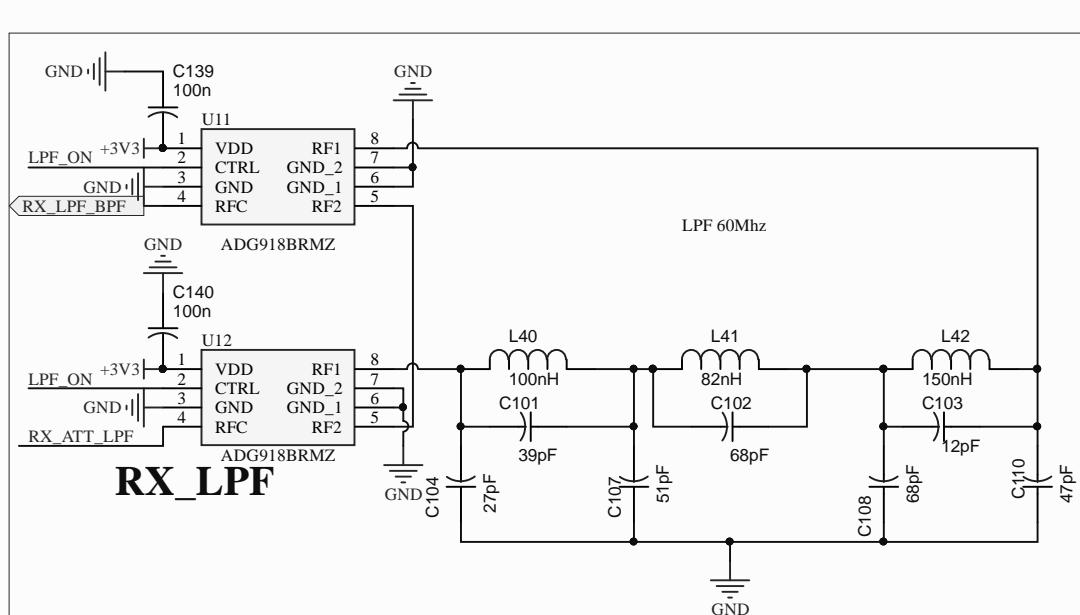
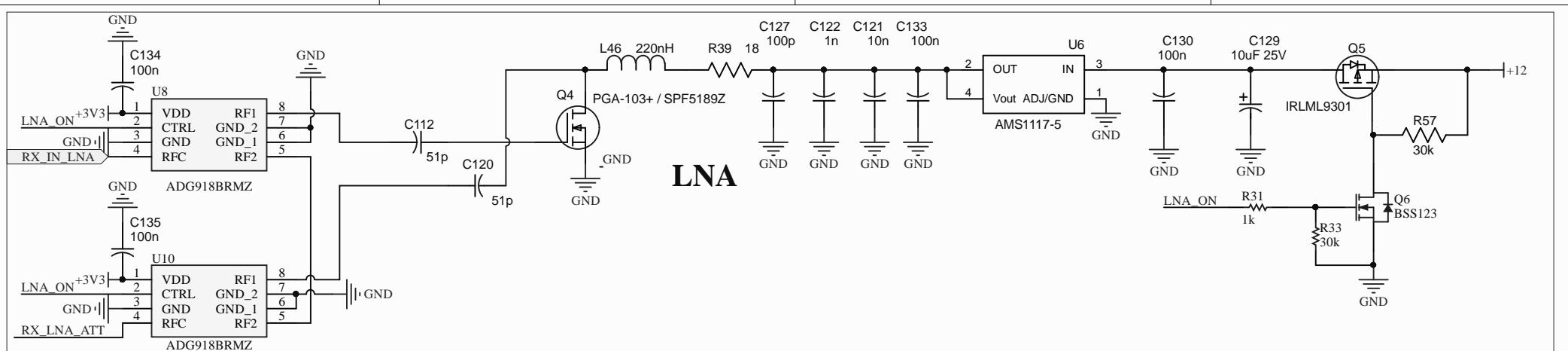
RF_AMPTitle **RF_AMP**

Size: A4 Number:17 Revision:2.0

Date: 21.05.2021 Time: 13:38:09 Sheet:17 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\RF_AMP.SchDoc

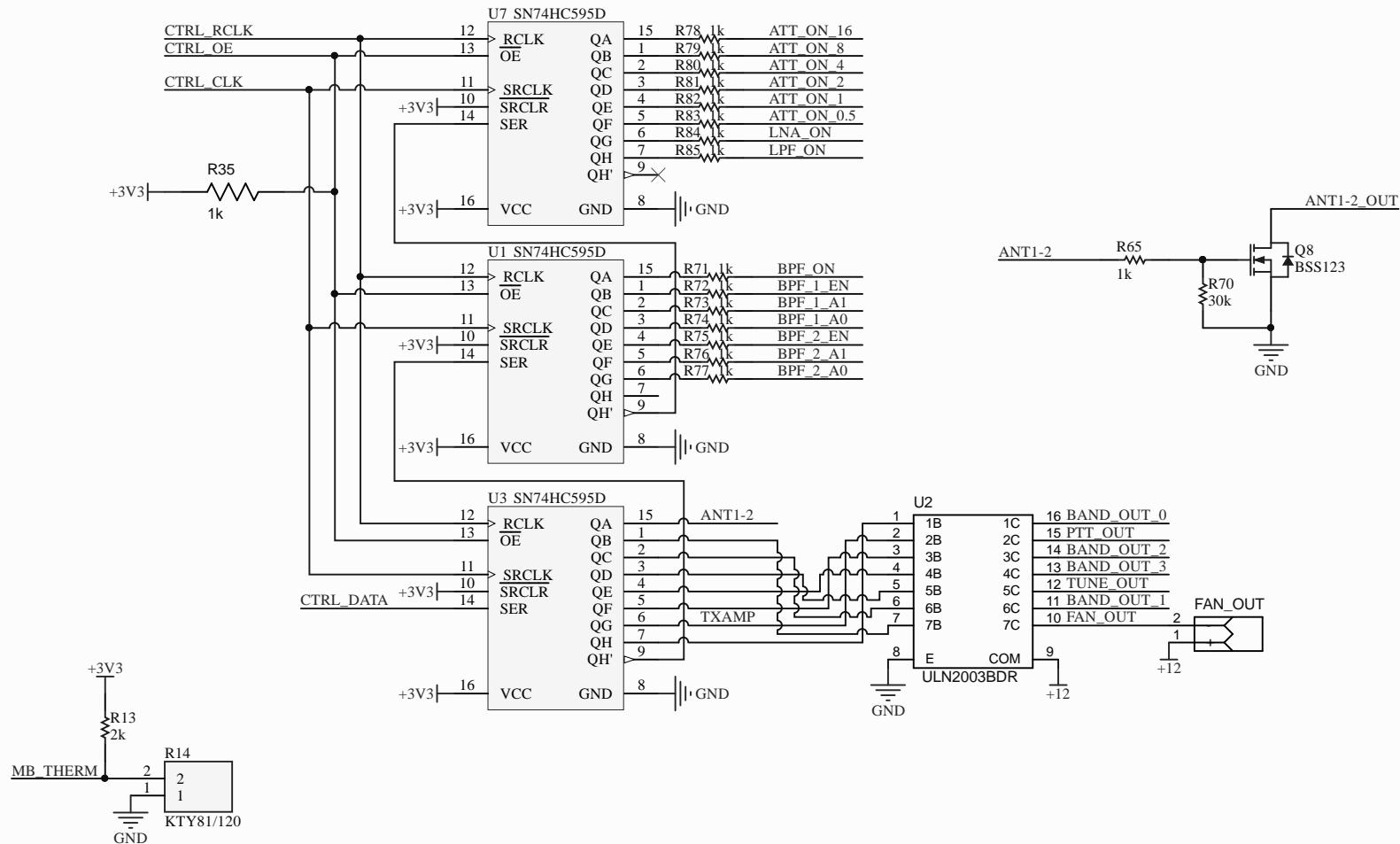
Altium



Title LNA			*
Size: A4	Number:16	Revision2.0	*
Date: 21.05.2021	Time: 13:38:11	Sheet16 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\RF-UNIT\LNA.SchDoc			

Altium

A

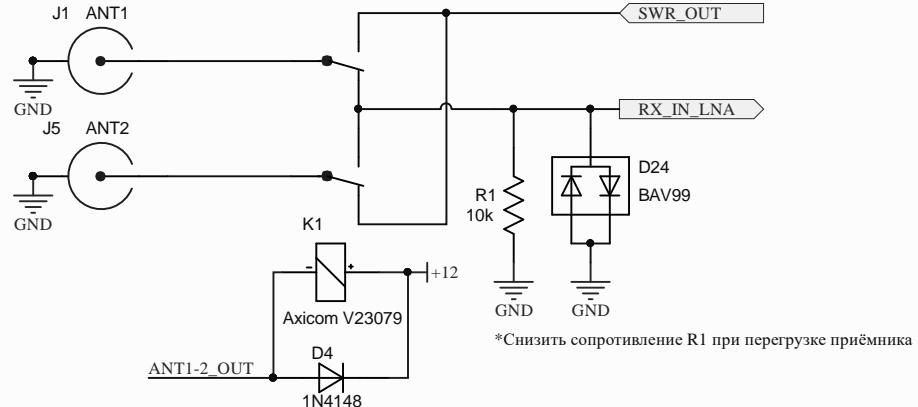
Title **CONTROL**

Size: A4	Number:15	Revision2.0
Date: 21.05.2021	Time: 13:38:13	Sheet15 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONTROL.SchDoc		*

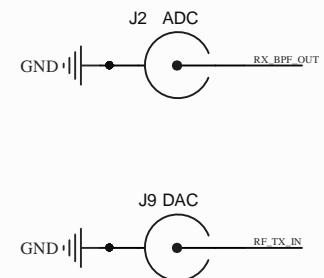
Altium

A

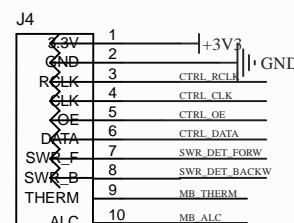
TXRX_COMUTATOR



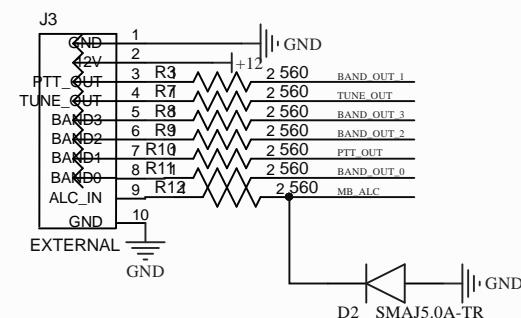
CONN



B



MOTHERBOARD



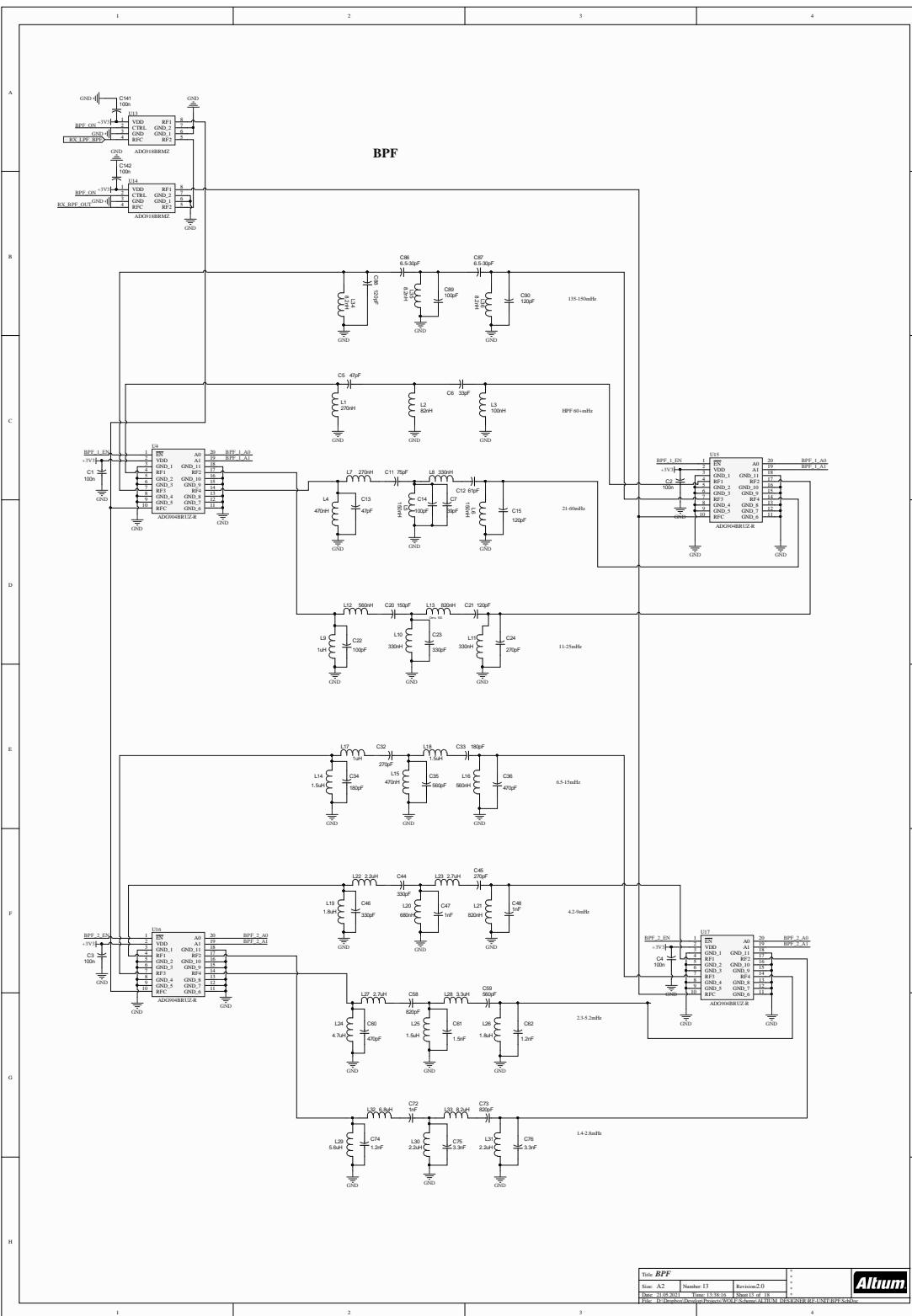
C

Title **CONN**

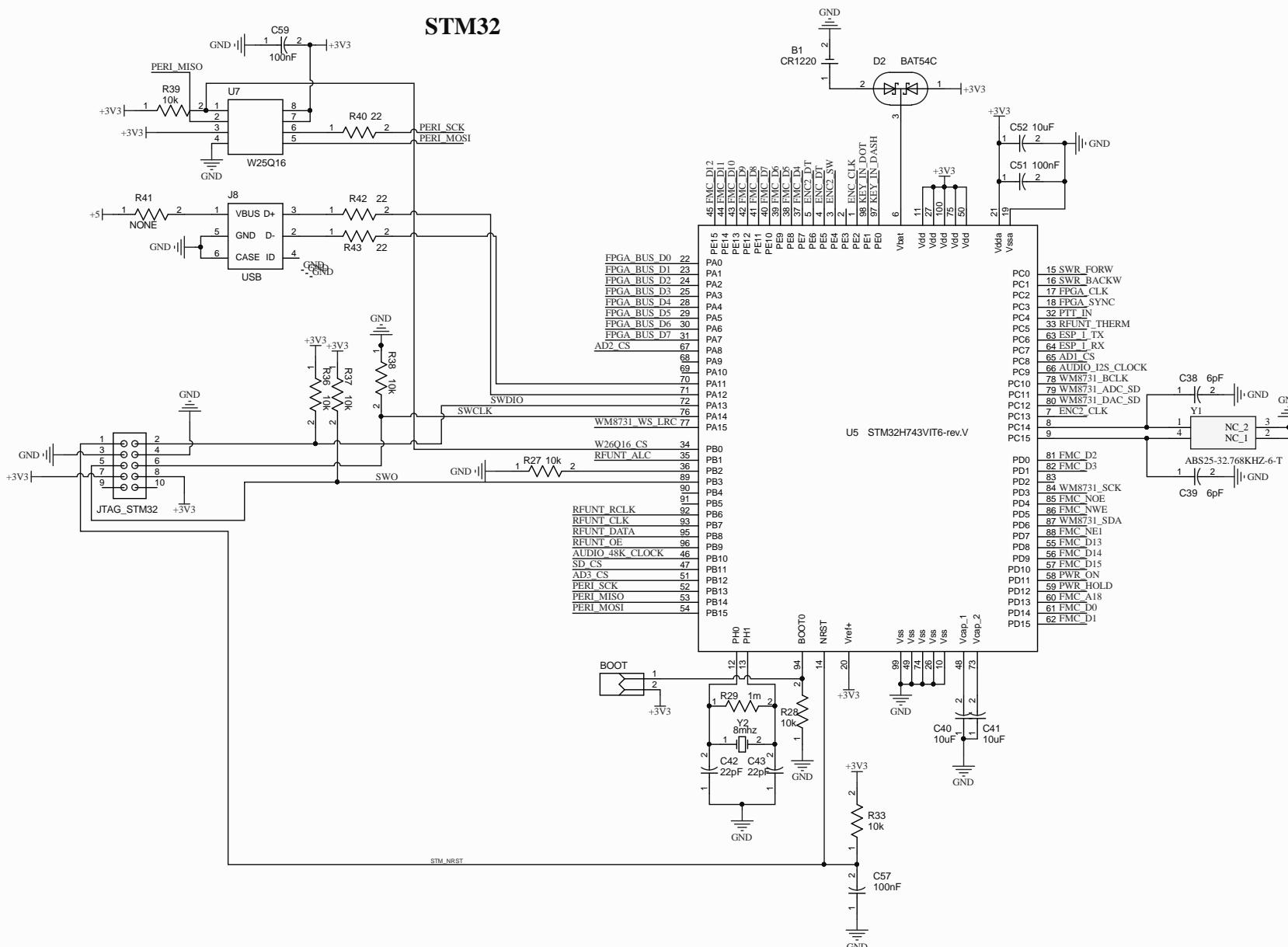
Size: A4 Number:14 Revision:2.0

Date: 21.05.2021 Time: 13:38:14 Sheet:14 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONN.SchDoc



STM32

Title **STM32**

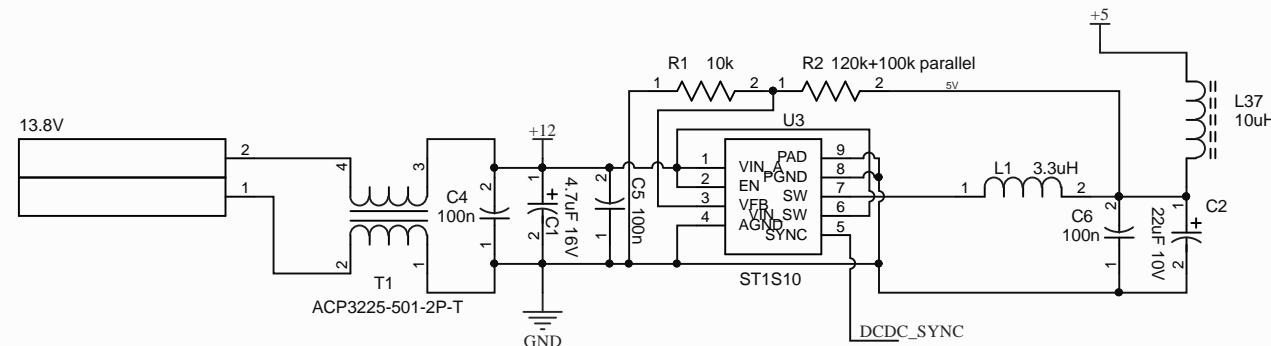
Size: A3 Number: 11 Revision: 2.0

Date: 21.05.2021 Time: 13:38:19 Sheet 11 of 18

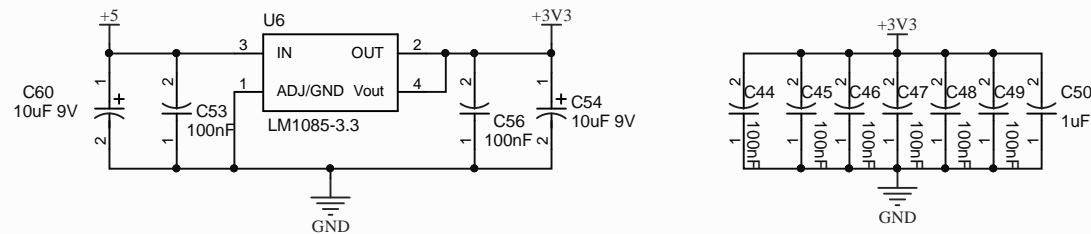
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM DESIGNER\MOTHERBOARD\STM32.SchDoc

Altium

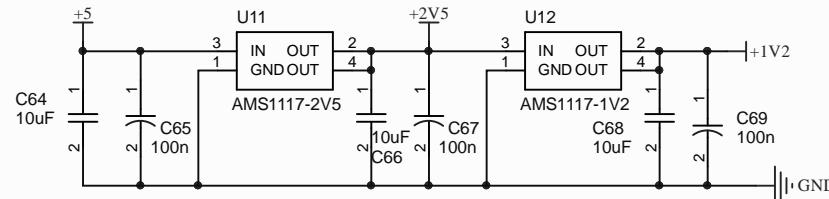
A



B

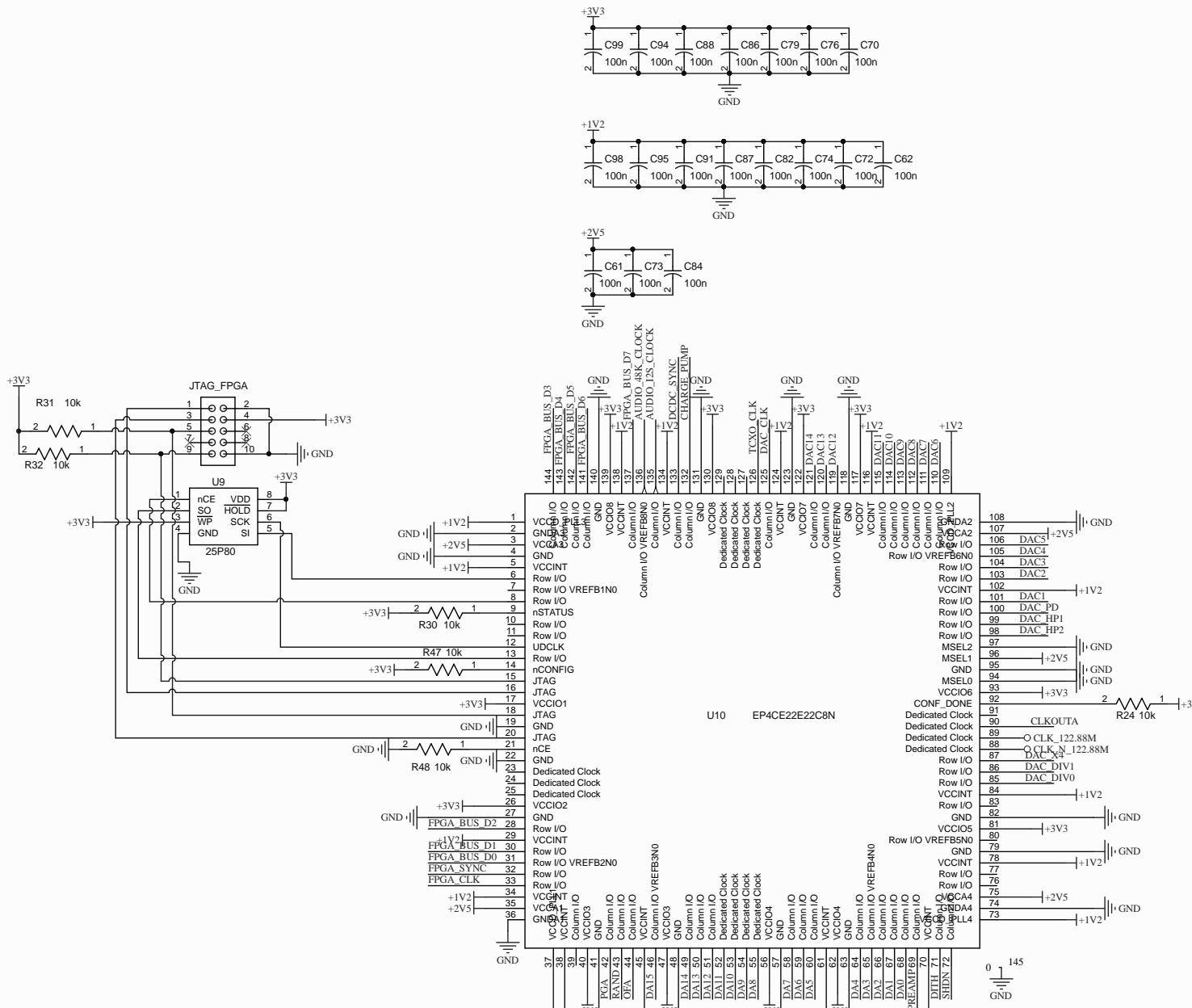


C

Title **POWER**

Size: A4	Number:10	Revision2.0
Date: 21.05.2021	Time: 13:38:21	Sheet10 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\POWER.SchDoc		*

Altium

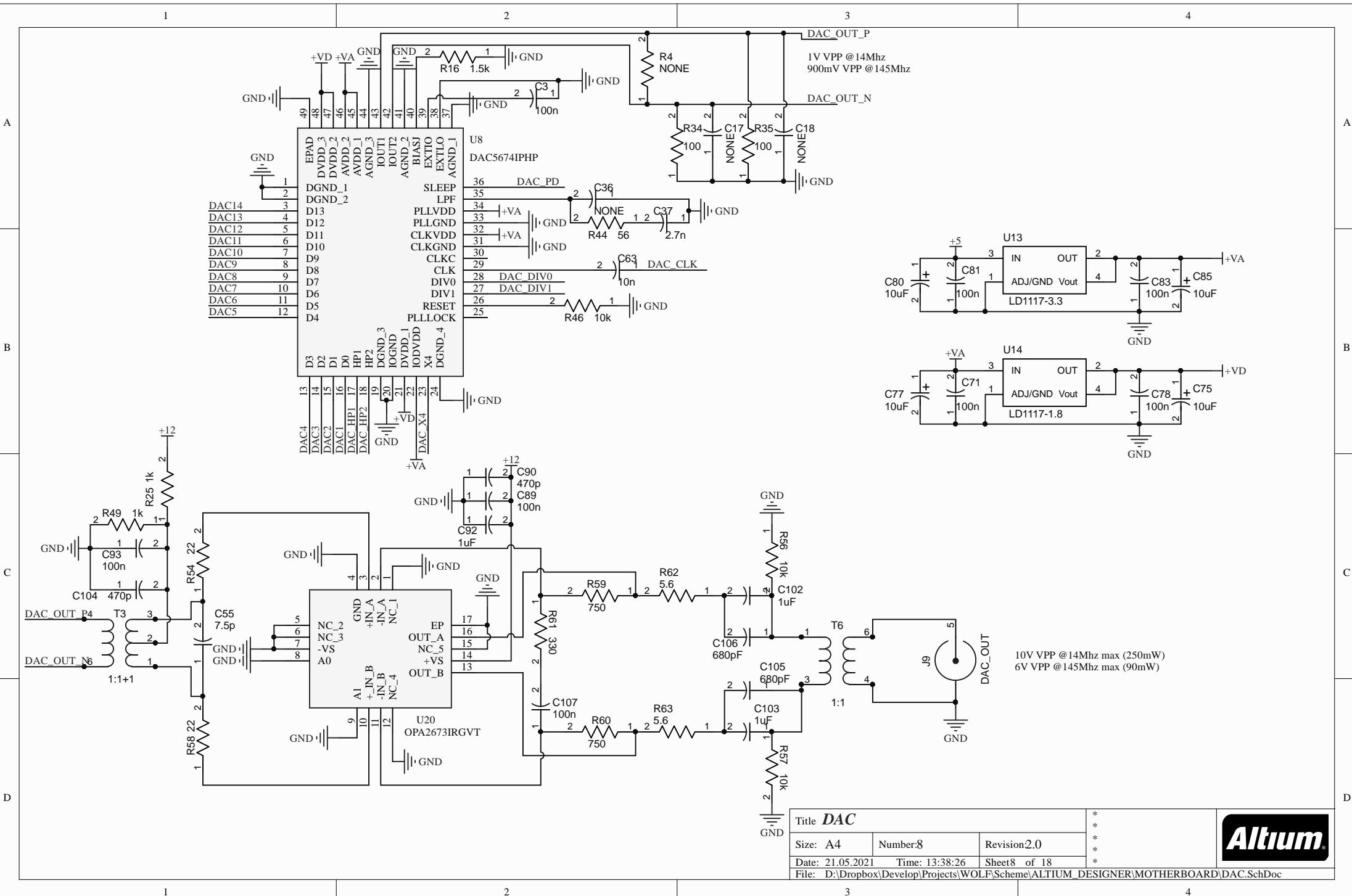


Title **FPGA**

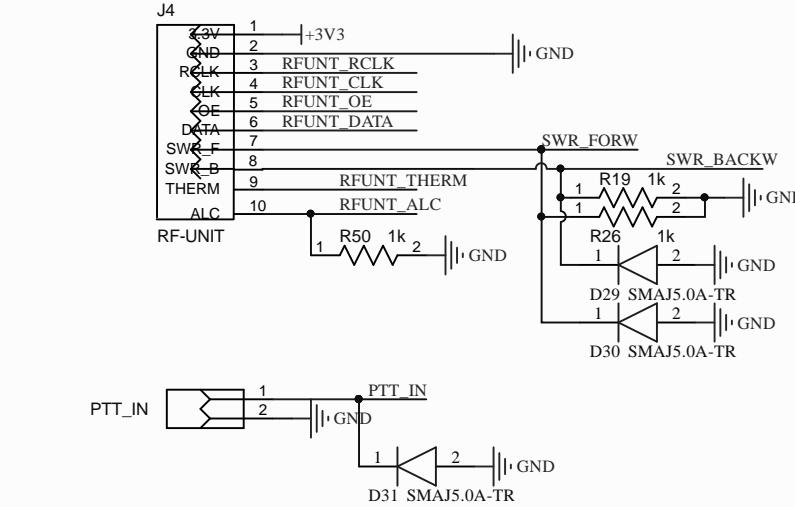
Size: A3

Number:9 Revision:2.0
Time: 13:38:23 Sheet 9 of 18

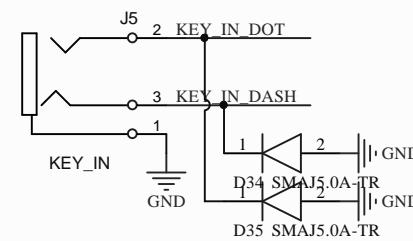
Altium



A



B



C

J6

1	CS 3v3
2	FMC_A18
3	RS 3v3
4	WR GND
5	RD GND
6	STM_NRST
7	RSTMISO
8	FMC_D1
9	FMC_D2
10	FMC_D3
11	FMC_D4
12	FMC_D5
13	FMC_D6
14	FMC_D7
15	FMC_D8
16	FMC_D9
17	FMC_D10
18	FMC_D11
19	FMC_D12
20	FMC_D13
21	FMC_D14
22	PWR_ON
23	PWR_HOLD
24	+3V3
25	+3V3
26	GND
27	PERI_MOSI
28	PERI_MOSI
29	ESP_1_TX
30	ESP_1_RX
31	DB2WRTX
32	DB2SCLK
33	DB3CLK
34	SD_CS
35	AD1_CS
36	AD2_CS
37	ENC2_DT
38	E2A
39	ENC2_CLK
40	DB8_E2B
41	DB9_E2S
42	DB10E1A
43	DB10E1B
44	DBAD3C
45	DB13_5v
46	DB1GND
47	DB1GND

FPC-45P-0.5mm

A

B

C

D

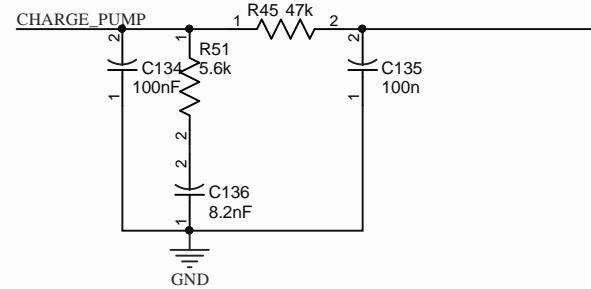
D

Title **CONNECTIONS**

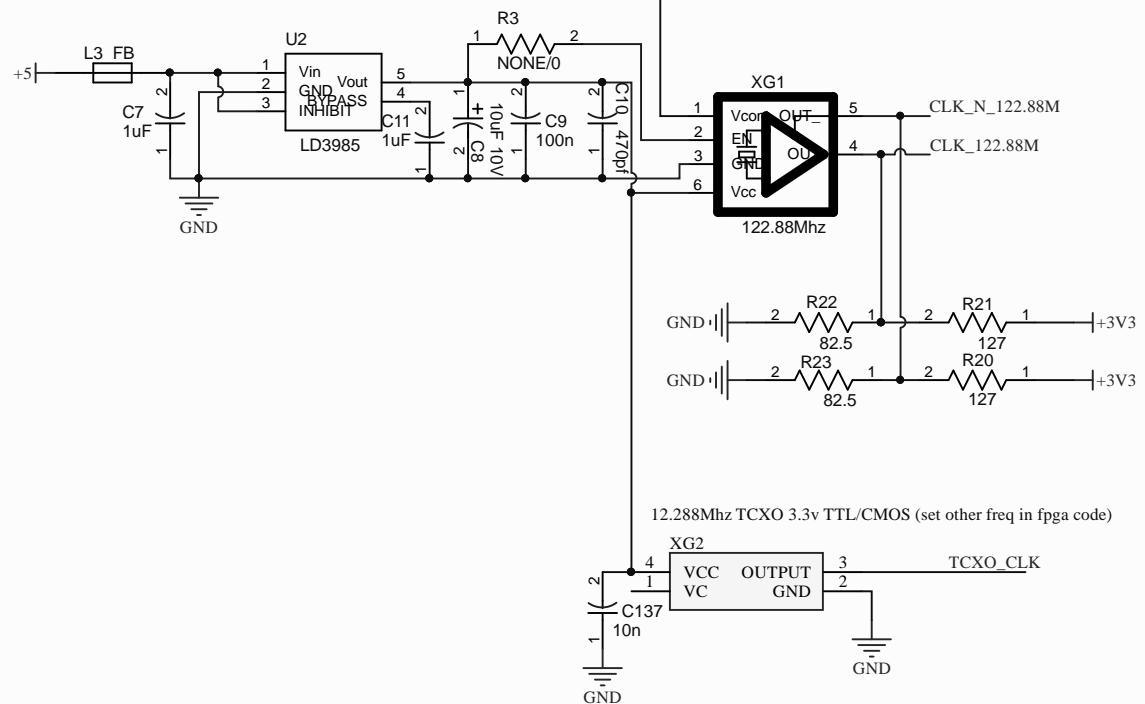
Size: A4	Number: 7	Revision 2.0
Date: 21.05.2021	Time: 13:38:28	Sheet 7 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\CONNECTIONS.SchDoc		

Altium

A



B



C

D

Title **CLOCK_GENERATOR**

Size: A4 Number:6 Revision 2.0

Date: 21.05.2021 Time: 13:38:30 Sheet 6 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\MOTHERBOARD\CLOCK_GENERATOR.SchDoc

Altium

A

B

C

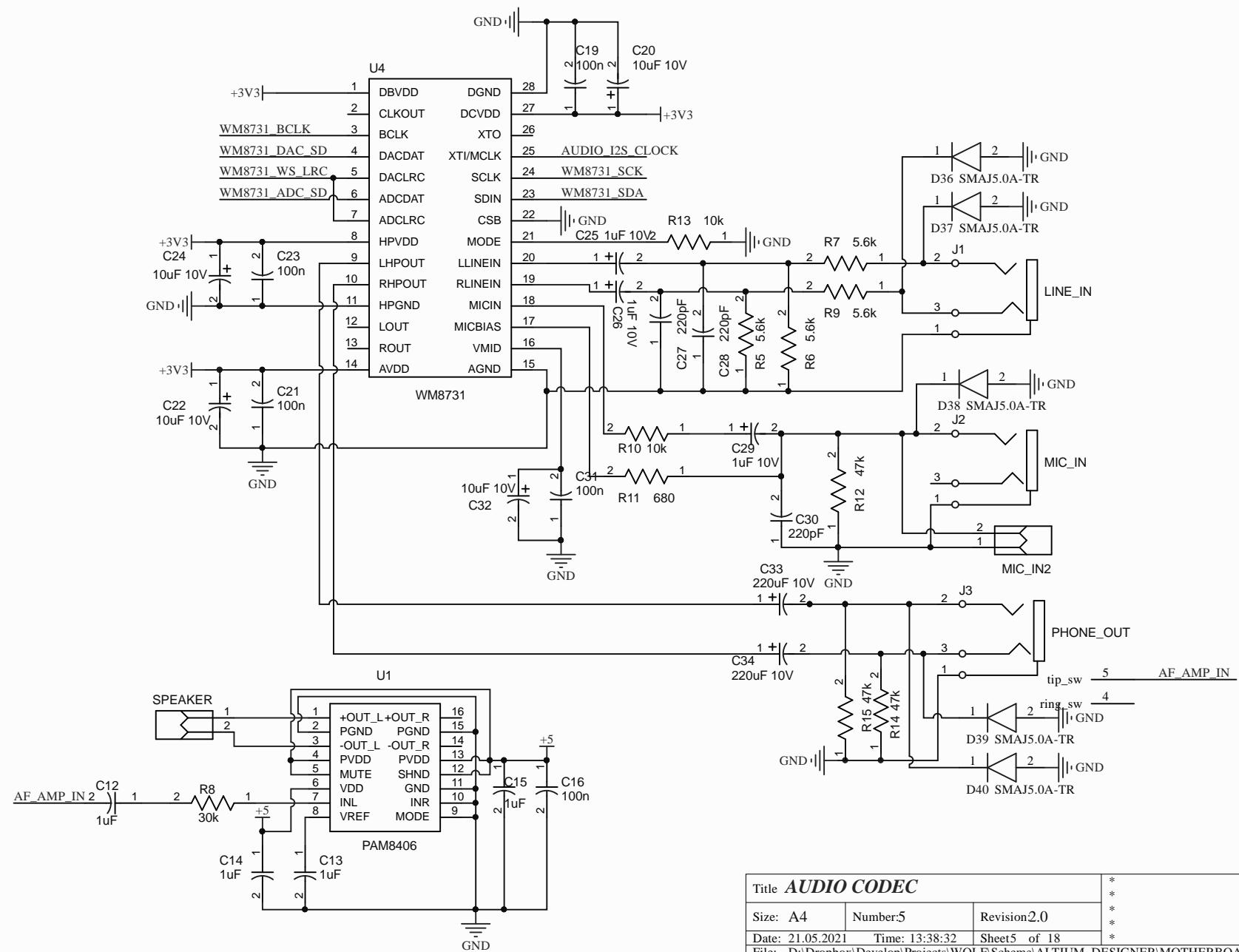
D

A

B

C

D

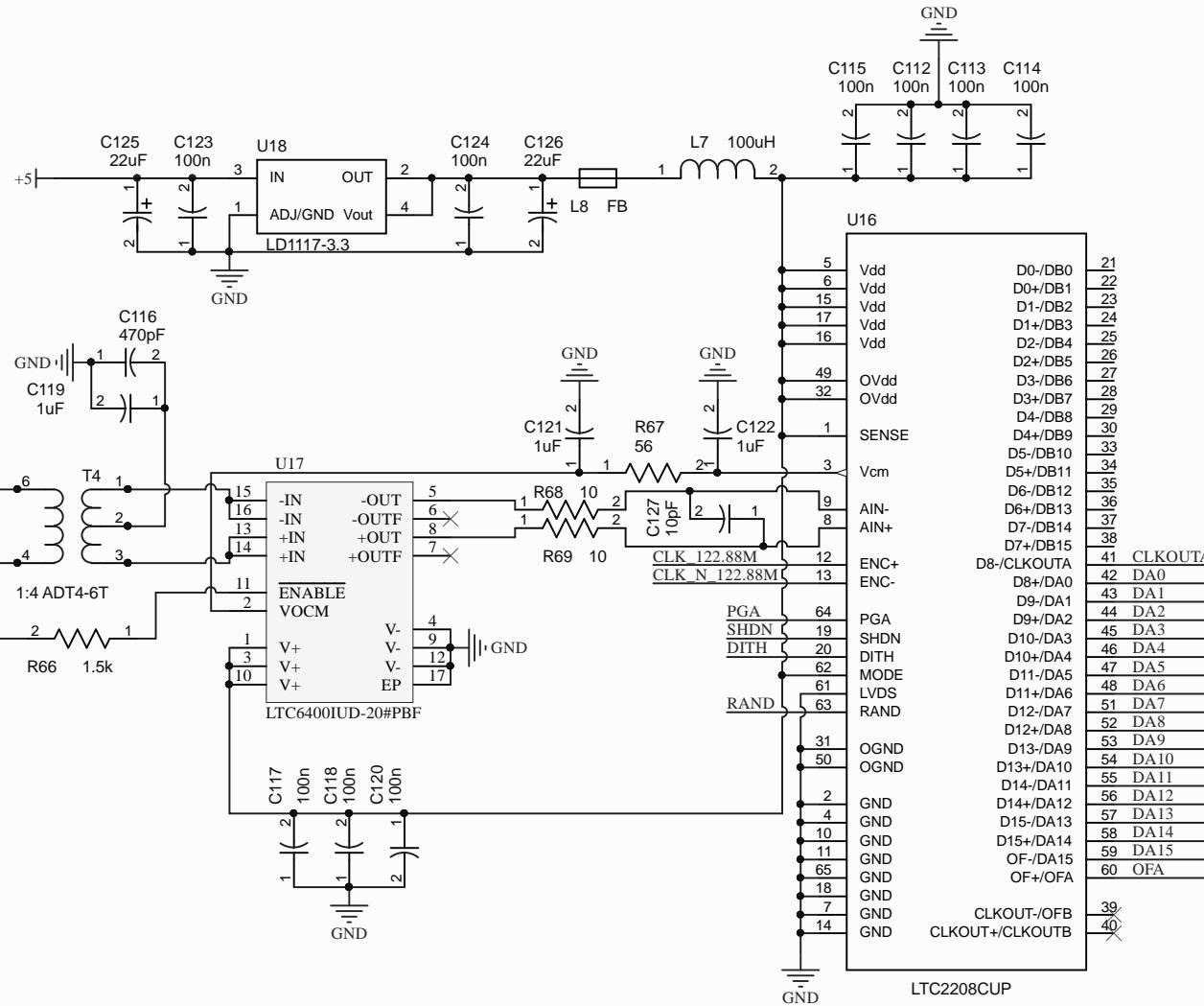
Title **AUDIO CODEC**

Size: A4 Number:5 Revision:2.0

Date: 21.05.2021 Time: 13:38:32 Sheet 5 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\AUDIO_CODEC.SchDoc

Altium

Title **ADC**

Size: A4 Number:3 Revision 2.0

Date: 21.05.2021 Time: 13:38:34 Sheet 3 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\ADC.SchDoc

Altium

