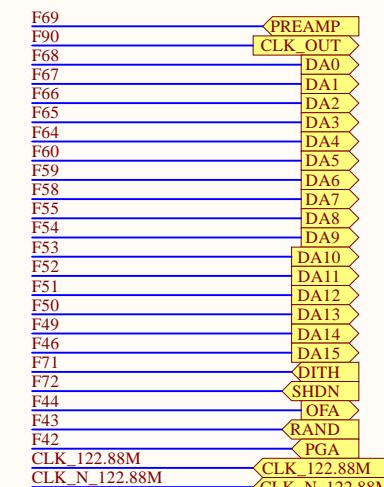
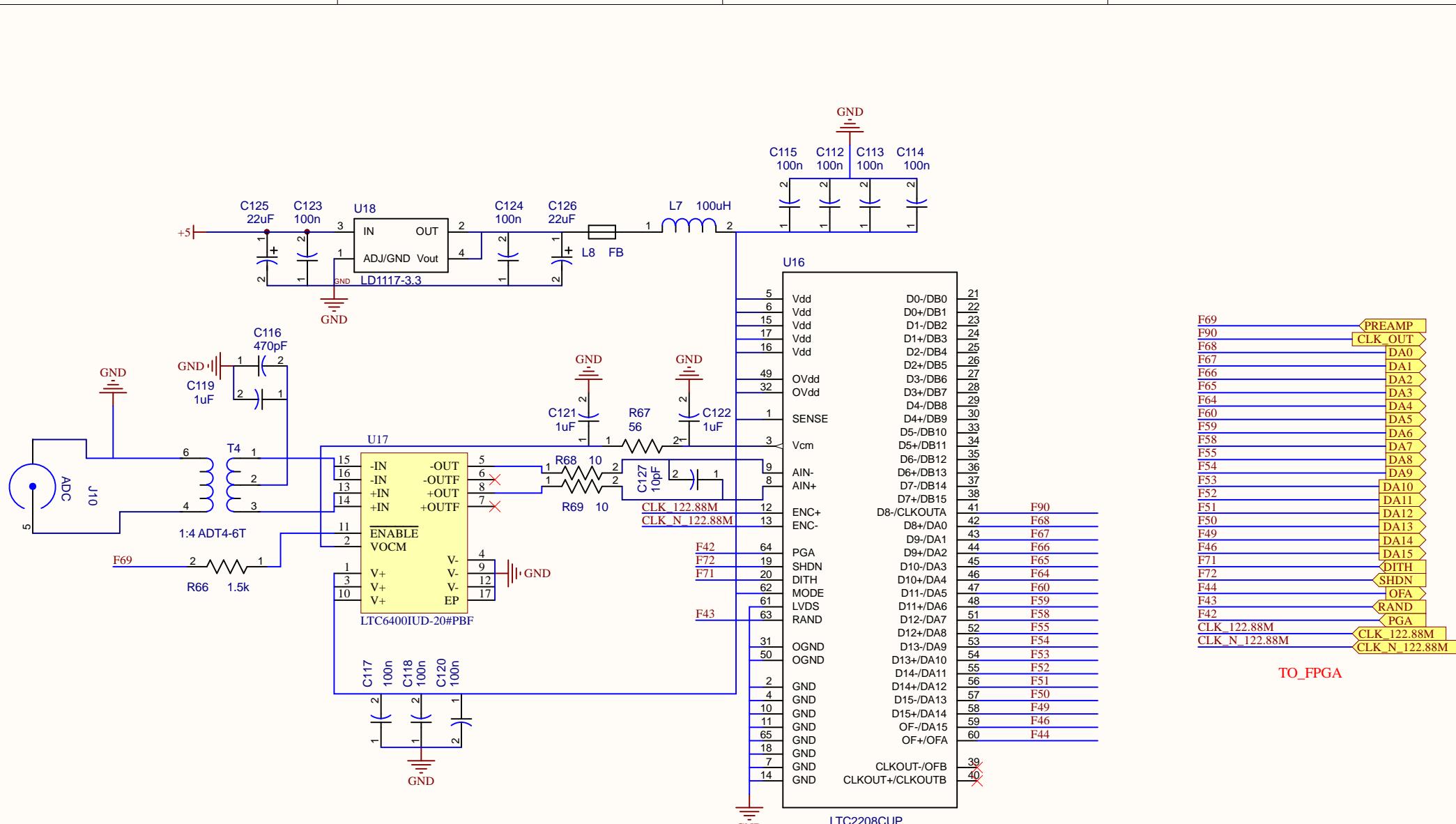
Title **MOTHERBOARD**

Size: A4 Number:2 Revision:1.9

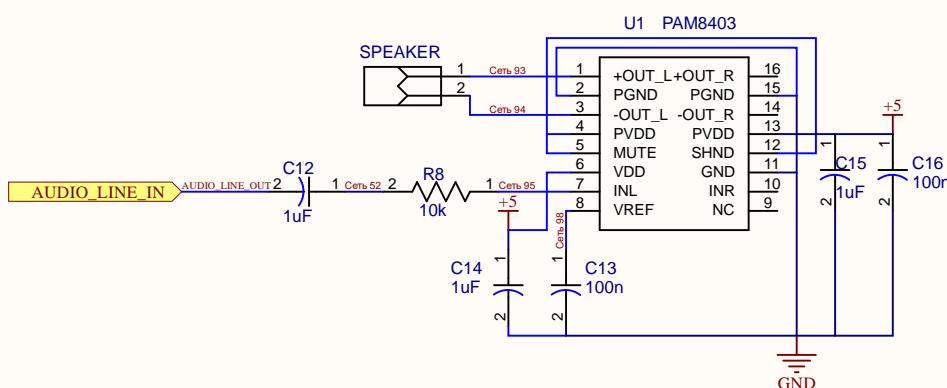
Date: 29.03.2020 Time: 18:23:50 Sheet 2 of 18

File: D:\Dropbox\Develop\Projects\UA3REO\Schema\ALTIUM DESIGNER\MOTHERBOARD\MOTHERBOARD.SCHDOC

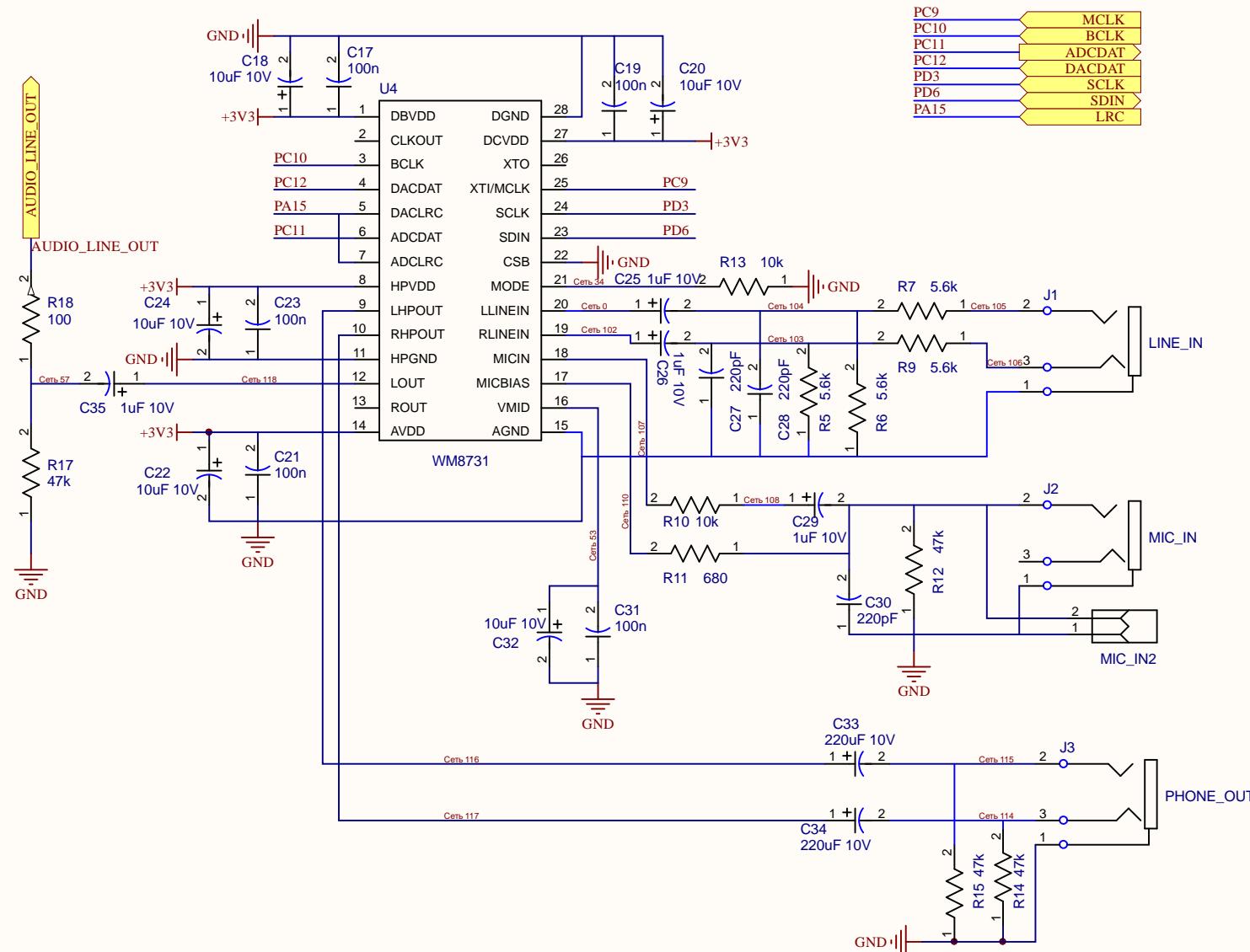
Altium



Title ADC			*
Size: A4	Number: 3	Revision: 1.9	*
Date: 29.03.2020	Time: 18:23:51	Sheet 3 of 18	*
File: D:\Dropbox\Develop\Projects\UA3REO\Scheme\ALTUIM DESIGNER\MOTHERBOARD\ADC.SchDoc			*



Title AF AMP		
Size: A4	Number:4	Revision:1.9
Date: 29.03.2020	Time: 18:23:51	Sheet4 of 18
File: D:\Dropbox\Develop\Projects\UA3REO\Scheme\ALTIUM DESIGNER\MOTHERBOARD\AF AMP.SchDoc		*

Title **AUDIO CODEC**

Size: A4 Number:5 Revision:1.9

Date: 29.03.2020 Time: 18:23:51 Sheet 5 of 18

File: D:\Dropbox\Develop\Projects\UA3REO\Schema\ALTIUM DESIGNER\MOTHERBOARD\AUDIO CODEC.SchDoc

Altium

A

A

B

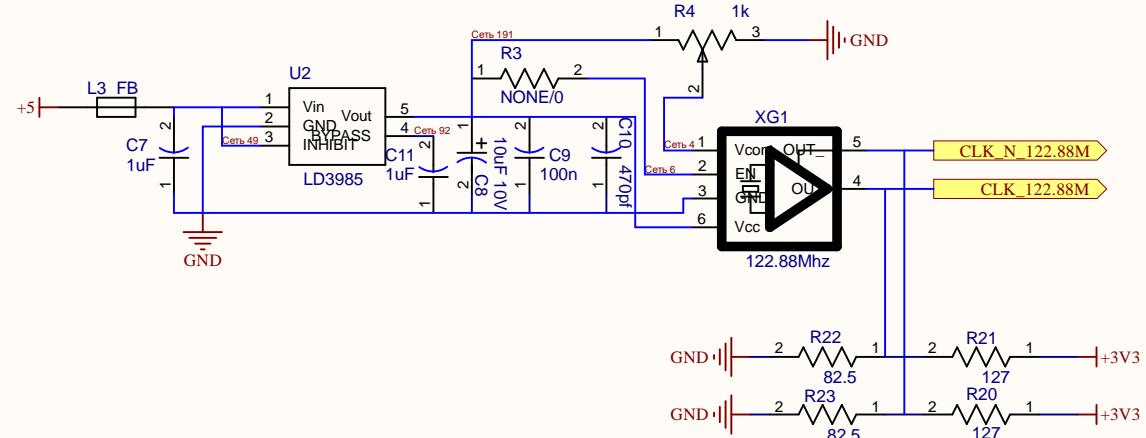
B

C

C

D

D

Title **CLOCK_GENERATOR**

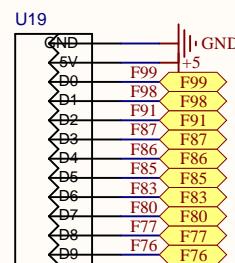
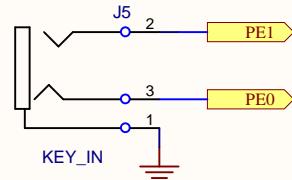
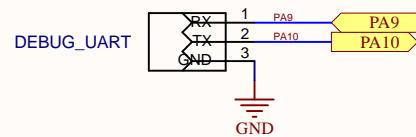
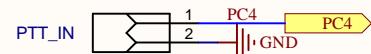
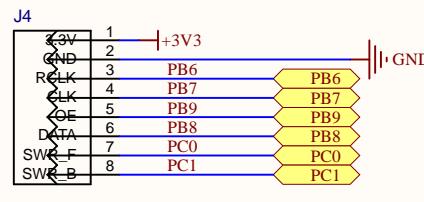
Size: A4 Number:6 Revision:1.9

Date: 29.03.2020 Time: 18:23:51 Sheet6 of 18

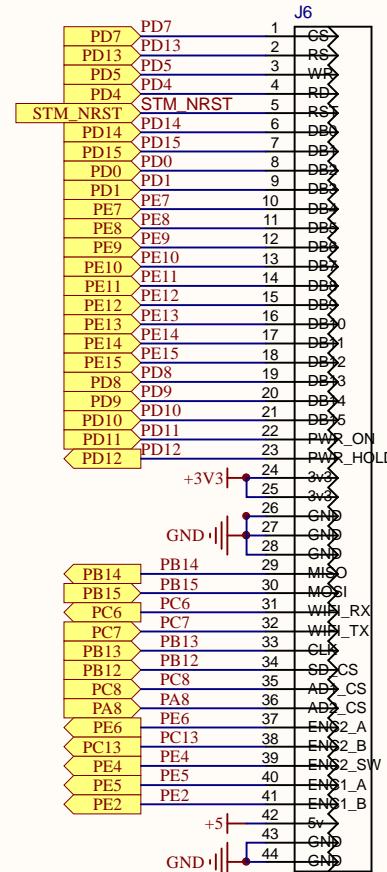
File: D:\Dropbox\Develop\Projects\UA3REO\Scheme\ALTIUM DESIGNER\MOTHERBOARD\CLOCK_GENERATOR.SchDoc

Altium

A



EXT-CONN



FRONT-UNIT

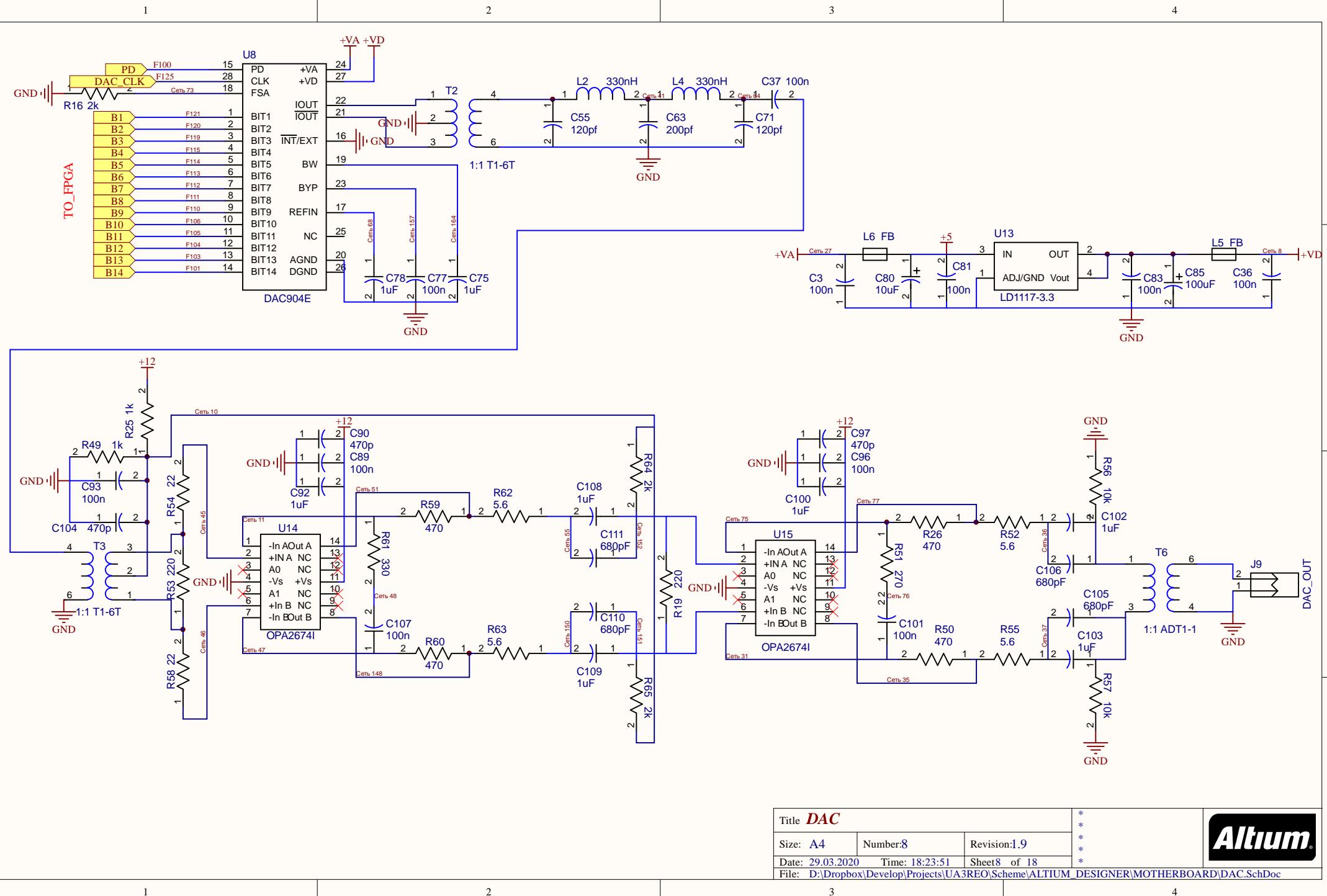
Title **CONNECTIONS**

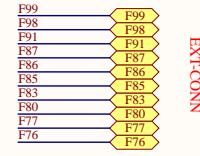
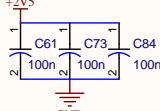
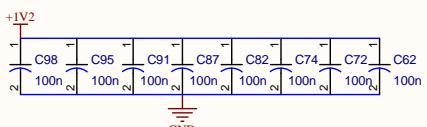
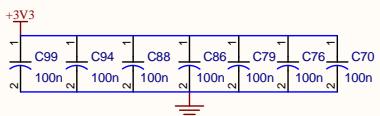
Size: A4 Number: 7 Revision: 1.9

Date: 29.03.2020 Time: 18:23:51 Sheet 7 of 18

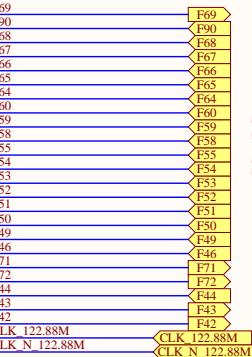
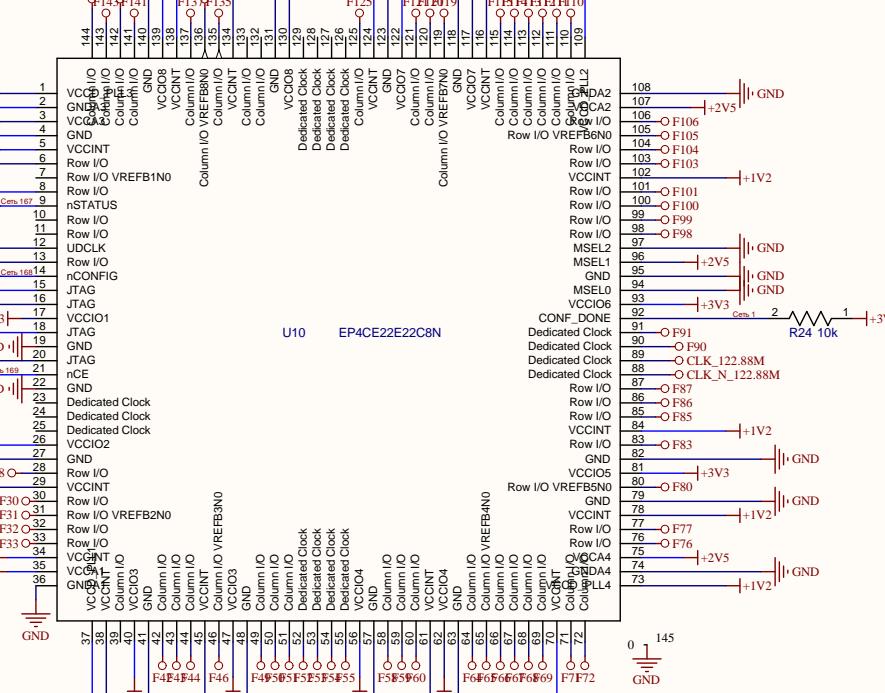
File: D:\Dropbox\Develop\Projects\UA3REO\Schema\ALTUIM DESIGNER\MOTHERBOARD\CONNECTIONS.SchDoc

Altium





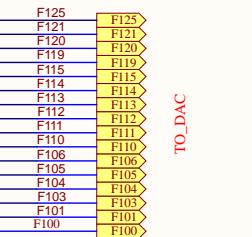
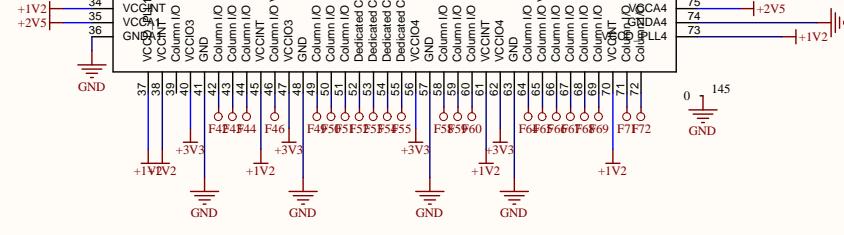
A



B

B

0 1 145
GND

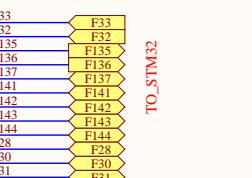


C

C

TO_DAC

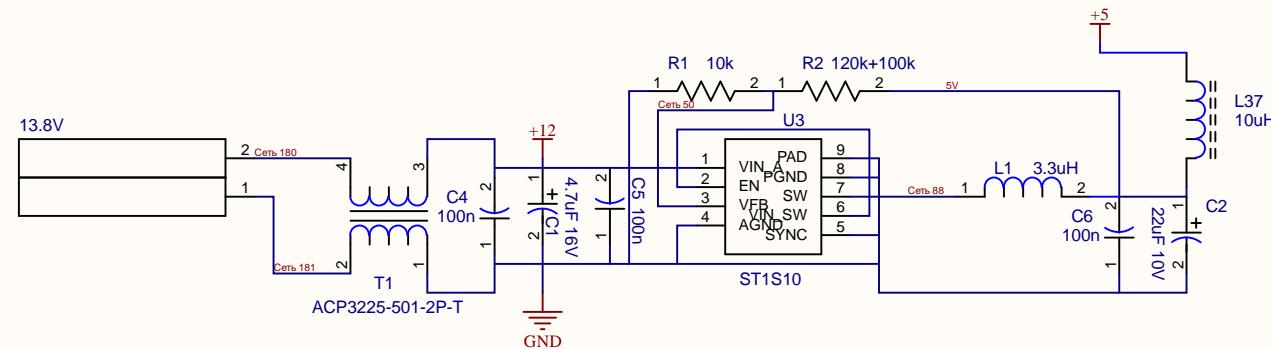
D



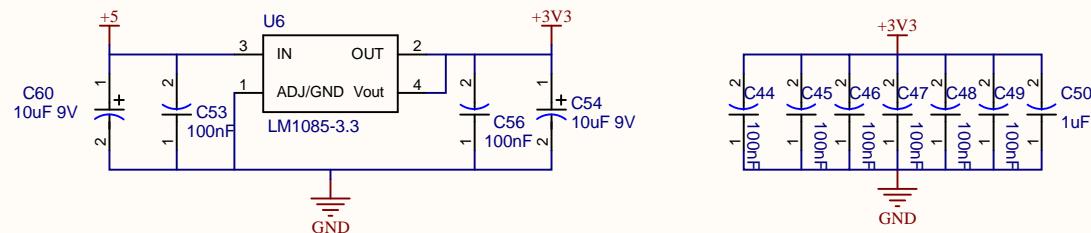
Title FPGA		
Size: A3	Number: 9	Revision: 1.9
Date: 29.03.2020	Time: 18:23:51	Sheet 9 of 18
File: D:\Dropbox\Develop\Projects\U3REO\Scheme\ALTIUM DESIGNER\MOTHERBOARD\FPGA.SchDoc		



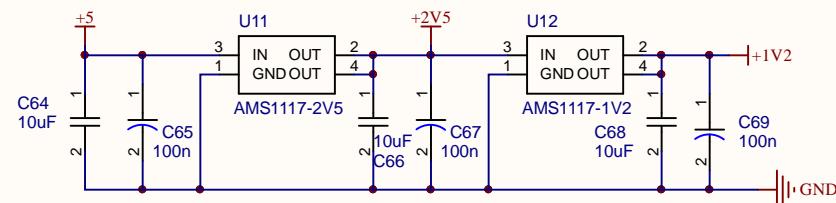
A



B



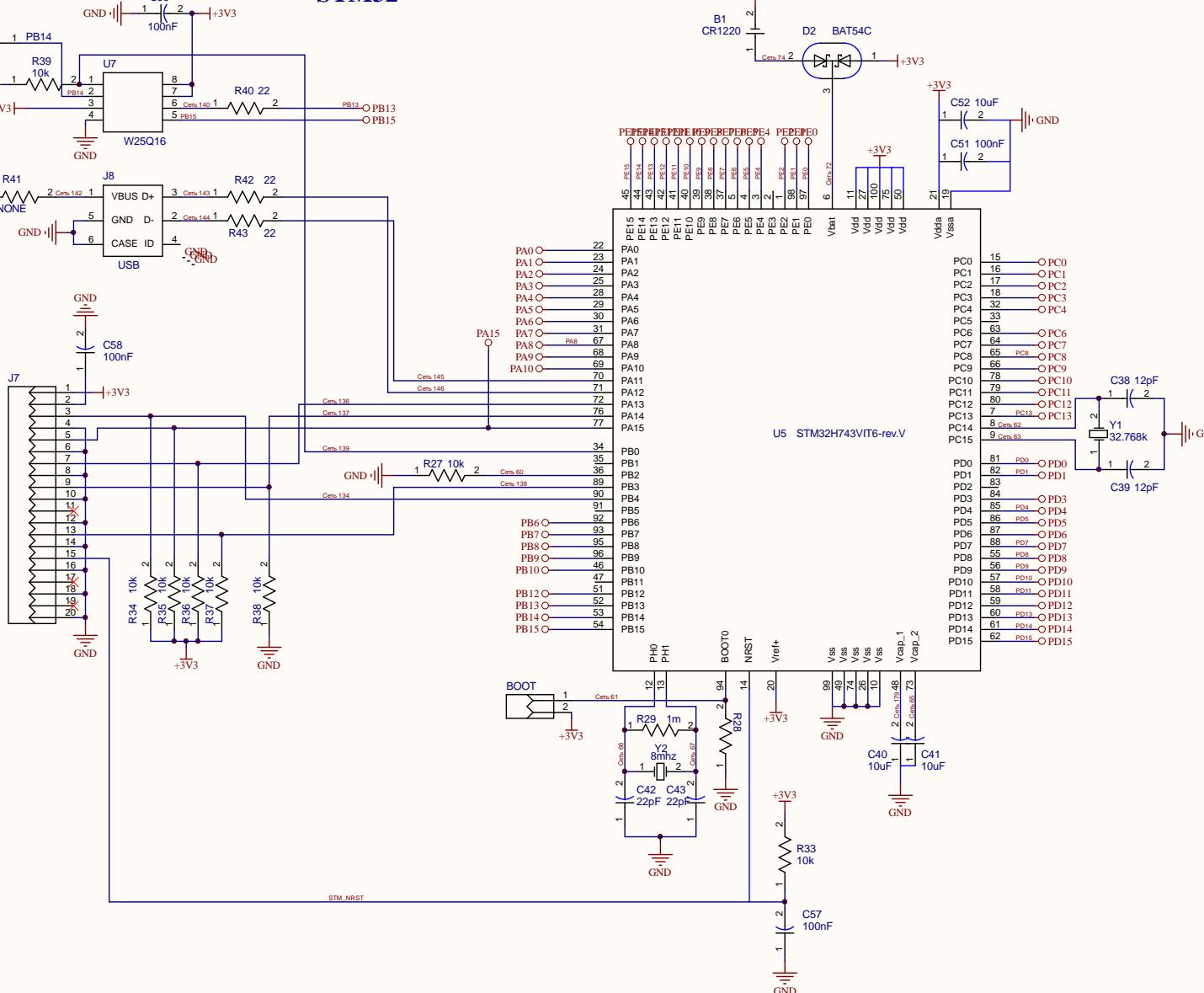
C



D

Title POWER			*
Size: A4	Number:10	Revision:1.9	*
Date: 29.03.2020	Time: 18:23:52	Sheet10 of 18	*
File: D:\Dropbox\Develop\Projects\UA3REO\Scheme\ALTIUM DESIGNER\MOTHERBOARD\POWER.SchDoc			Altium

STM32

Title **STM32**

Size: A3

Number: 11

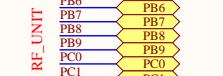
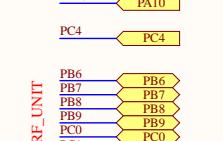
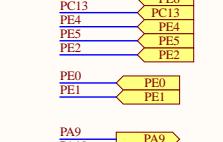
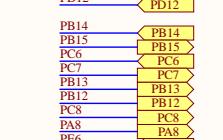
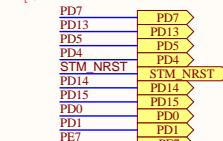
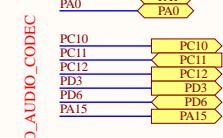
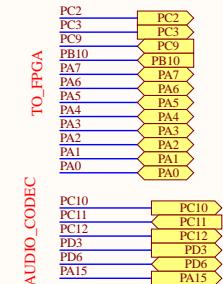
Revision: 1.9

Date: 29.03.2020

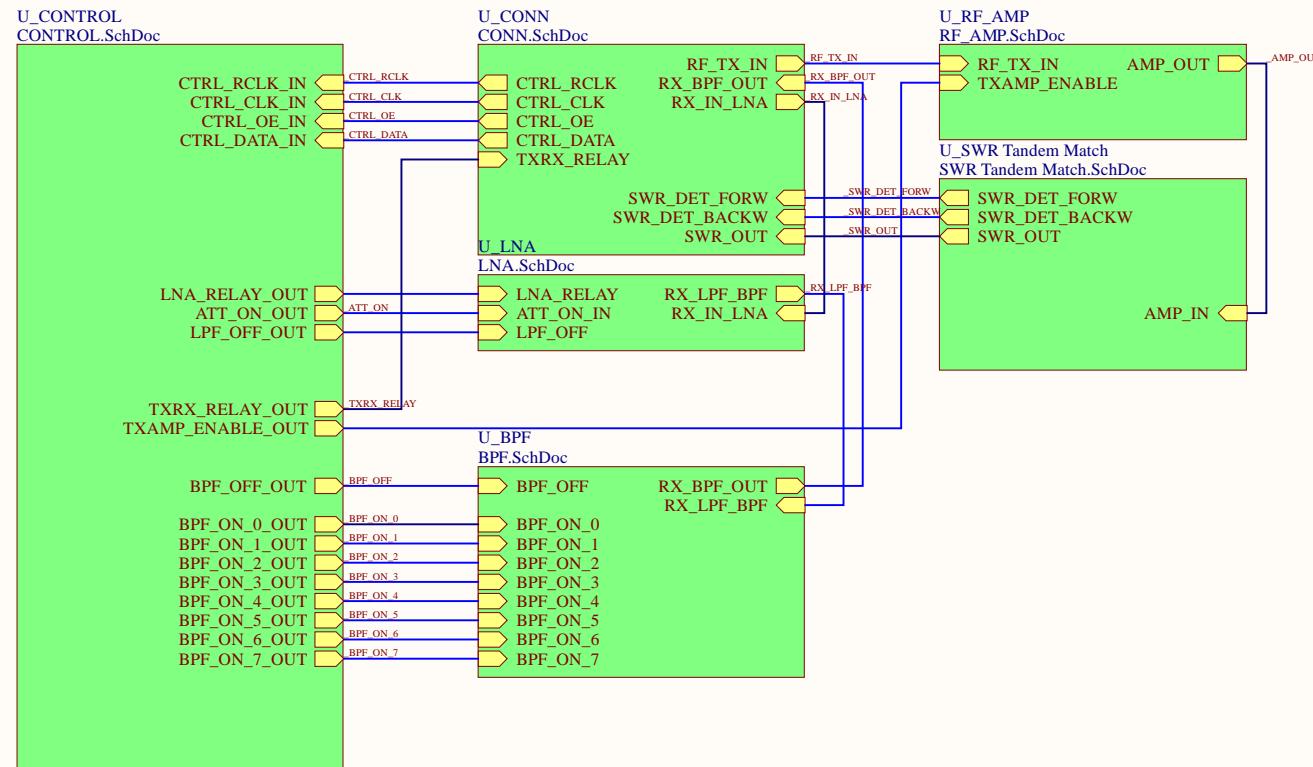
Time: 18:23:52

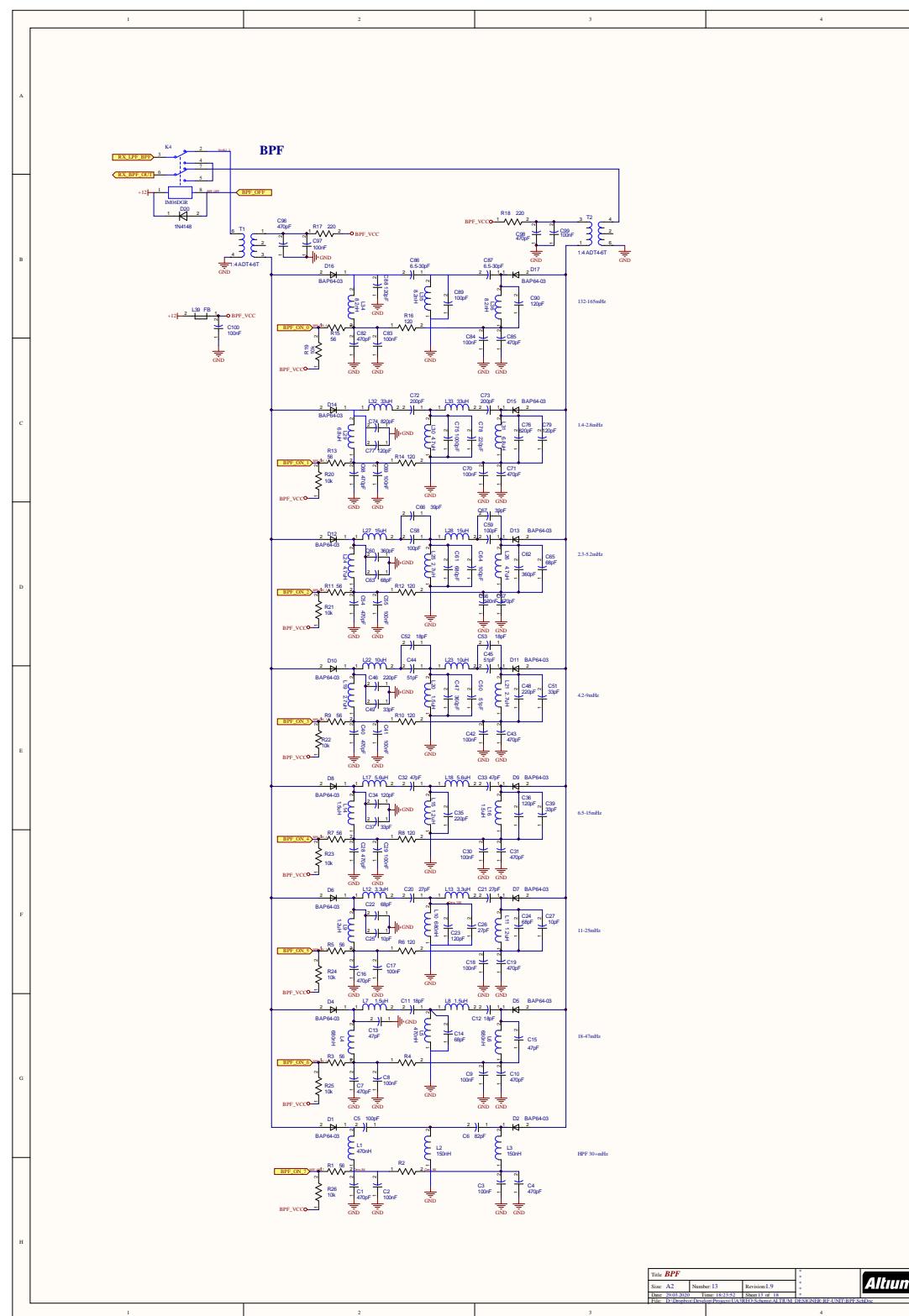
Sheet 11 of 18

File: D:\Dropbox\Develop\Projects\U3R0\Scheme\ALTIUM DESIGNER\MOTHERBOARD\STM32.SchDoc



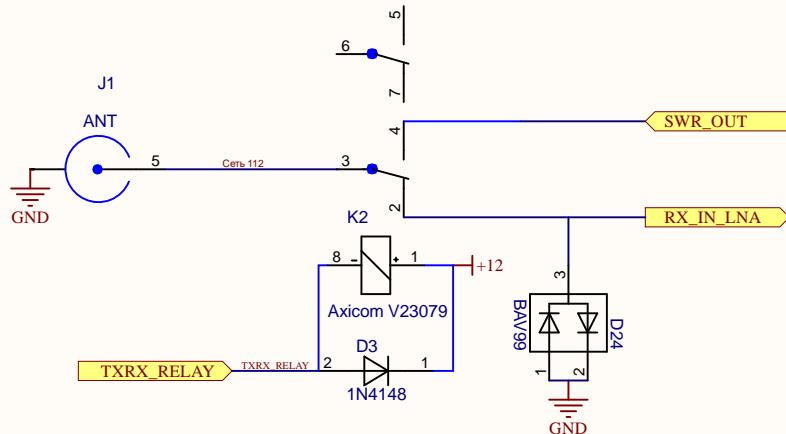
A

Title **RF-UNIT**Size: **A4** Number:**12** Revision:**1.9**Date: **29.03.2020** Time: **18:23:52** Sheet**12** of **18**File: **D:\Dropbox\Develop\Projects\UA3REO\Scheme\ALTIUM DESIGNER\RF-UNIT\RF-UNIT.SCHDOC****Altium**



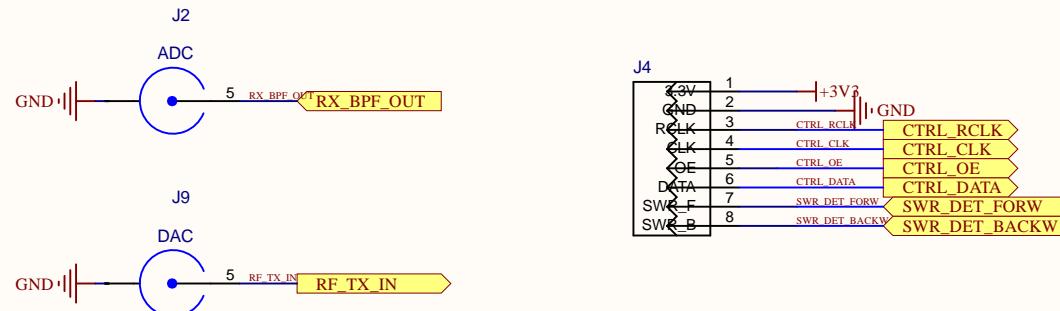
A

TXRX_COMUTATOR



B

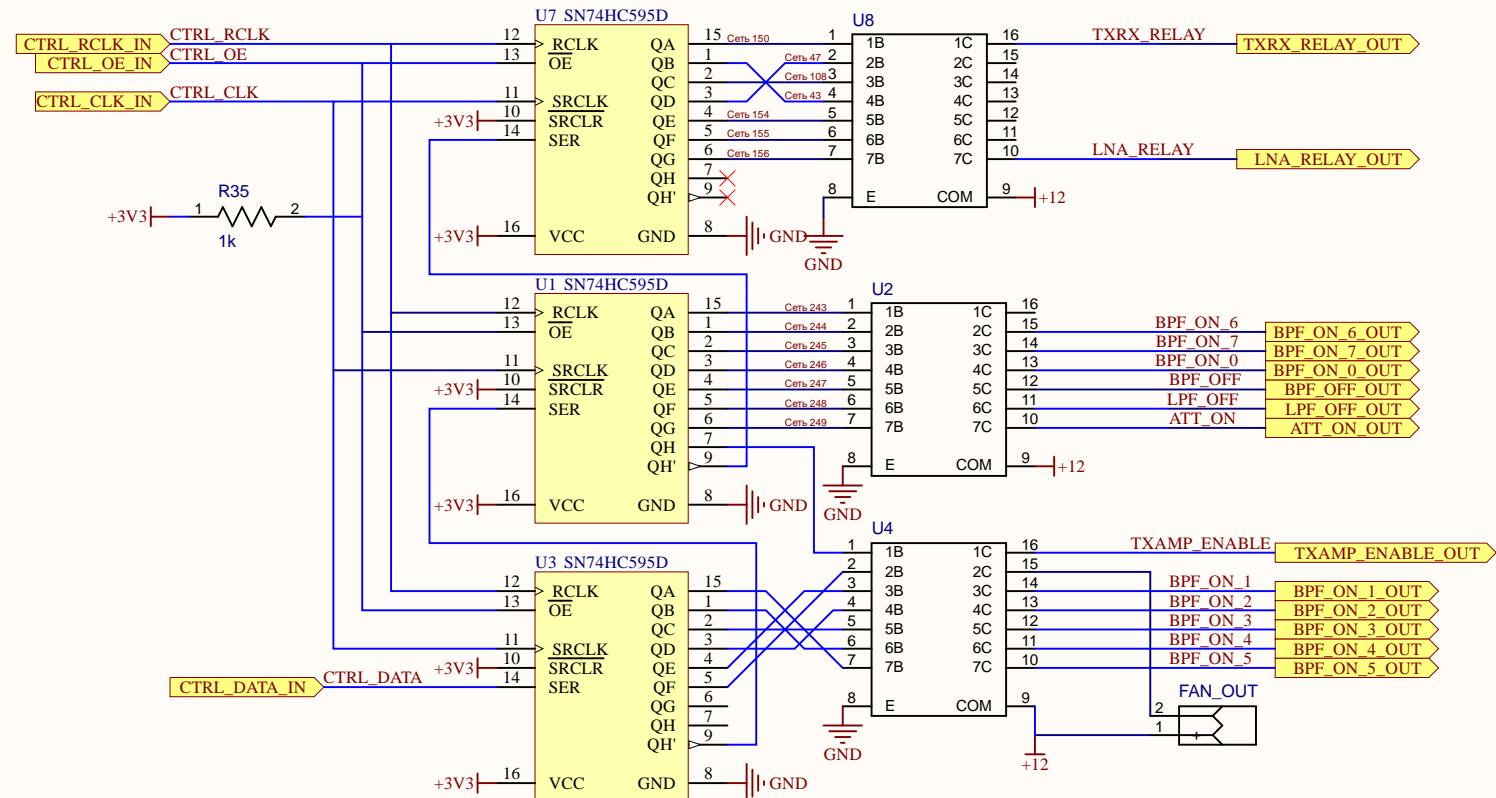
CONN

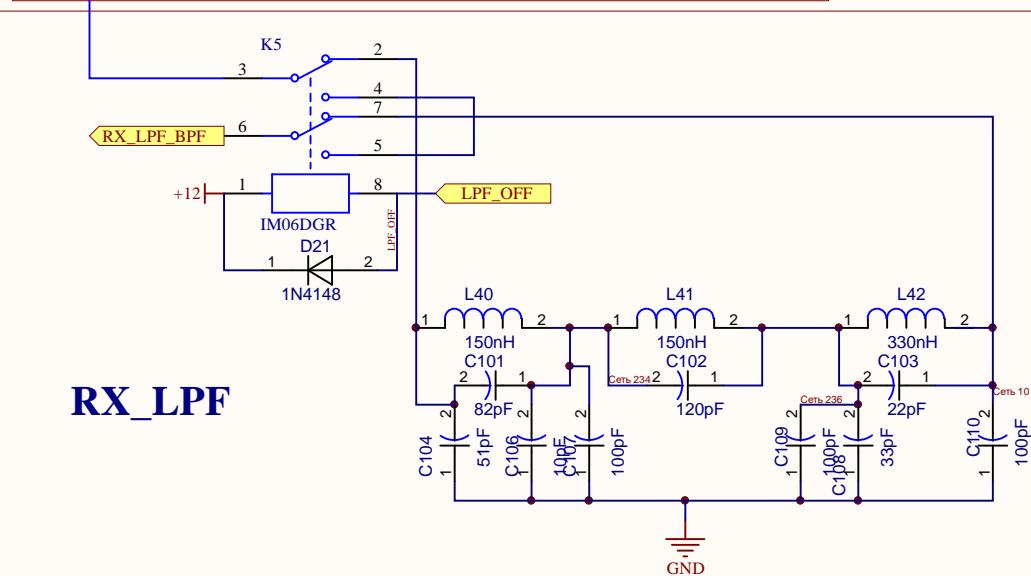
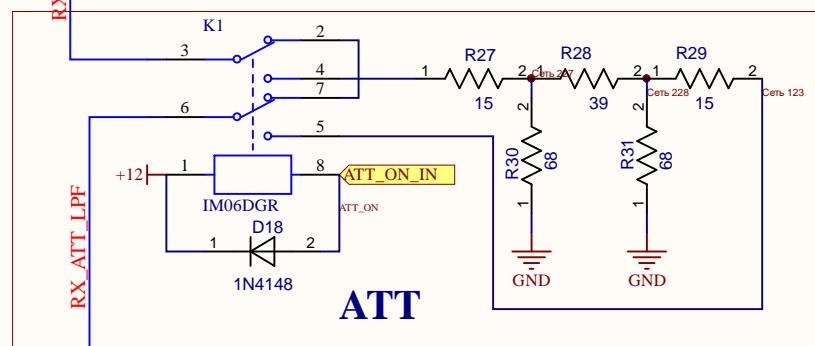
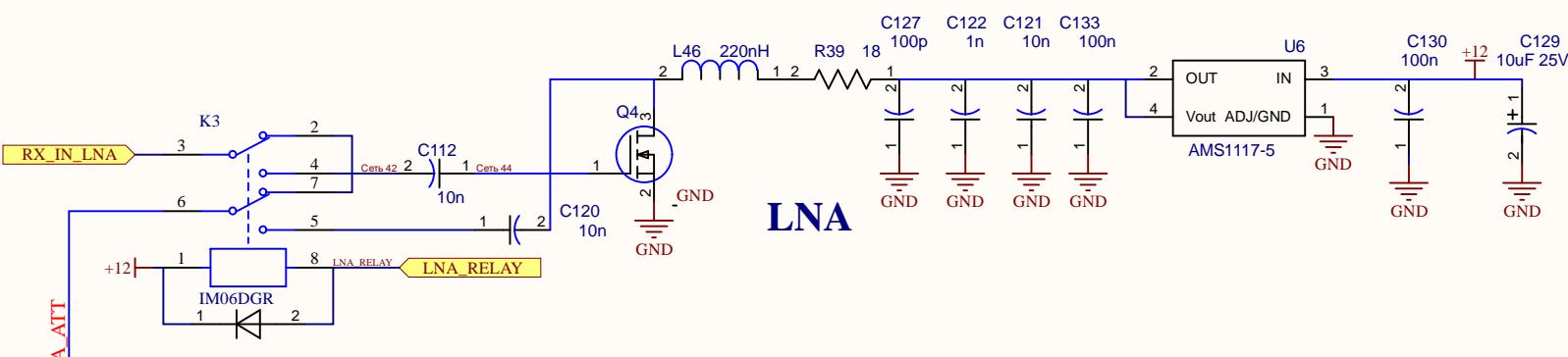


C

D

Title CONN			*
Size: A4	Number:14	Revision:1.9	*
Date: 29.03.2020	Time: 18:23:53	Sheet14 of 18	*
File: D:\Dropbox\Develop\Projects\UA3REO\Scheme\ALTIUM DESIGNER\RF-UNIT\CONN.SchDoc			Altium®

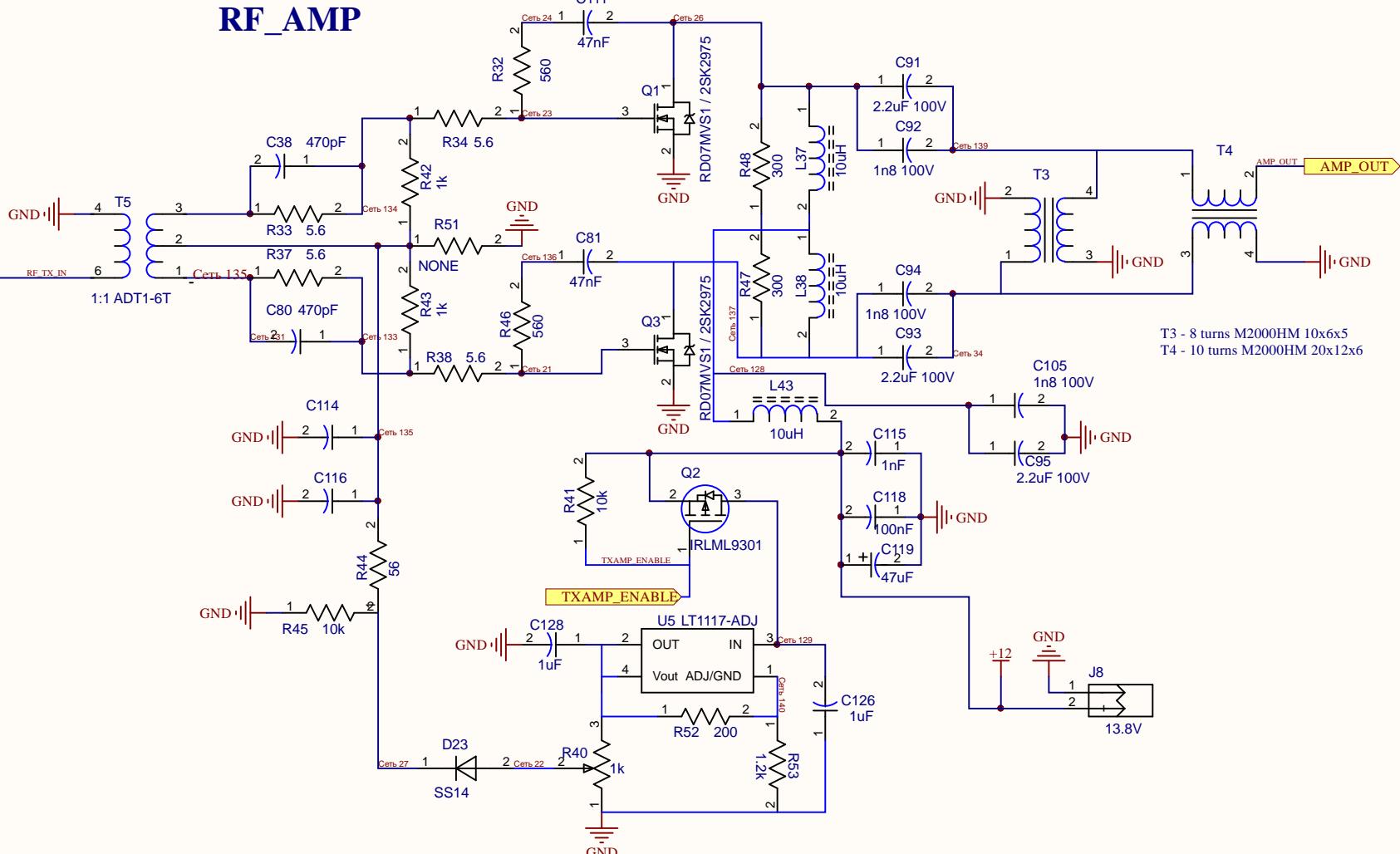
Title **CONTROL**Size: **A4** Number:**15** Revision:**1.9**Date: **29.03.2020** Time: **18:23:53** Sheet**15** of **18**File: **D:\Dropbox\Develop\Projects\UA3REO\Schema\ALTIVUM DESIGNER\RF-UNIT\CONTROL.SchDoc****Altium**



Title LNA			*
Size: A4	Number: 16	Revision: 1.9	*
Date: 29.03.2020	Time: 18:23:53	Sheet 16 of 18	*
File: D:\Dropbox\Develop\Projects\UA3REO\Scheme\ALTIUM DESIGNER\RF-UNIT\LNA.SchDoc			*

Altium

RF_AMP



Title **RF_AMP**

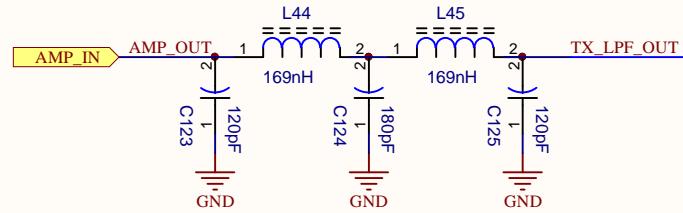
Size: **A4** Number: **17** Revision: **1.9**

Date: **29.03.2020** Time: **18:23:53** Sheet **17** of **18**

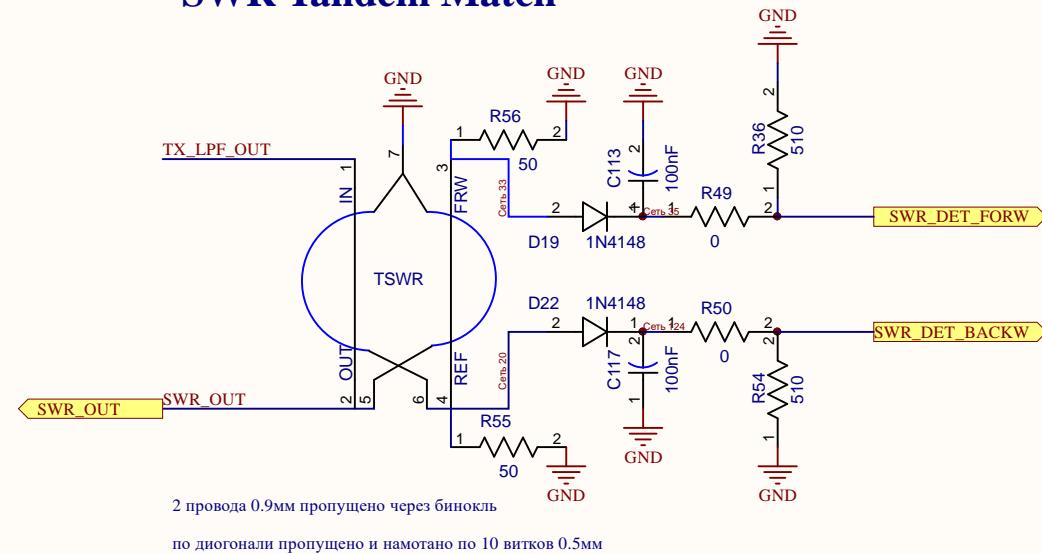
File: **D:\Dropbox\Develop\Projects\UA3REO\Scheme\ALTIUM DESIGNER\RF-UNIT\RF_AMP.SchDoc**



TX_LPF



SWR Tandem Match



Title ***SWR Tandem Match***

Size: A4	Number: 18	Revision: 1.9
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Date: 29.03.2020	Time: 18:23:53	Sheet 18 of 18
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File: D:\Dropbox\Develop\Projects\UA3REO\Scheme\ALTIUM DESIGNER\RF-UNIT\SWR Tandem Match.SchDoc	*
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Altium

Comment	Description	Designator	Footprint	LibRef	Quantity
	JX2	13, 8/	JX2	JX2	1
CR1220	BR1632A/FA	B1	BR1632A/FA	BR1632A/FA	1
	51022-0200	BOOT, MIC_IN2, PT	51022-0200	51022-0200	4
4.7uF 16V	CAPAE_6_x6x_6h54	C1	CAPAE_6_x6x_6h54	CAPAE_6_x6x_6h54	1
22uF 10V	CAPAE_4_3x4_3h54	C2	CAPAE_4_3x4_3h54	CAPAE_4_3x4_3h54	1
100n	CAP_0805	C3, C4, C5, C6, C9	CAP_0805	CAP_0805	44
1uF	CAP_0805	C7, C11, C12, C14	CAP_0805	CAP_0805	9
10uF 10V	CAPAE_4_3x4_3h54	C8, C18, C20, C22	CAPAE_4_3x4_3h54	CAPAE_4_3x4_3h54	6
470p	CAP_0805	C10	CAP_0805	CAP_0805	1
1uF 10V	CAPAE_4_3x4_3h54	C25, C26, C29, C35	CAPAE_4_3x4_3h54	CAPAE_4_3x4_3h54	4
220uF	CAP_0805	C27, C28, C30	CAP_0805	CAP_0805	3
220uF 10V	CAPAE_4_3x4_3h54	C33, C34	CAPAE_4_3x4_3h54	CAPAE_4_3x4_3h54	2
12pF	CAP_0805	C38, C39	CAP_0805	CAP_0805	2
10F	CAP_0805	C40, C41, C50, C52	CAP_0805	CAP_0805	4
22pF	CAP_0805	C42, C43	CAP_0805	CAP_0805	2
100pF	CAP_0805	C44, C45, C46, C47	CAP_0805	CAP_0805	12
10uF 9V	CAPAE_4_3x4_3h54	C54, C60	CAPAE_4_3x4_3h54	CAPAE_4_3x4_3h54	2
120pF	CAP_0805	C55, C71	CAP_0805	CAP_0805	2
200pF	CAP_0805	C63	CAP_0805	CAP_0805	1
10uF	C0805	C64, C66, C68	C0805	C0805	3
10uF	EEEF_C_sizeB	C80	EEEF_C_sizeB	EEEF_C_sizeB	1
100uF	EEEF_C_sizeB	C85	EEEF_C_sizeB	EEEF_C_sizeB	1
47p	CAP_0805	C90, C97, C101	CAP_0805	CAP_0805	3
1uF	CAP_1206	C102, C103, C108	CAP_1206	CAP_1206	4
680pF	CAP_1206	C105, C106, C110	CAP_1206	CAP_1206	4
CAP_0805	CAP_0805	C112, C113, C114	CAP_0805	CAP_0805	13
22uF		C125	TC3528-1411	EEEF_C_sizeB	1
EEEF_C_sizeB	EEEF_C_sizeB	C126	TC3528-1411	EEEF_C_sizeB	1
BAT54C	BAT54C	D2	BAT54C	BAT54C	1
	51022-0300	DEBUG_UART	51022-0300	51022-0300	1
LINE_IN	SJ1-3523N	J1	SJ1-3523N	SJ1-3523N	1
MIC_IN	SJ1-3523N	J2	SJ1-3523N	SJ1-3523N	1
PHONE_OUT	SJ1-3523N	J3	SJ1-3523N	SJ1-3523N	1
90131-0124	90131-0124	J4	90131-0124	90131-0124	1
KEY_IN	SJ1-3523N	J5	SJ1-3523N	SJ1-3523N	1
FRONT-UNIT	90131-0142	J6	90131-0142	90131-0142	1
JTAG STM32	90131-0130	J7	90131-0130	90131-0130	1
USB	2-17340353	J8	2-17340353	2-17340353	1
DAC_OUT	6444562	J9	6444562	6444562	1
ADC	31-5431	J10	31-5431	31-5431	1
	IDC10	JTAG_FPGA	IDC10	IDC10	1
3.3uH	IND_1812	L1	IND_1812	IND_1812	1
330nH	IND_0805	L2, L4	IND_0805	IND_0805	2
FB	Ferrite bead on cond	L3, L5, L6, L8	IND_FERRITE_BEAD	IND_FERRITE_BEAD	4
IND_1206	IND_1206	L7	14-1210	IND_1206	1
10uH	Inductor	L7	IND_FERRITE_CORE	IND_FERRITE_CORE	1
10k	RES_0805	R1, R8, R10, R13, R2	RES_0805	RES_0805	21
120k+100k	RES_0805	R2	RES_0805	RES_0805	1
0/NONE	By used crystal	R3	RES_0805	RES_0805	1
1k	EVM1DSX30B	R4	EVM1DSX30B	EVM1DSX30B	1
5.6k	RES_0805	R5, R6, R7, R9	RES_0805	RES_0805	4
680	RES_0805	R11	RES_0805	RES_0805	1
47k	RES_0805	R12, R14, R15, R17	RES_0805	RES_0805	4
2k	RES_0805	R16, R64, R65	RES_0805	RES_0805	3
100	RES_0805	R18	RES_0805	RES_0805	1
220	RES_0805	R19, R53	RES_0805	RES_0805	2
127	RES_0805	R20, R21	RES_0805	RES_0805	2
82.5	RES_0805	R22, R23	RES_0805	RES_0805	2
18	RES_0805	R25, R49	RES_0805	RES_0805	2
470	RES_0805	R26, R50, R59, R60	RES_0805	RES_0805	4
1m	RES_0805	R29	RES_0805	RES_0805	1
22	RES_0805	R40, R42, R43, R54	RES_0805	RES_0805	5
NONE	RES_0805	R41	RES_0805	RES_0805	1
330	RES_0805	R51, R61	RES_0805	RES_0805	2
5.6	RES_1206	R52, R55, R62, R63	RES_1206	RES_1206	4
RES_0805	RES_0805	R66, R67	RES_0805	RES_0805	2
RES_0603	RES_0603	R68, R69	RES_0603	RES_0603	2
ACP3225-501-2RT	Transformer	T1	TRANSF_ICORE	TRANSF_ICORE	1
1:1 T1-6T	Transformer	T2, T3	TRANSF_ACORE2	TRANSF_ACORE2	2
1:4 ADT4-6T	Transformer	T4	TRANSF_ACORE2	TRANSF_ACORE2	1
1:1 ADT1-1	Transformer	T6	TRANSF_ACORE	TRANSF_ACORE	1
PAM8403	PAM8403	U1	PAM8403	PAM8403	1
LD3985	LD3985	U2	LD3985	LD3985	1
ST1S10	ST1S10	U3	ST1S10	ST1S10	1
WM8731	WM8731	U4	WM8731	WM8731	1
STM32HT743V1T6-rev	STM32HT743V1T6	U5	STM32HT743V1T6	STM32HT743V1T6	1
LM1085-3.3	LM1085-3.3	U6	LM1085-3.3	LM1085-3.3	1
W25016	W25016	U7	W25016	W25016	1
DAC904E	DAC904E	U8	DAC904E	DAC904E	1
25P80	25P80	U9	25P80	25P80	1
EP4CE22E22C8N	EP4CE22E22C8N	U10	EP4CE22E22C8N	EP4CE22E22C8N	1
2V5	AMS1110	U11	AMS1110	AMS1110	1
1V2	AMS1110	U12	AMS1110	AMS1110	1
LD1117-3.3	LT1117CST-3.3	U13, U18	LT1117CST-3.3	LT1117CST-3.3	2
OPA2674	OPA2674	U14, U15	OPA2674	OPA2674	2
LTC2208CUP	LTC2208CUP	U16	LTC2208CUP	LTC2208CUP	1
LTC6400IUD-20#PB#	Single Channel, low	U17	LT-UD-16_N	CMP-0359-038562	1
EXT-CONN	90131-0130	U19	EXT-CONN	EXT-CONN	1
12.88Mhz	NV7050SA	XG1	NV7050SA	NV7050SA	1
32.768k	R26-32_768	Y1	R26-32_768	R26-32_768	1
8mhz	ECS-200-201	Y2	ECS-200-201	ECS-200-201	1