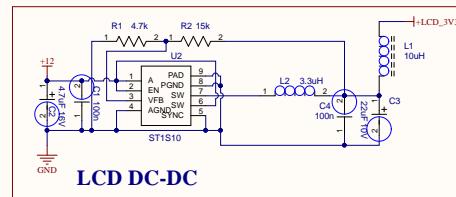
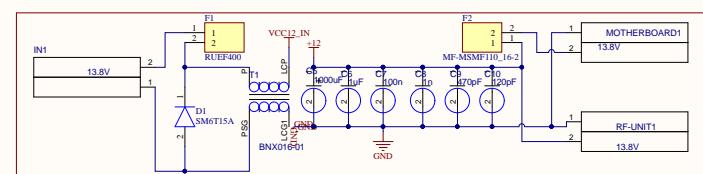
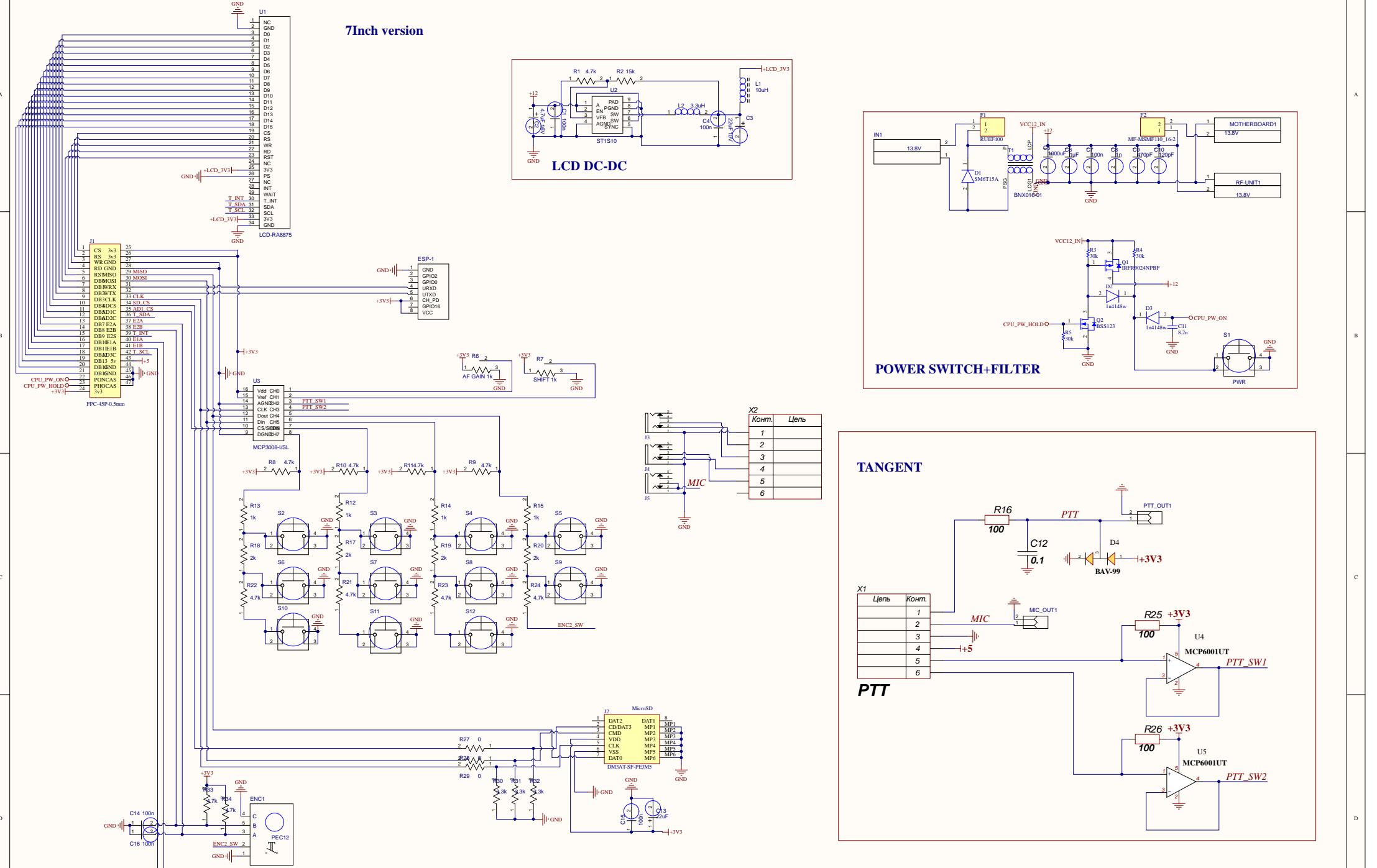


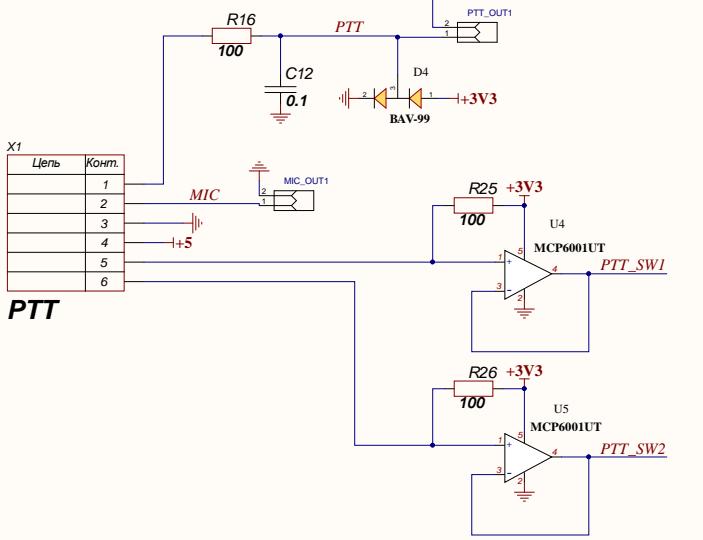
7Inch version



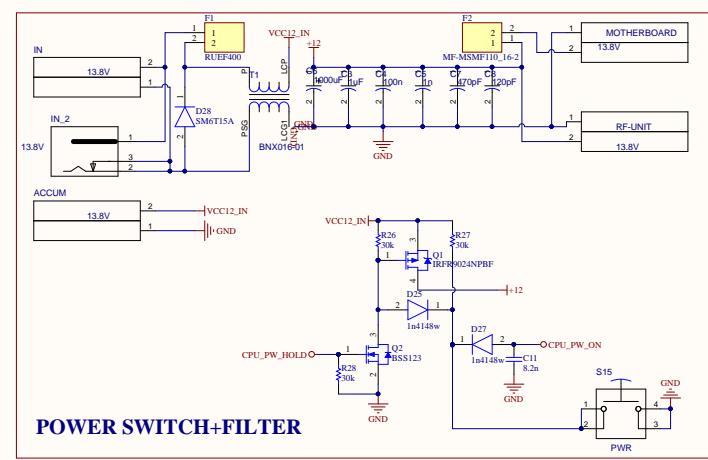
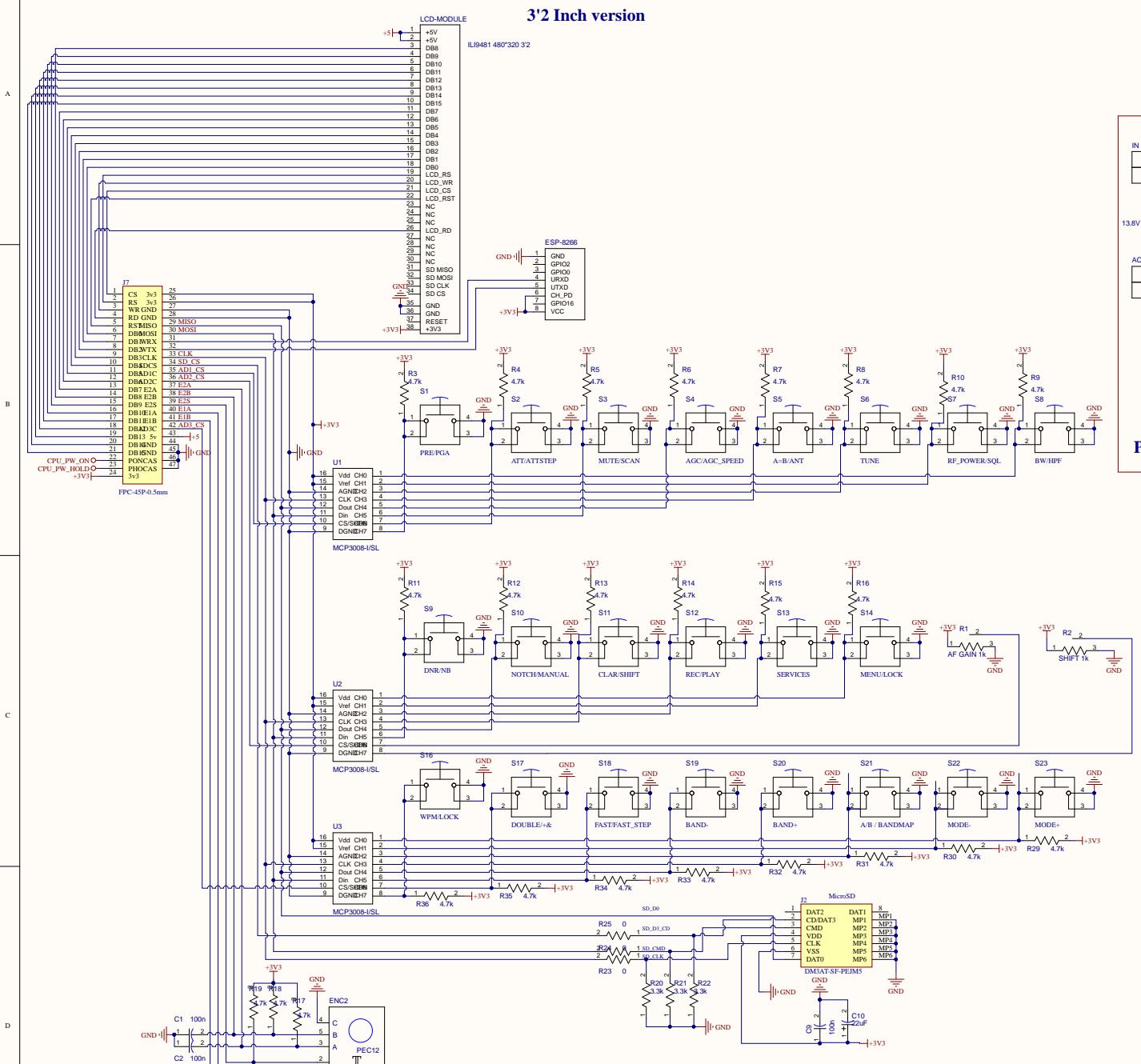
A



TANGENT



3'2 Inch version



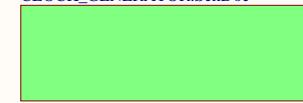
POWER SWITCH+FILTER

A

U_POWER
POWER.SchDoc

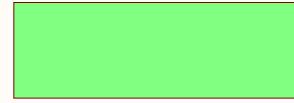


U_CLOCK_GENERATOR
CLOCK_GENERATOR.SchDoc

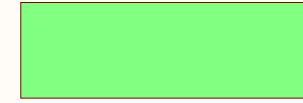


B

U_CONNECTIONS
CONNECTIONS.SchDoc



U_FPGA
FPGA.SchDoc



U_STM32
STM32.SchDoc



U_AUDIO_CODEC
AUDIO_CODEC.SchDoc



A

B

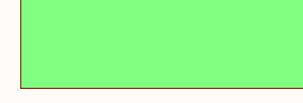
C

D

U_DAC
DAC.SchDoc



U_ADC
ADC.SchDoc



Title **MOTHERBOARD**

Size: A4	Number:2	Revision:2.0
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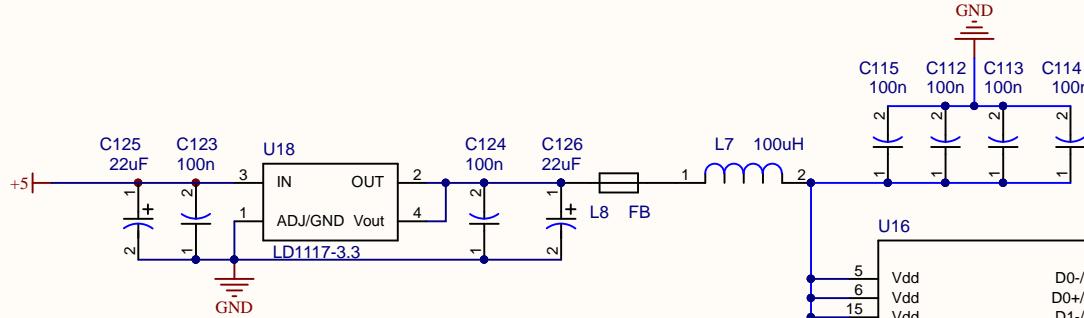
Date: 07.11.2020	Time: 22:59:19	Sheet 2 of 18
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File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\MOTHERBOARD.SCHDOC	*	*	*	*
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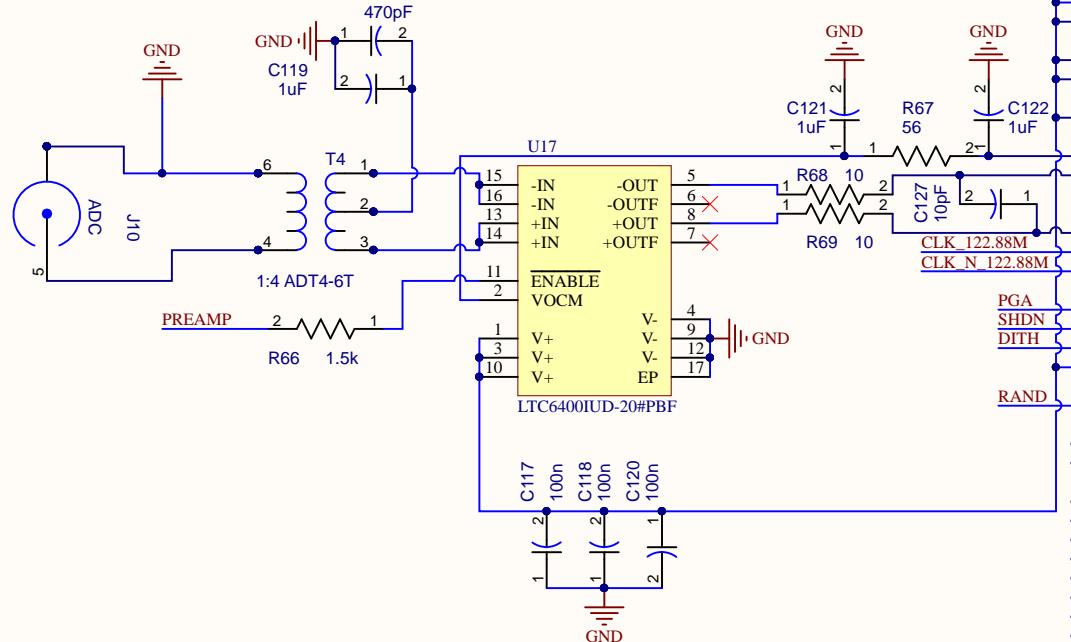
A

A



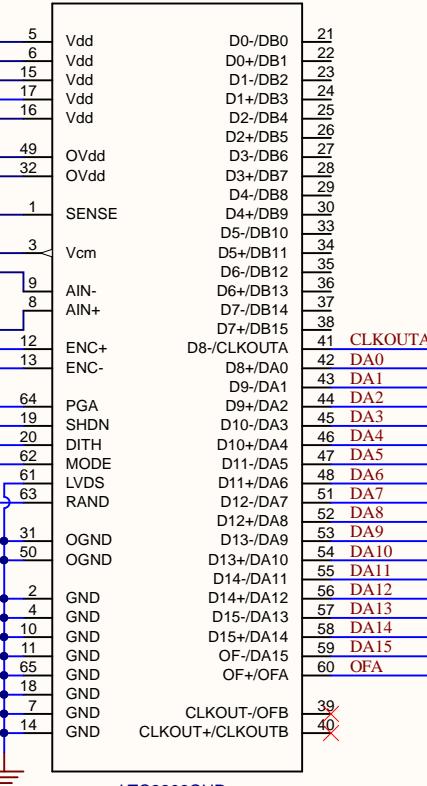
B

B



C

C

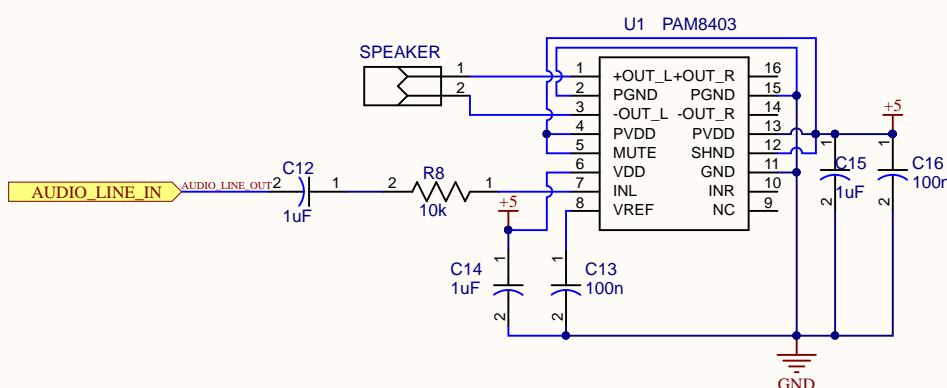
Title **ADC**

Size: A4 Number:3 Revision:2.0

Date: 07.11.2020 Time: 22:59:19 Sheet 3 of 18

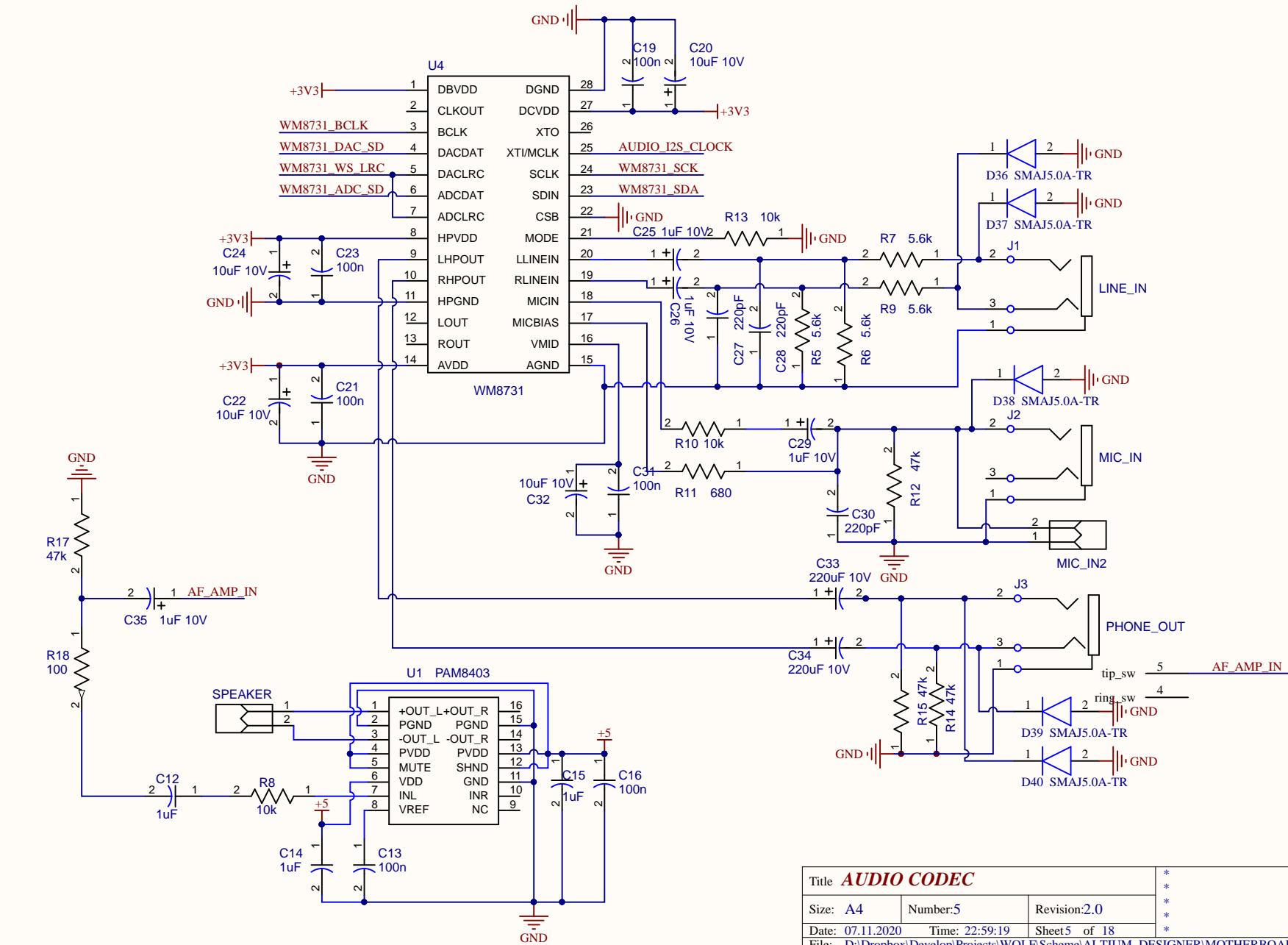
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\ADC.SchDoc

Altium



Title AF AMP			* UA3REO BH8CGJ *
Size: A4	Number:4	Revision:2.0	
Date: 07.11.2020	Time: 22:59:19	Sheet 4 of 18	
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\AF AMP.SchDoc			Altium

A

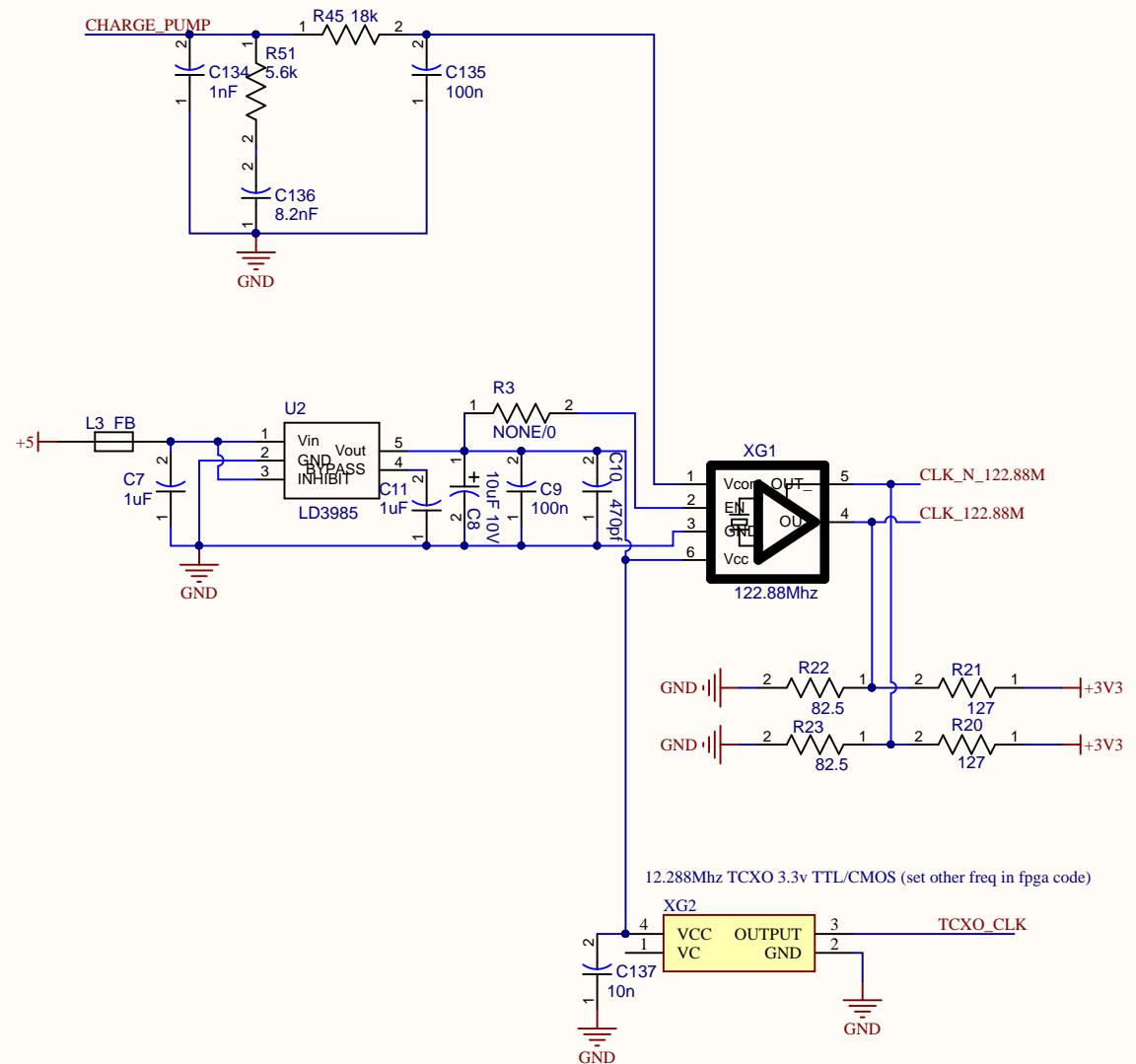
Title **AUDIO CODEC**

Size: A4 Number:5 Revision:2.0

Date: 07.11.2020 Time: 22:59:19 Sheet 5 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\AUDIO_CODEC.SchDoc

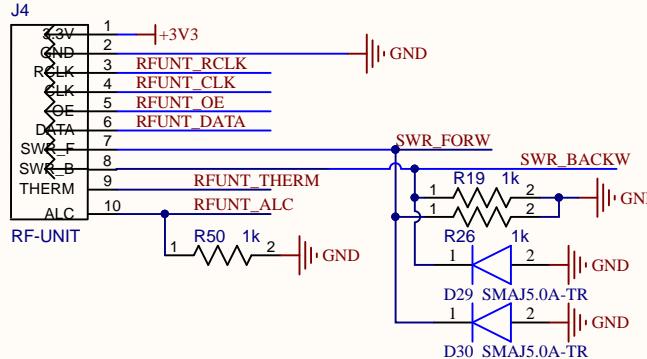
Altium



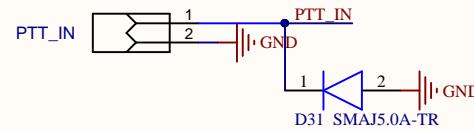
Title	CLOCK GENERATOR	
Size:	A4	Number:6
Date:	07.11.2020	Time: 22:59:20
File:	D:\Dropbox\Devolon\Projects\WQF\	

Altium

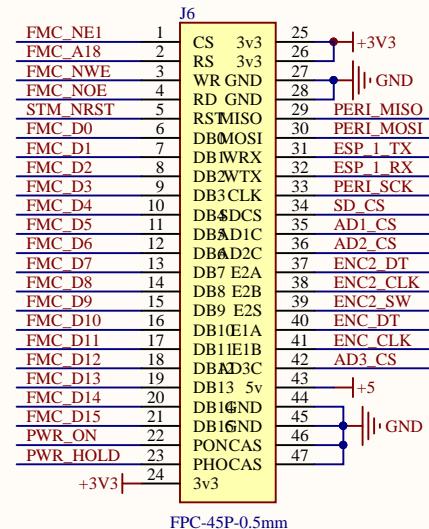
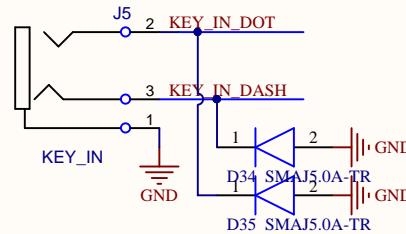
A

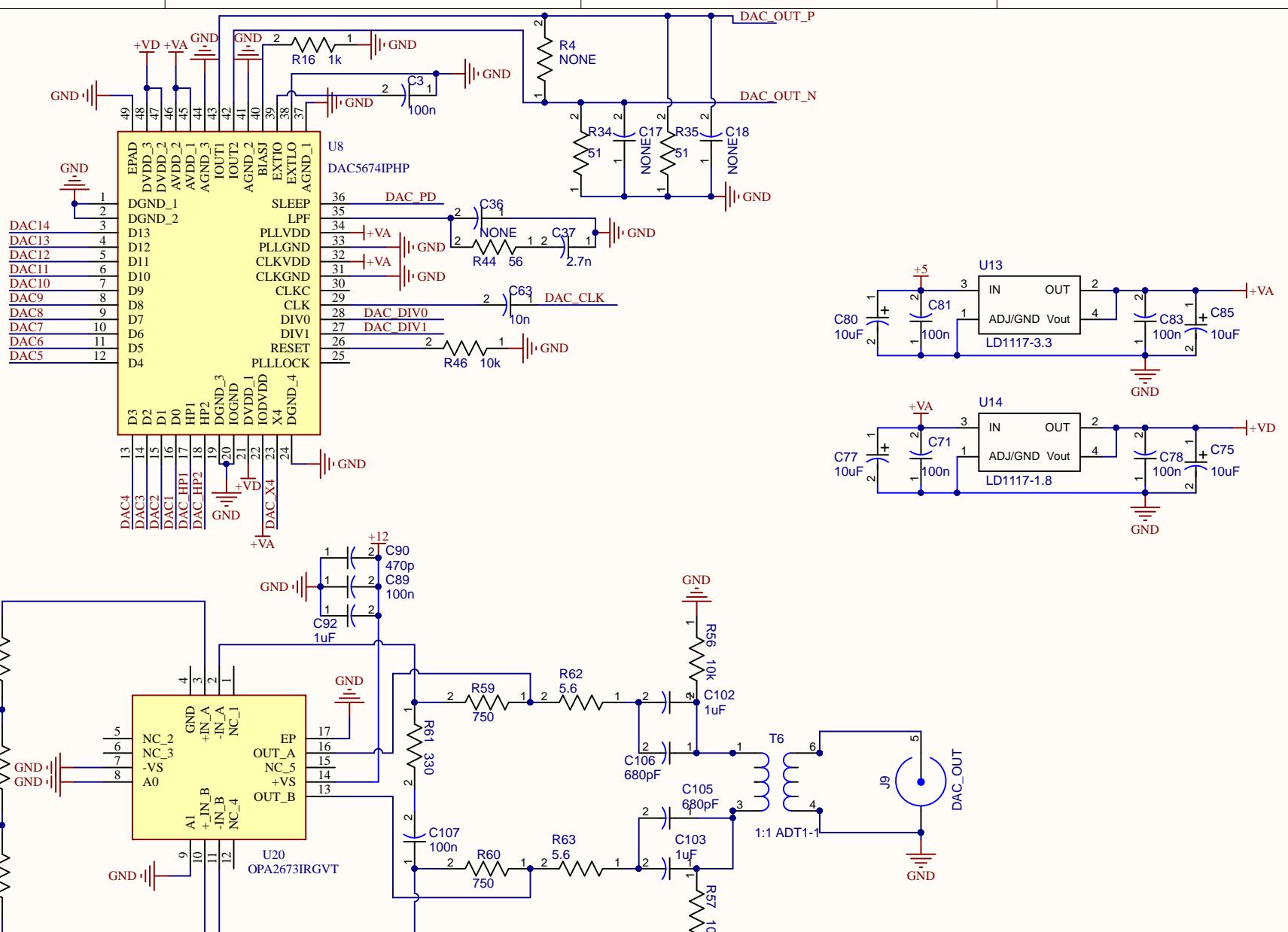


B



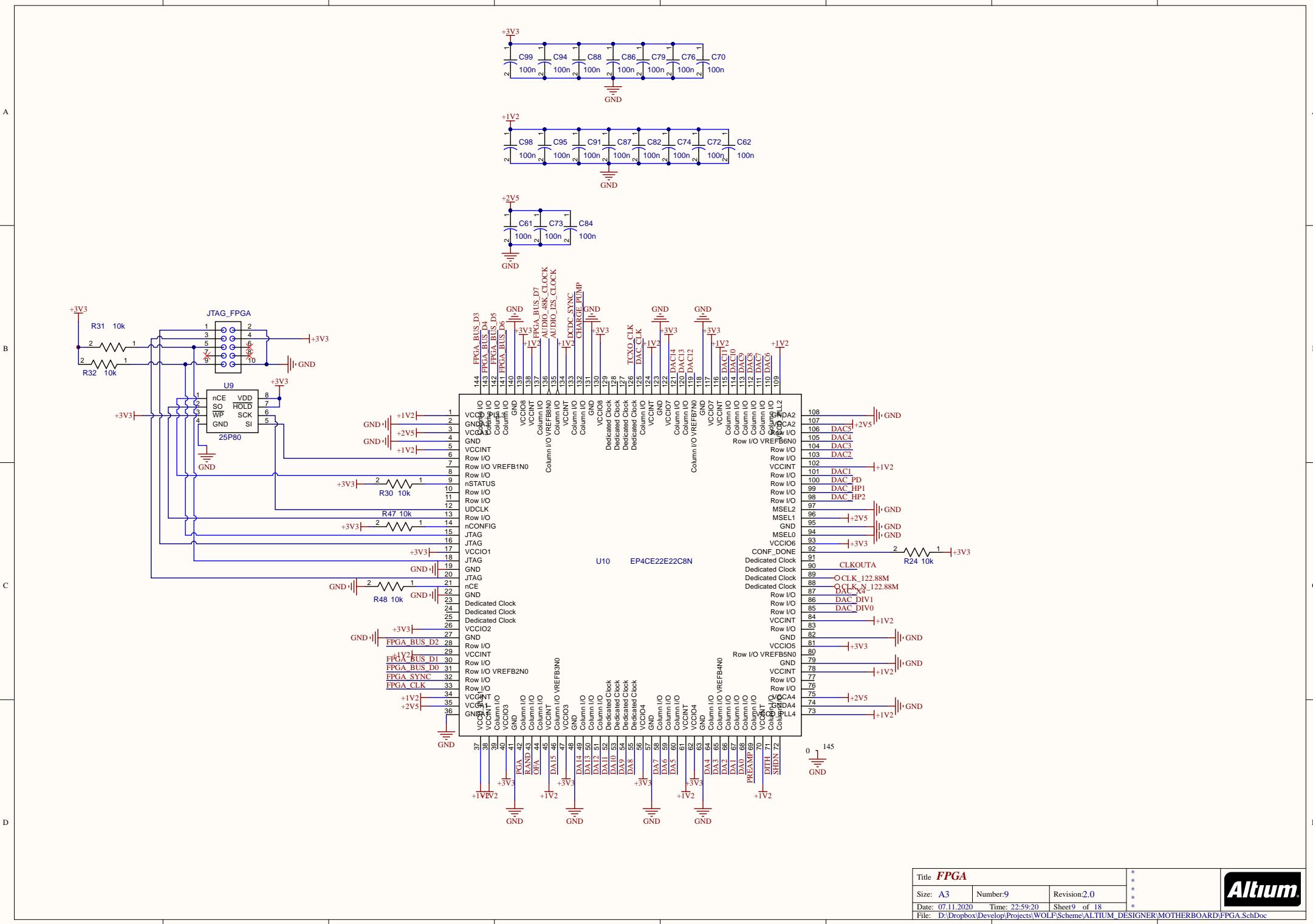
C



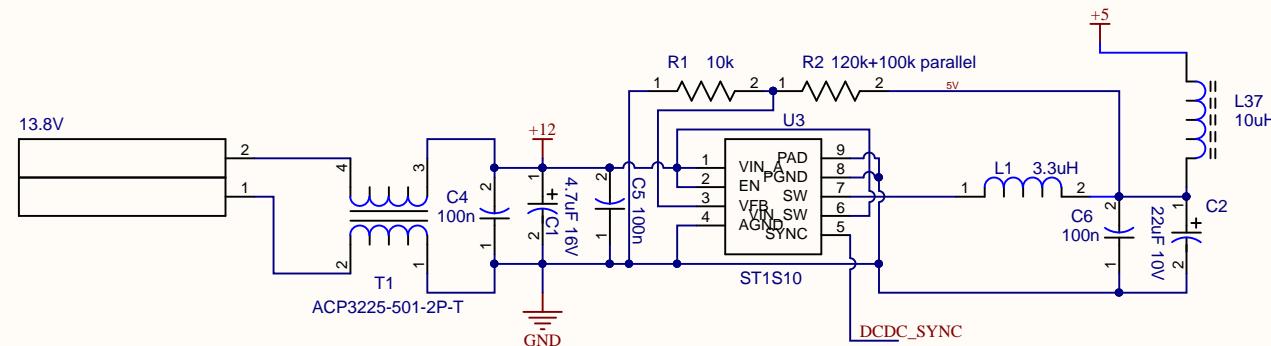


Title DAC		
Size: A4	Number: 8	Revision: 2.0
Date: 07.11.2020	Time: 22:59:20	Sheet 8 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\DAC.SchDoc	*	*

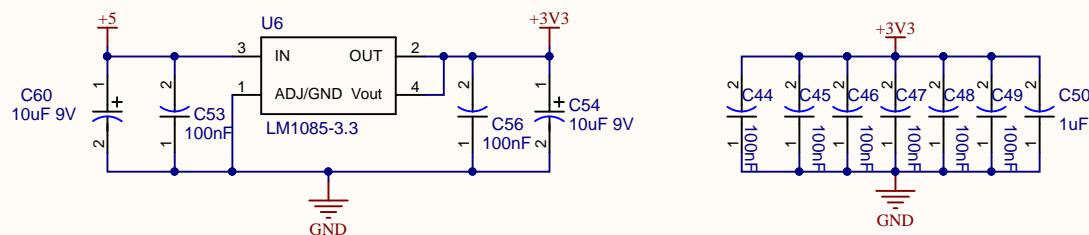
Altium



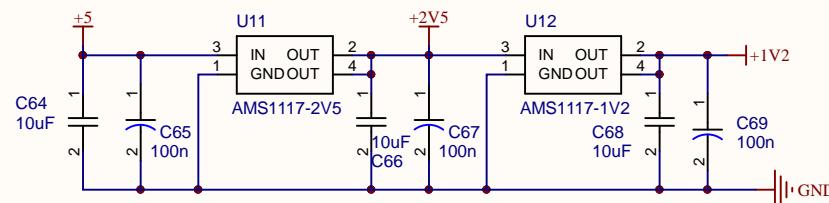
A



B



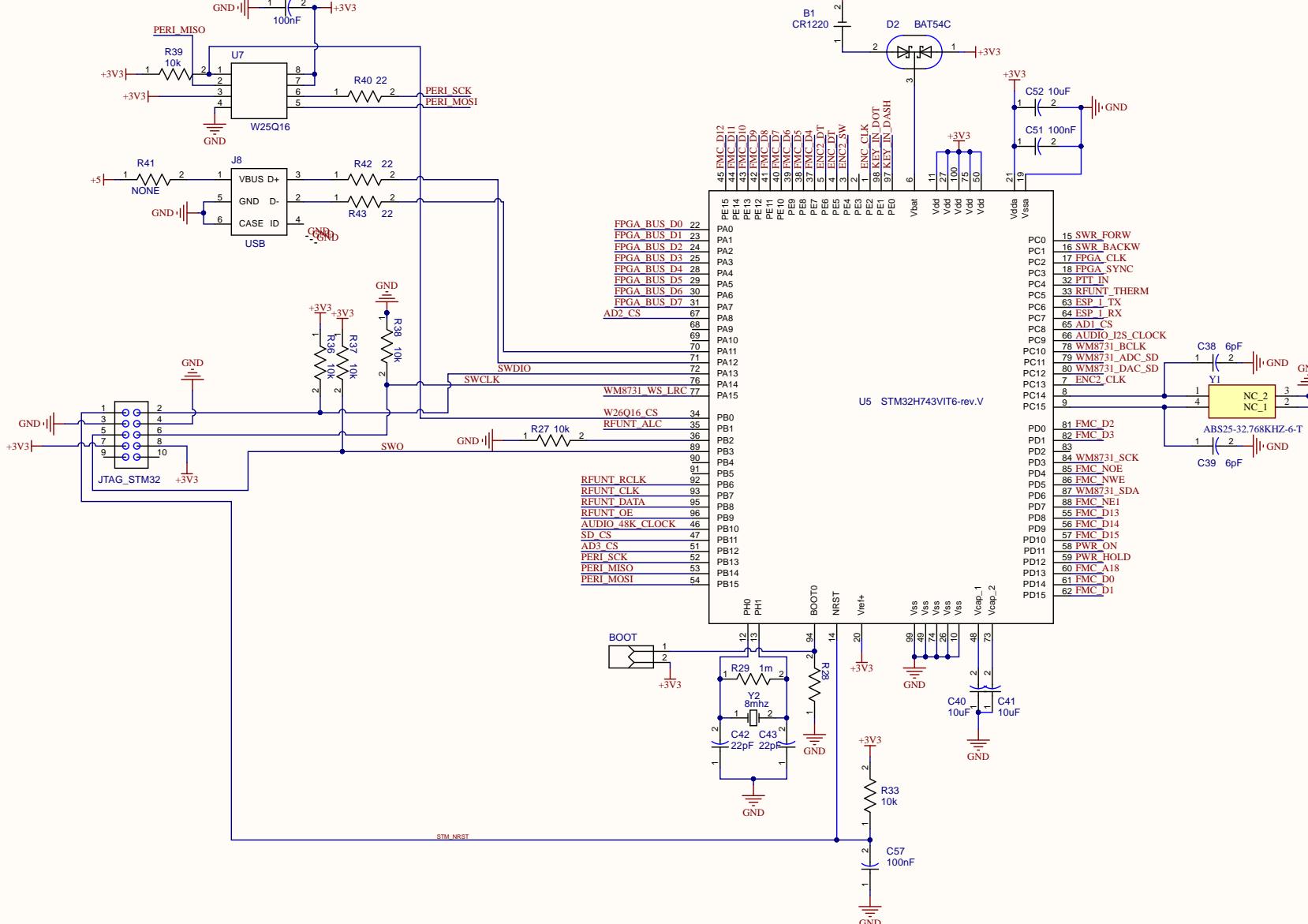
C



D

Title POWER		
Size: A4	Number: 10	Revision: 2.0
Date: 07.11.2020	Time: 22:59:20	Sheet 10 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\POWER.SchDoc		

STM32



A

A

B

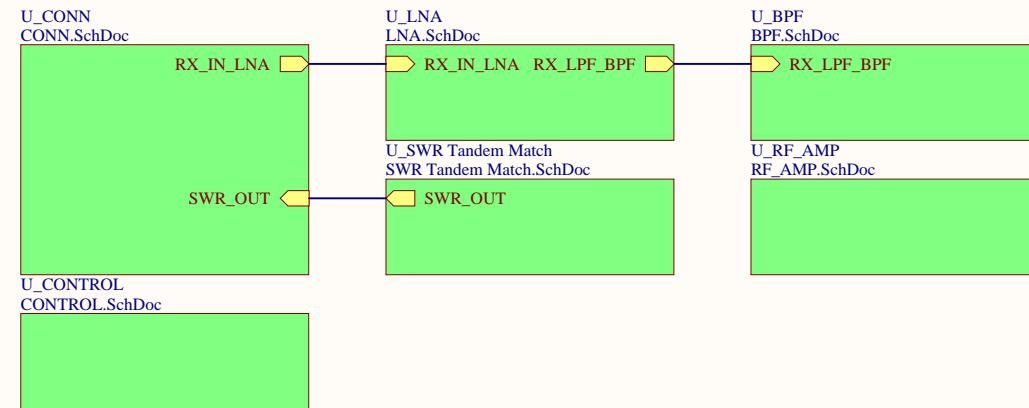
B

C

C

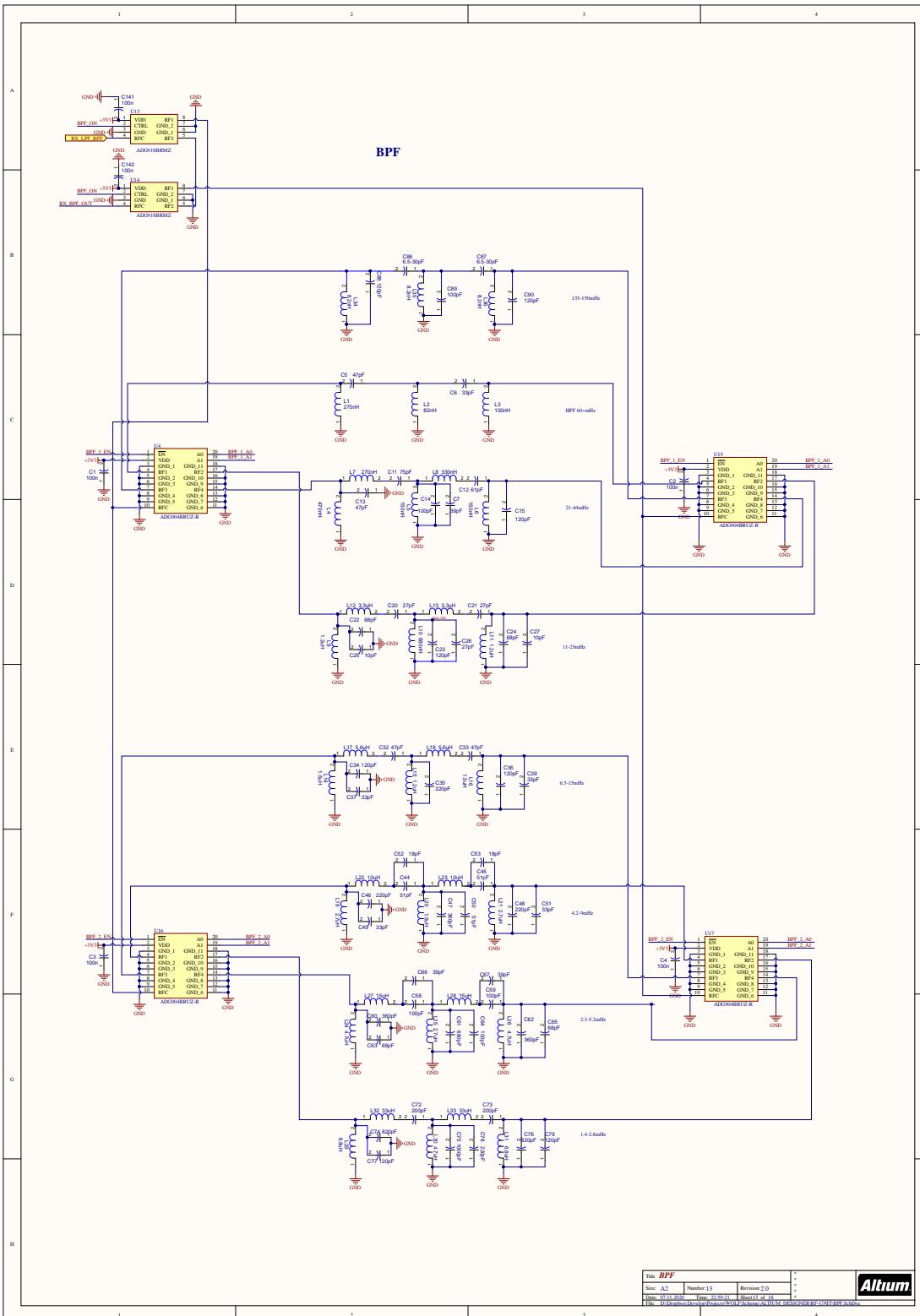
D

D



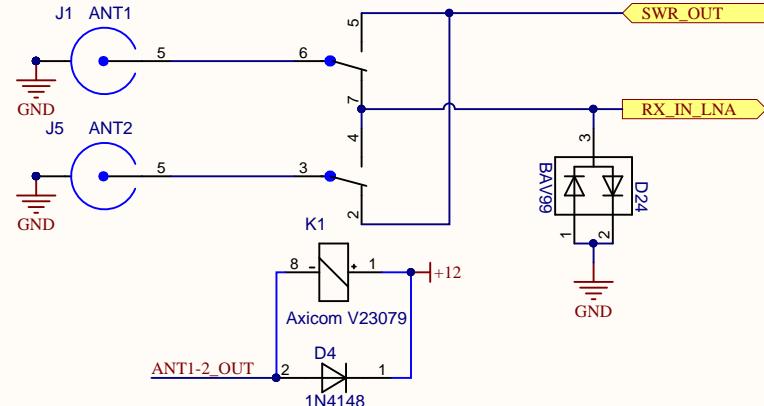
Title RF-UNIT			*
Size: A4	Number:12	Revision:2.0	*
Date: 07.11.2020	Time: 22:59:21	Sheet 12 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\RF-UNIT.SCHDOC			*

Altium

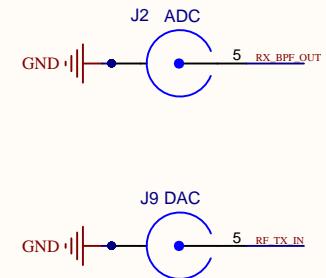


A

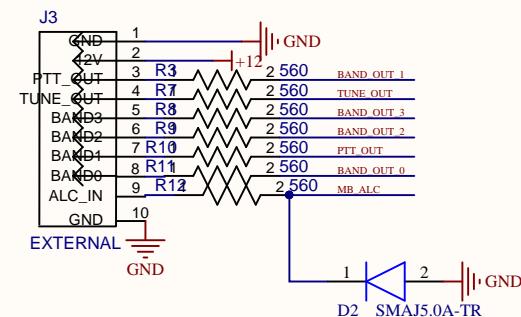
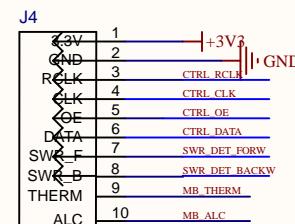
TXRX_COMUTATOR



CONN



B

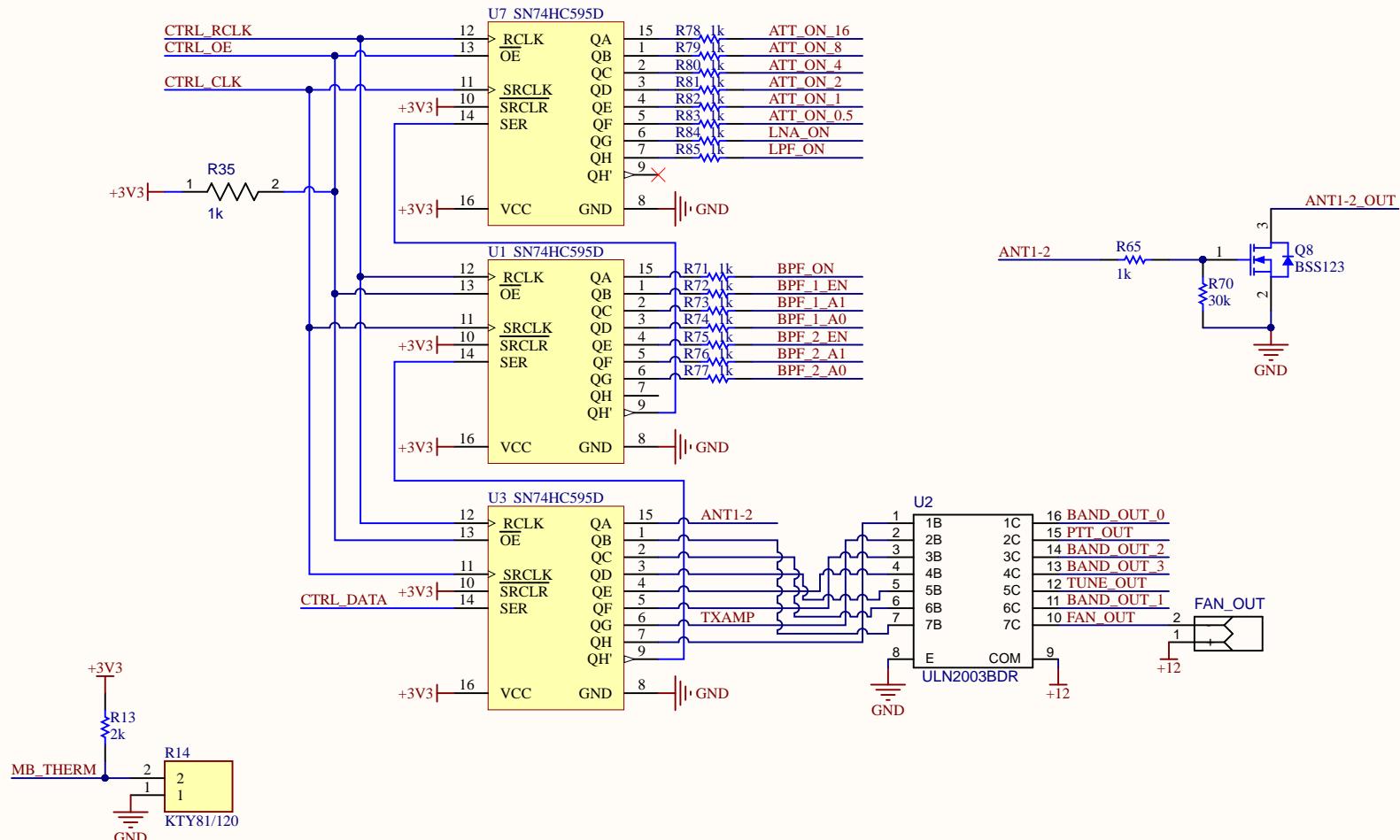
Title **CONN**

Size: A4 Number:14 Revision:2.0

Date: 07.11.2020 Time: 22:59:21 Sheet 14 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONN.SchDoc

A

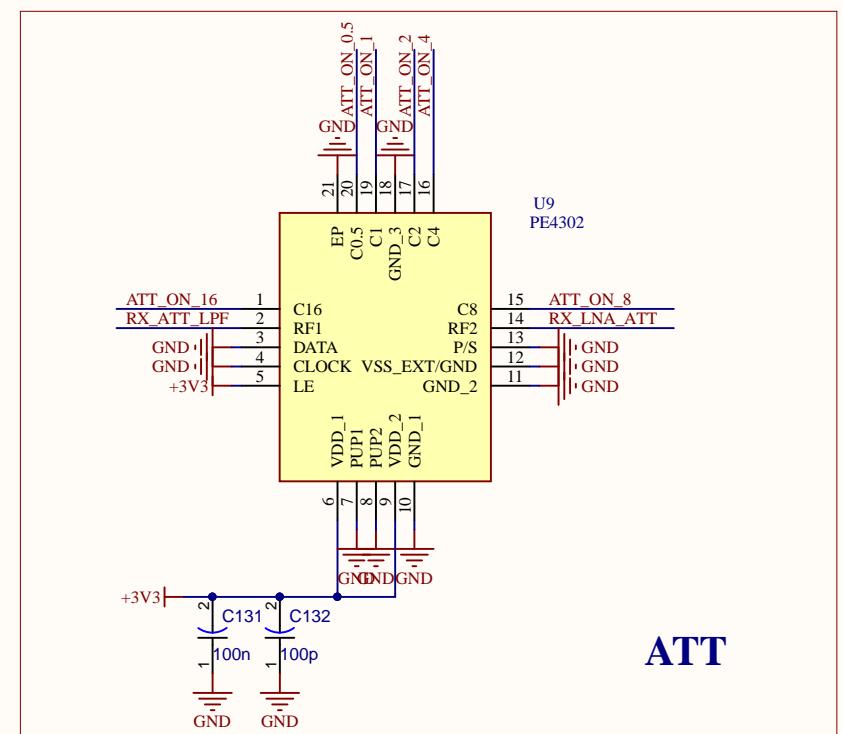
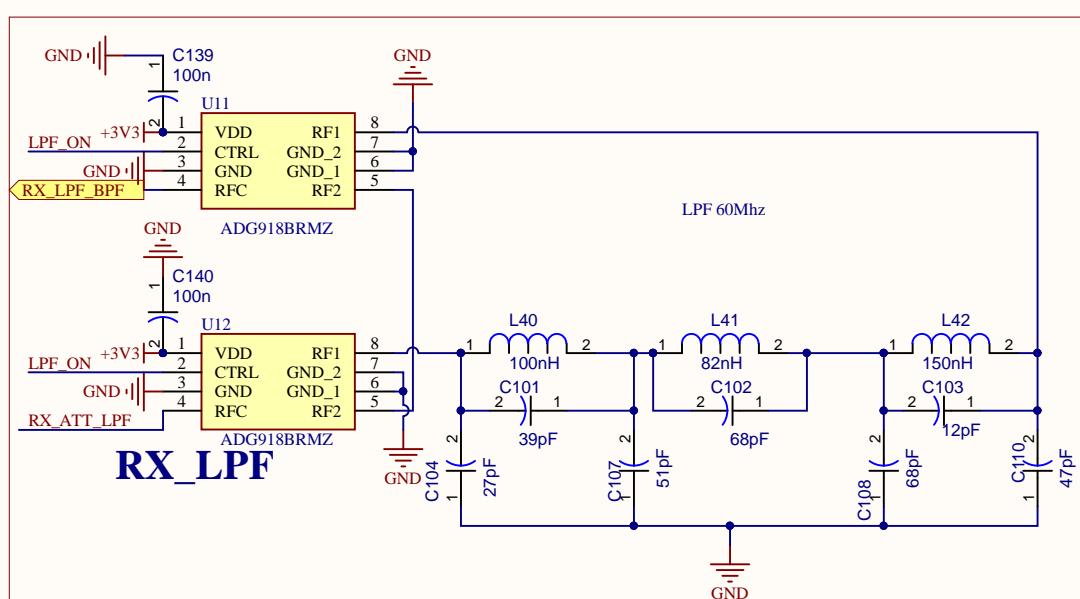
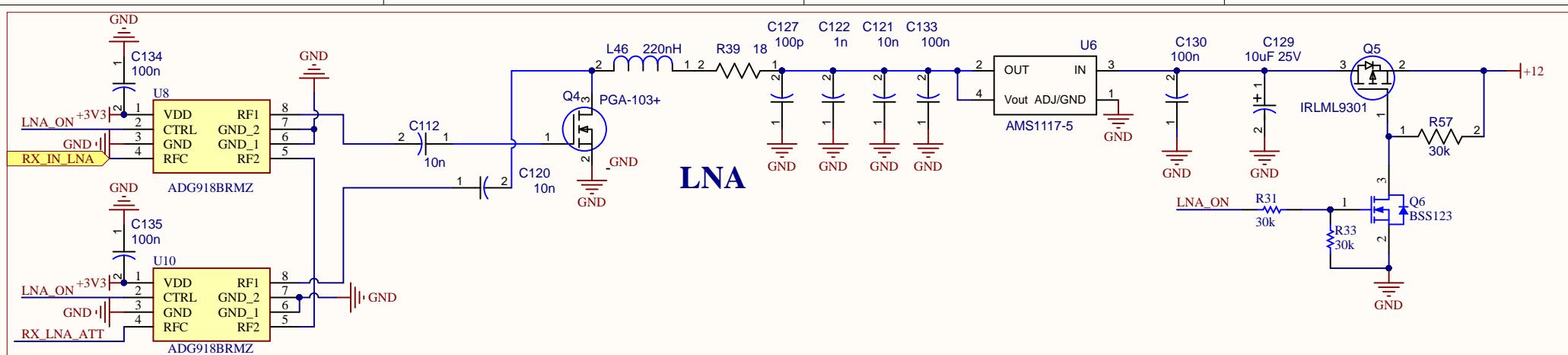
Title **CONTROL**

Size: A4 Number:15 Revision:2.0

Date: 07.11.2020 Time: 22:59:21 Sheet 15 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONTROL.SchDoc

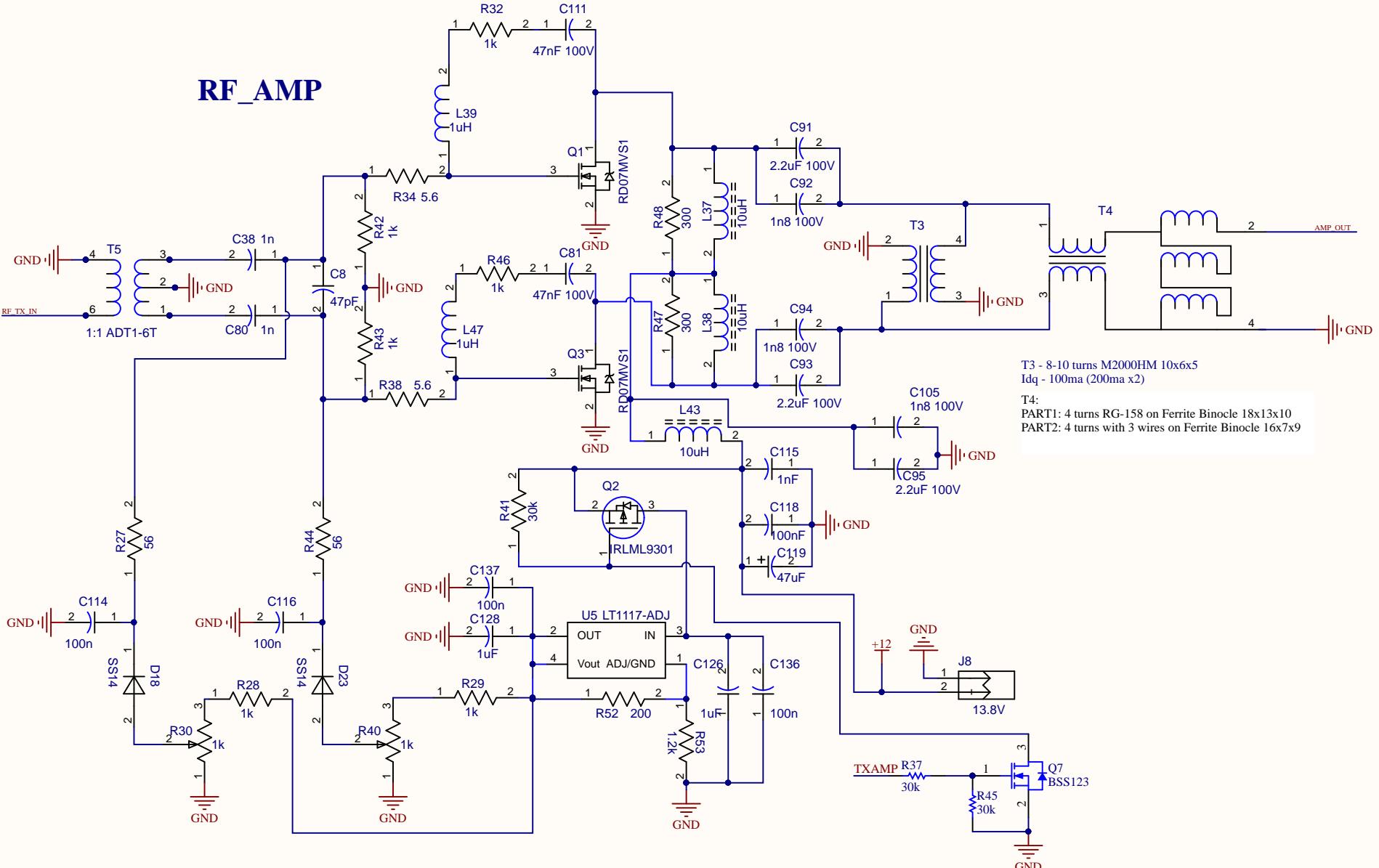
Altium



Title LNA			*
Size: A4	Number: 16	Revision: 2.0	*
Date: 07.11.2020	Time: 22:59:22	Sheet 16 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\LNA.SchDoc			*

Altium

A

RF_AMPTitle **RF_AMP**

Size: A4 Number: 17 Revision: 2.0

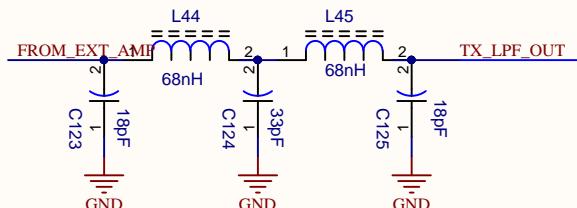
Date: 07.11.2020 Time: 22:59:22 Sheet 17 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\RF_AMP.SchDoc

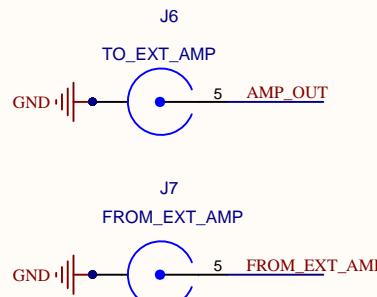
Altium

TX_LPF

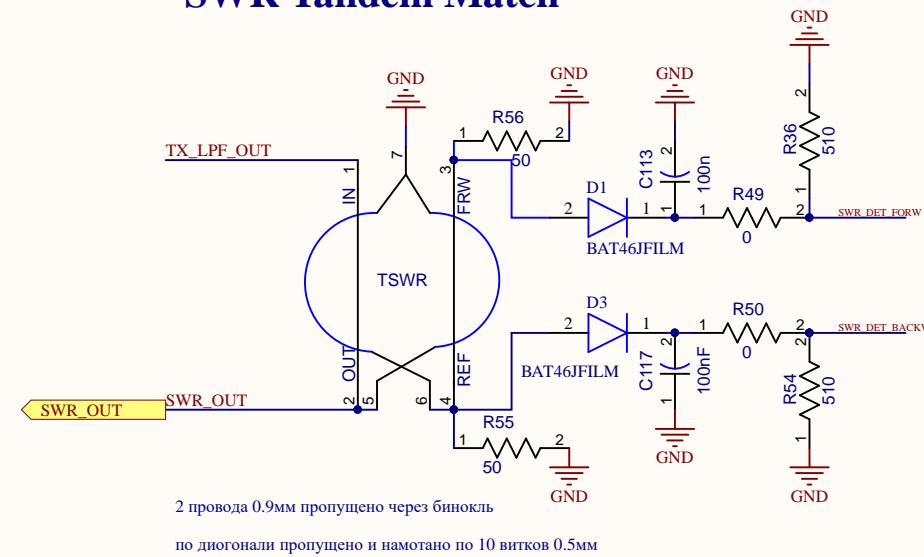
LPF 170Mhz



Катушки намотаны проводом 0.5мм на оправке диаметром 5мм, 3.5 витка



SWR Tandem Match



2 провода 0.9мм пропущено через бинокль
по диагонали пропущено и намотано по 10 витков 0.5мм

Title ***SWR Tandem Match***

Size: **A4** Number:**18** Revision:**2.0**

Date: **07.11.2020** Time: **22:59:22** Sheet**18** of **18**

File: **D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\SWR Tandem Match.SchDoc**

