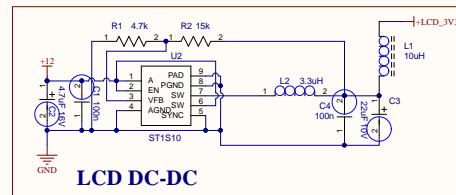
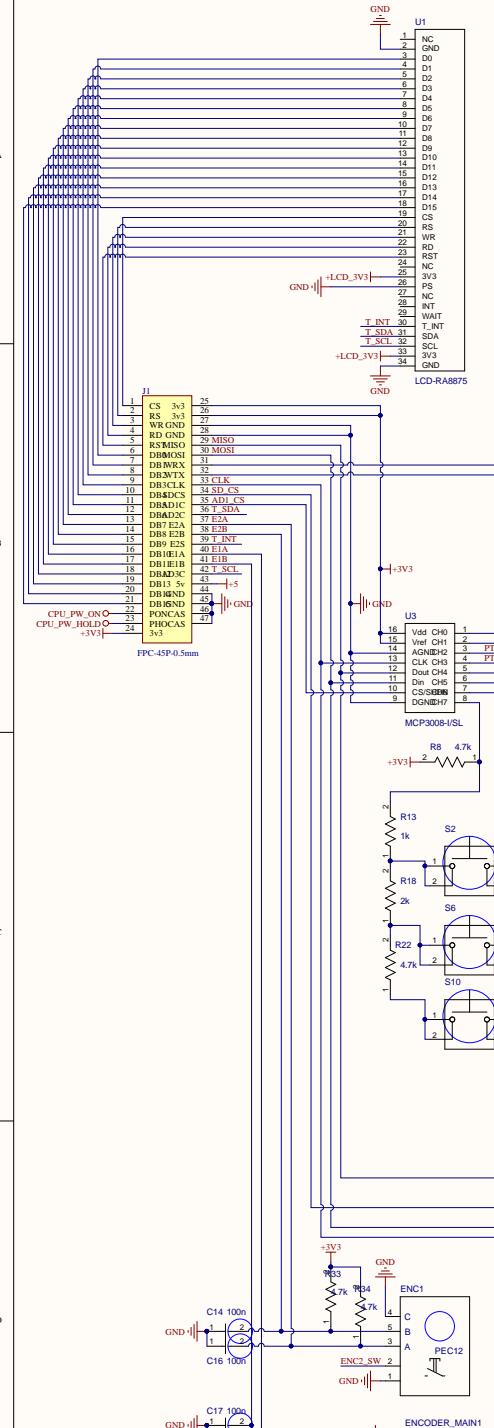


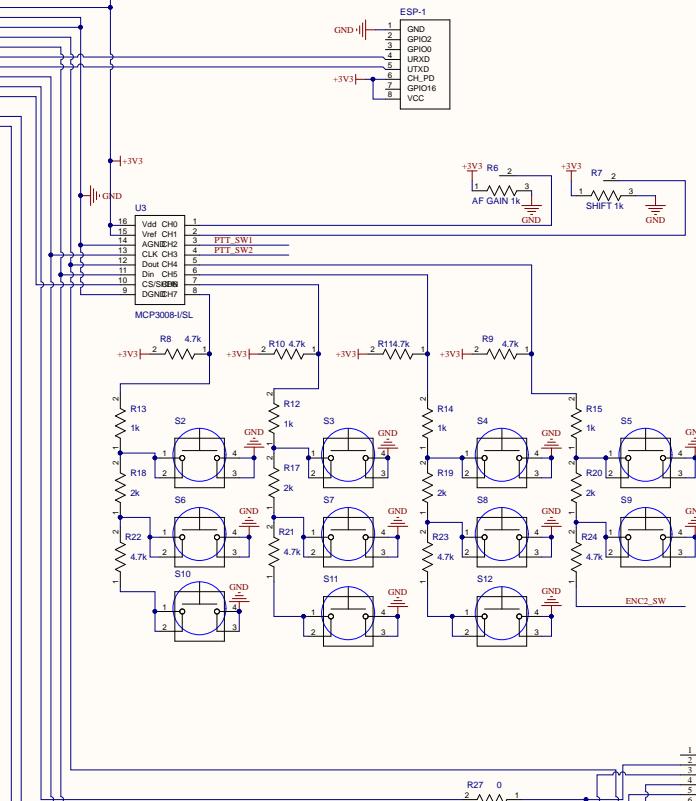
7Inch version



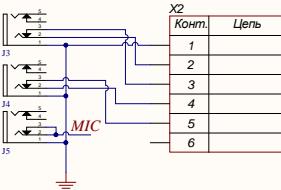
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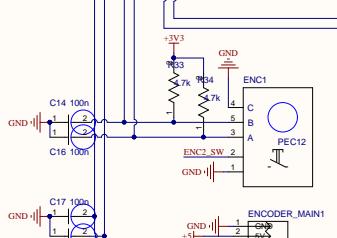
B



C



D



MicroSD

J2

IMMATE-SF-PEJMS

R27

R28

R29

R30

R31

R32

C15

C16

C17

C18

C19

C20

C21

C22

C23

C24

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C300

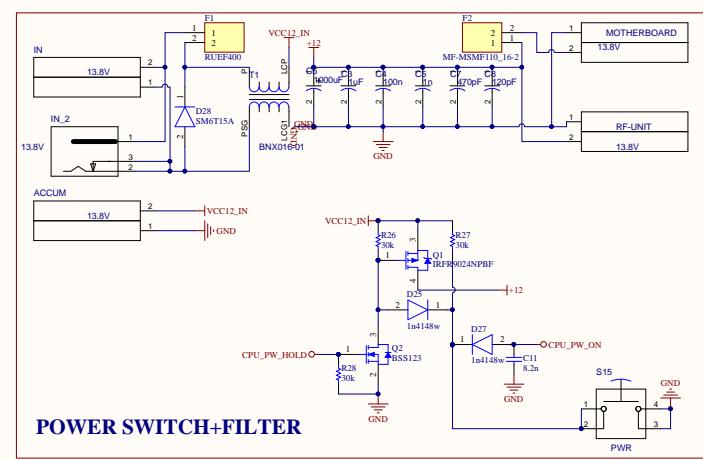
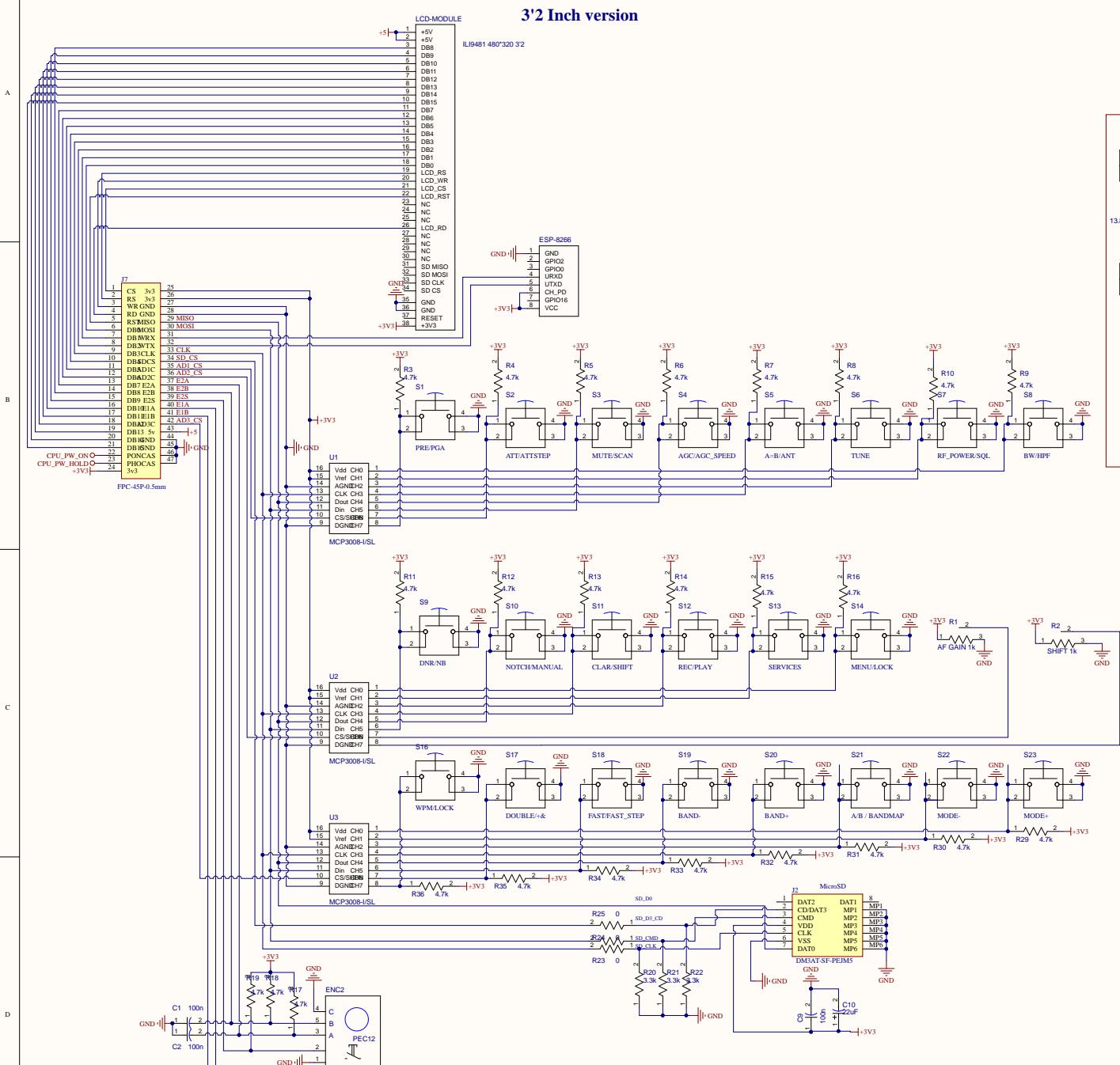
C301

C302

C303

C304

3'2 Inch version



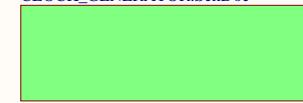
POWER SWITCH+FILTER

A

U_POWER
POWER.SchDoc

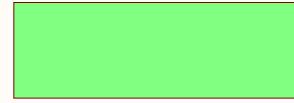


U_CLOCK_GENERATOR
CLOCK_GENERATOR.SchDoc

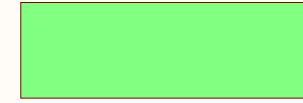


B

U_CONNECTIONS
CONNECTIONS.SchDoc



U_FPGA
FPGA.SchDoc



U_STM32
STM32.SchDoc



U_AUDIO_CODEC
AUDIO_CODEC.SchDoc



A

B

C

D

U_DAC
DAC.SchDoc



U_ADC
ADC.SchDoc



Title **MOTHERBOARD**

Size: A4	Number:2	Revision:2.0
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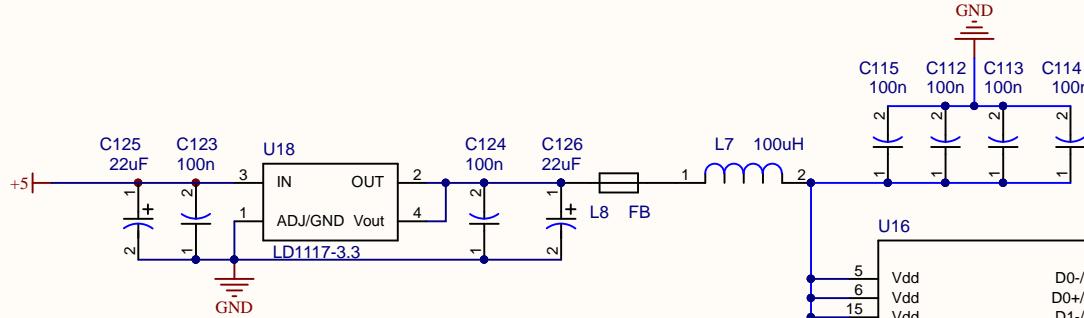
Date: 20.11.2020	Time: 0:43:51	Sheet 2 of 18
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File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\MOTHERBOARD.SCHDOC	*	*	*	*	*
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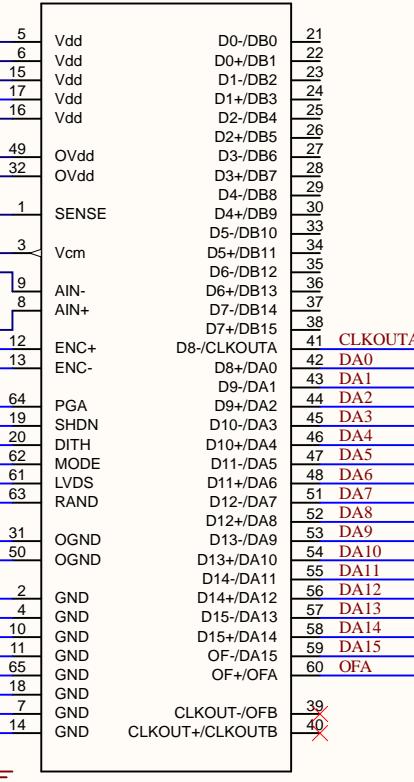
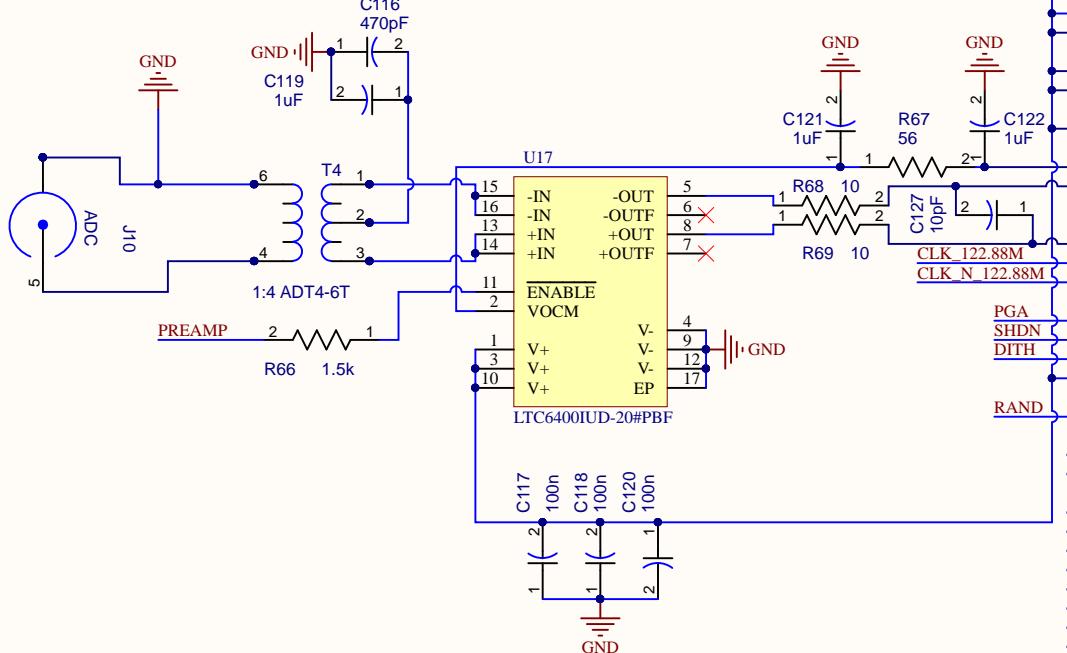
A

A



B

B

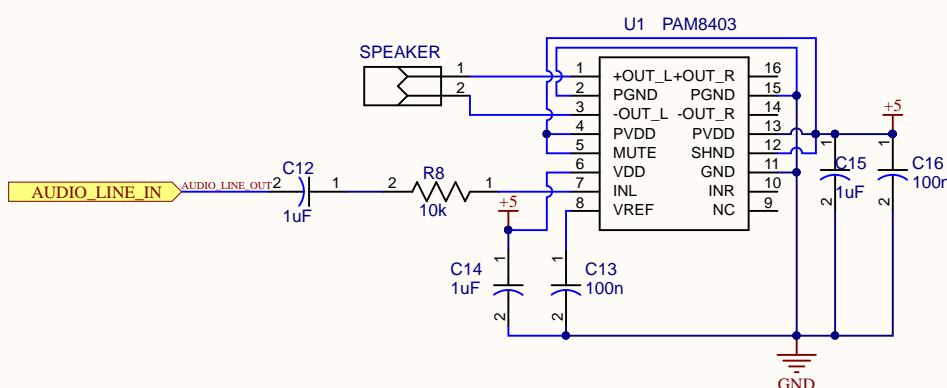
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Size: A4 Number:3 Revision:2.0

Date: 20.11.2020 Time: 0:43:51 Sheet 3 of 18

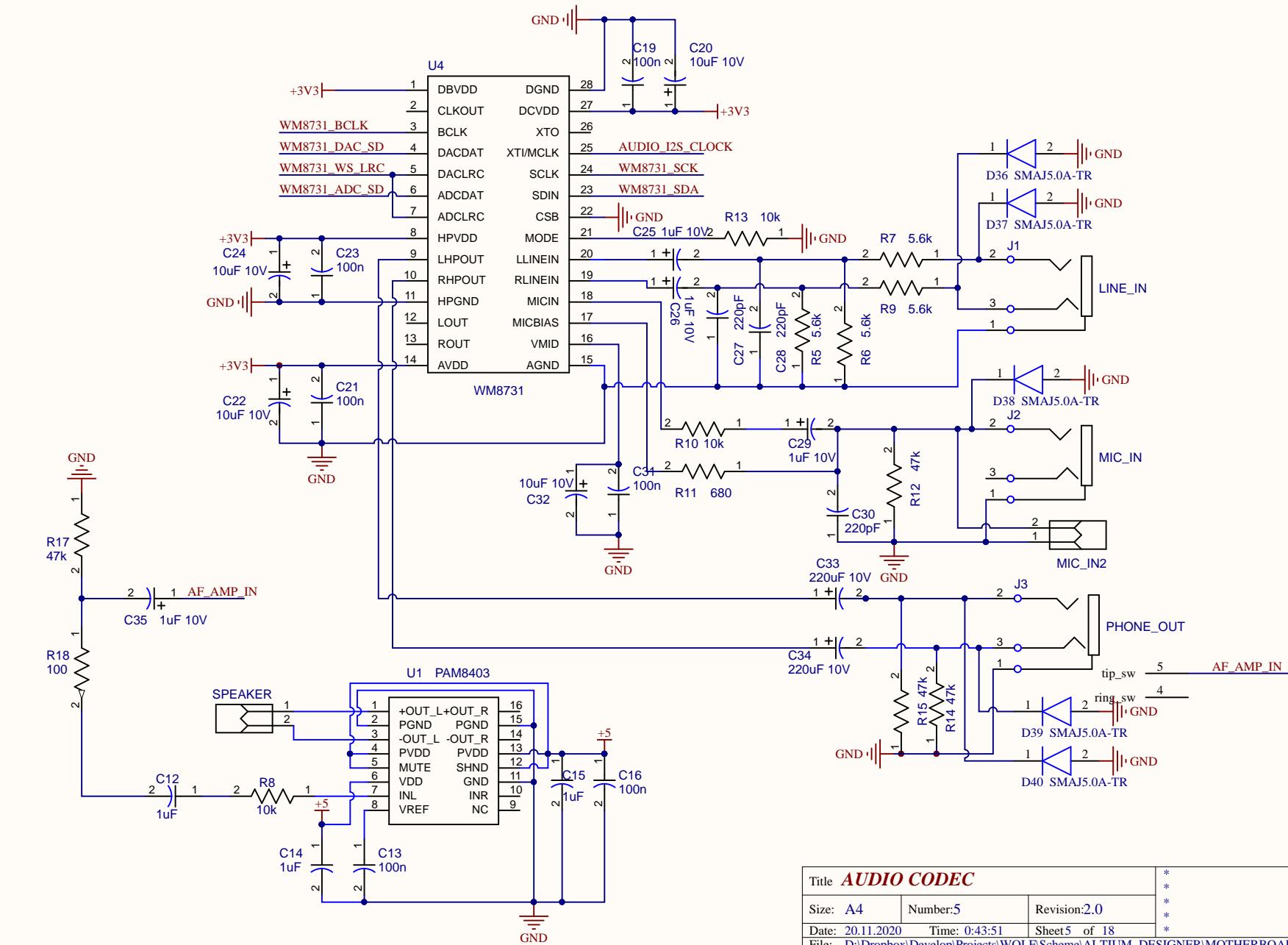
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Altium



Title AF AMP			* UA3REO BH8CGJ *
Size: A4	Number:4	Revision:2.0	
Date: 20.11.2020	Time: 0:43:51	Sheet 4 of 18	
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\AF AMP.SchDoc			Altium

A

Title **AUDIO CODEC**

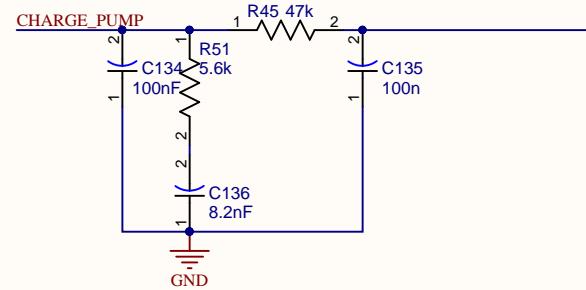
Size: A4 Number:5 Revision:2.0

Date: 20.11.2020 Time: 0:43:51 Sheet 5 of 18

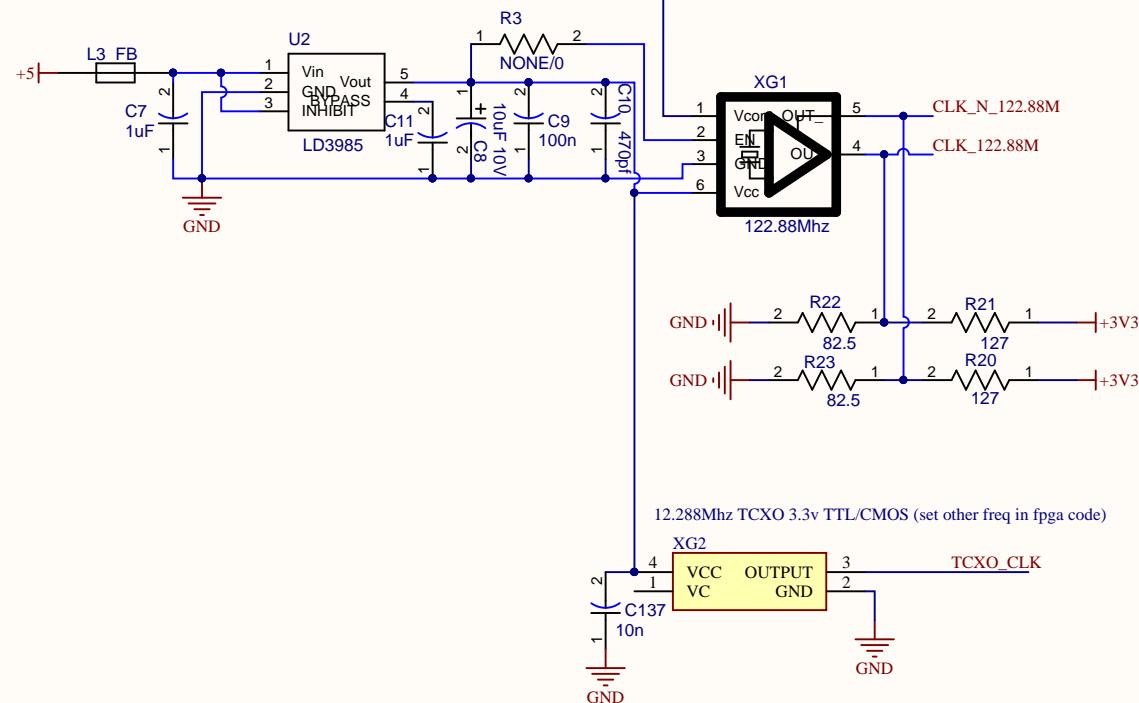
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Altium

A



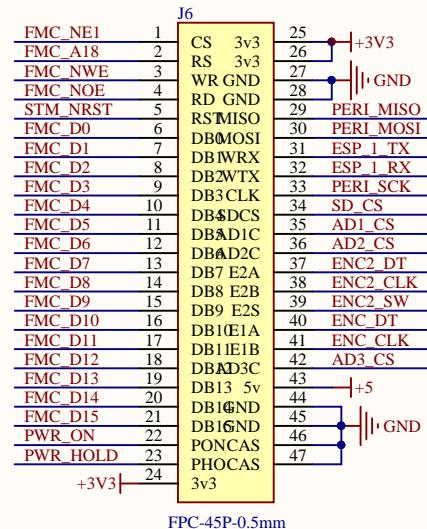
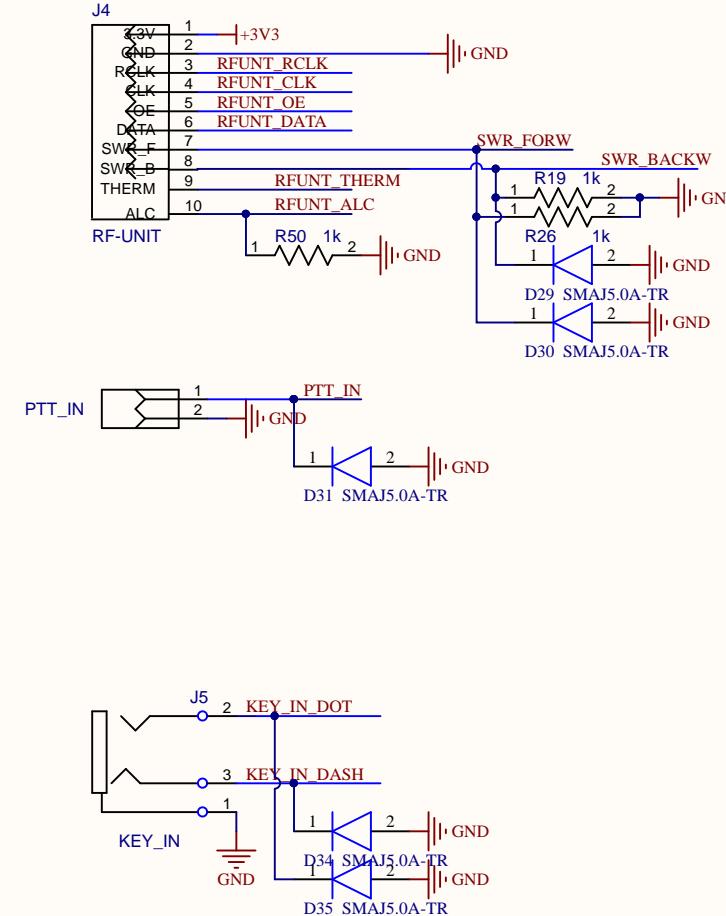
B



C

D

Title **CLOCK_GENERATOR**Size: **A4** Number: **6** Revision: **2.0**Date: **20.11.2020** Time: **0:43:52** Sheet **6** of **18**File: **D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\CLOCK_GENERATOR.SchDoc****Altium**



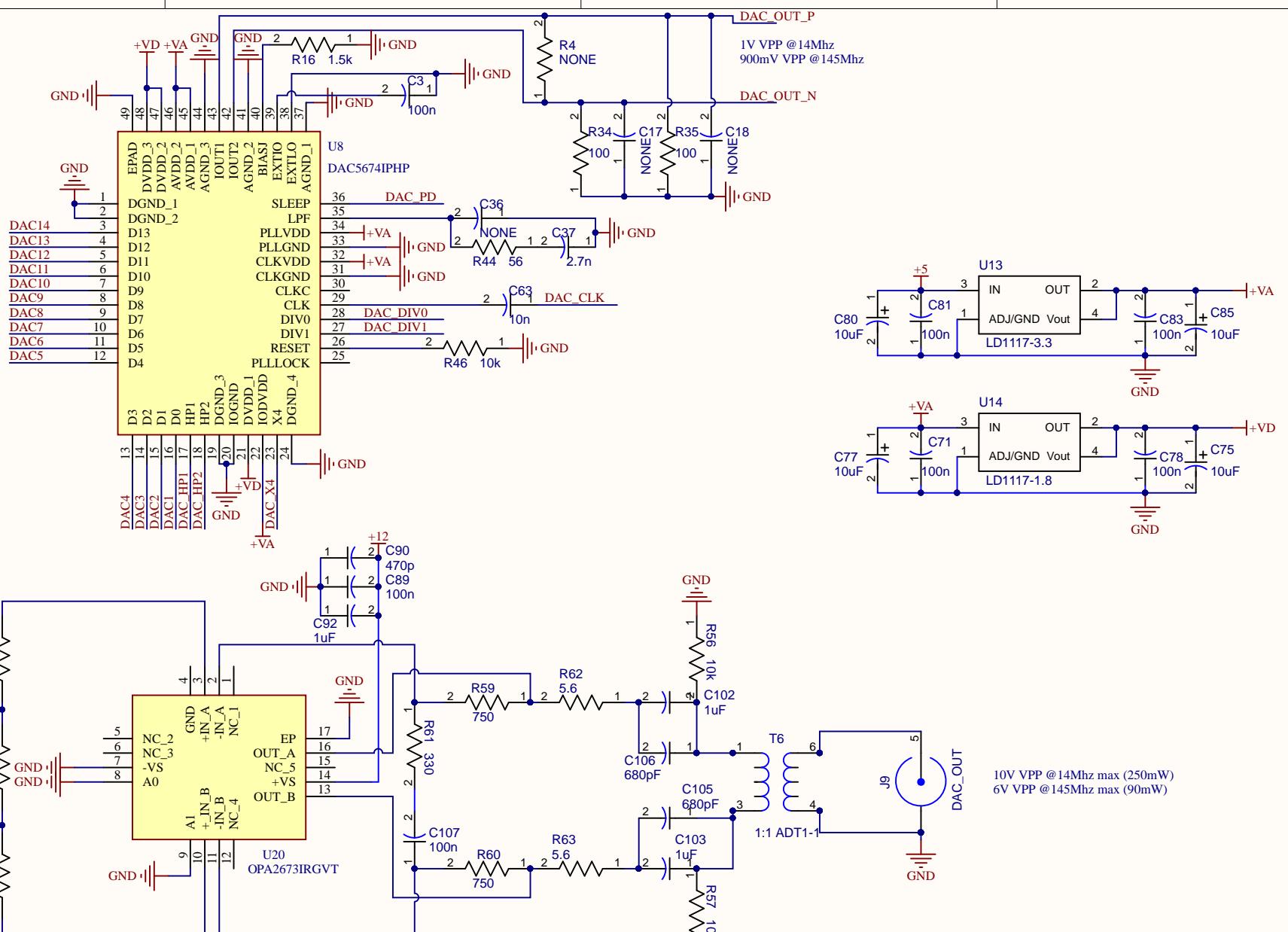
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Size: **A4** Number: **7** Revision: **2.0**

Date: **20.11.2020** Time: **0:43:52** Sheet **7** of **18**

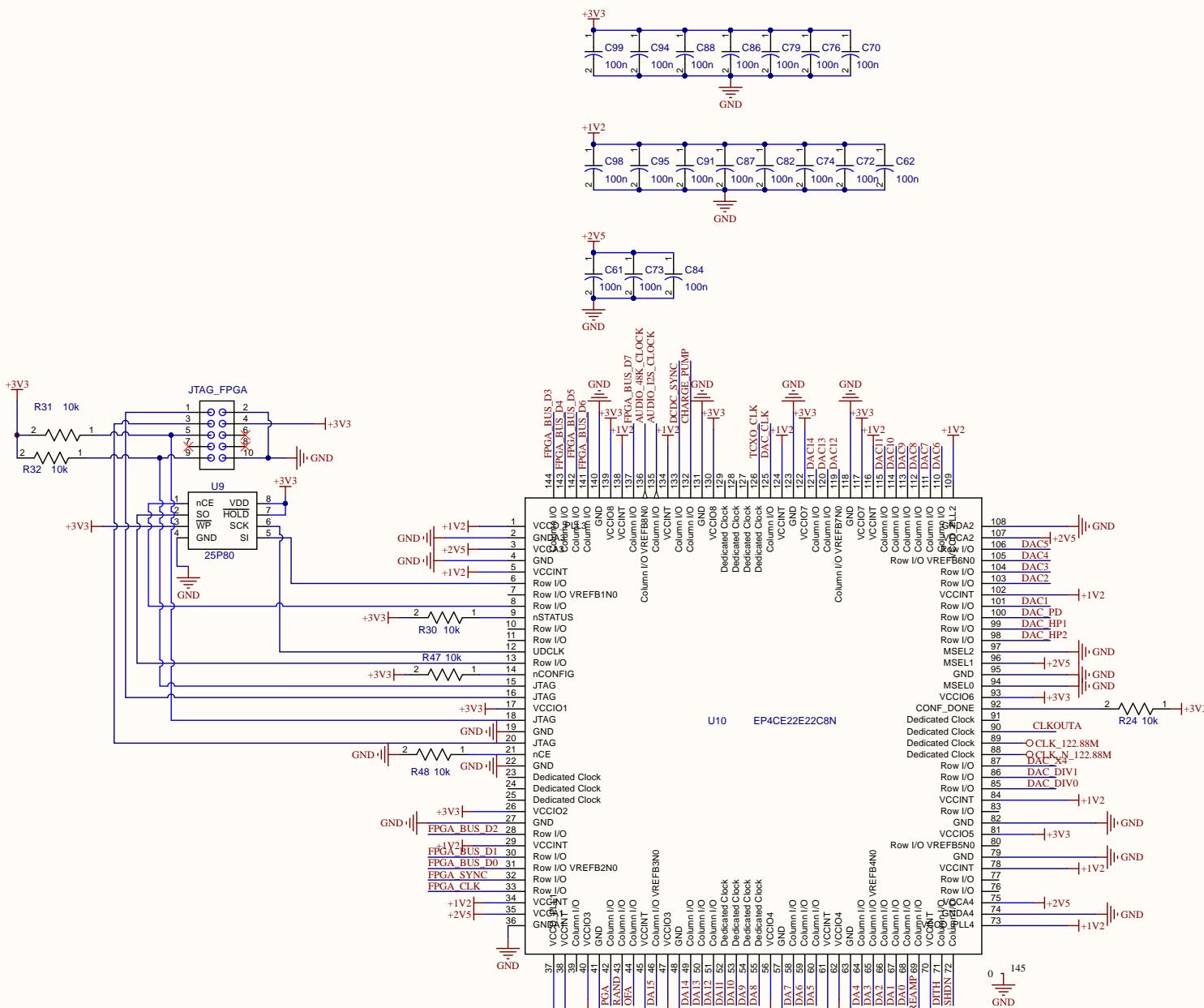
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Altium



Title DAC		
Size: A4	Number: 8	Revision: 2.0
Date: 20.11.2020	Time: 0:43:52	Sheet 8 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\DAC.SchDoc		*

Altium

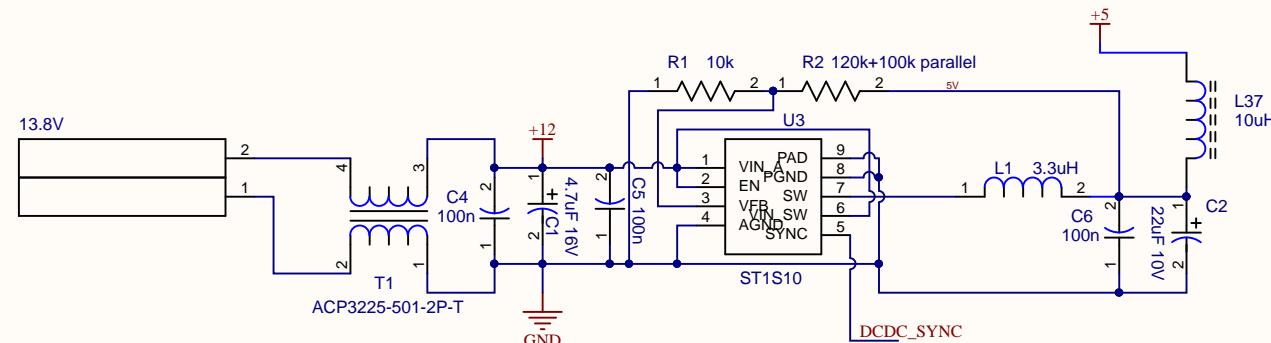


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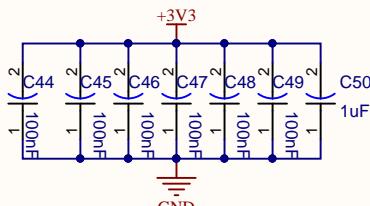
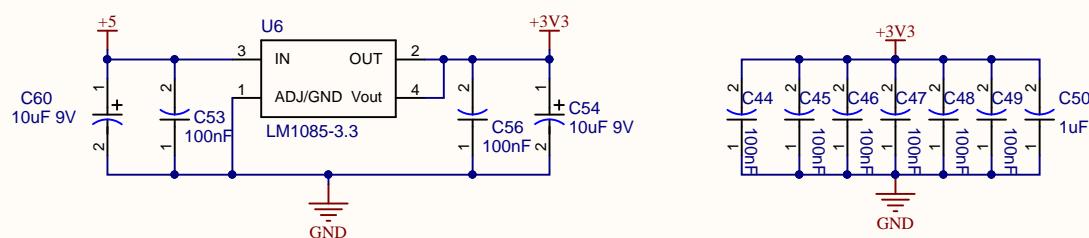
Size: A3 Number:9 Revision:2.0
Date: 20.11.2020 Time: 0:43:52 Sheet 9 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM DESIGN

Altium

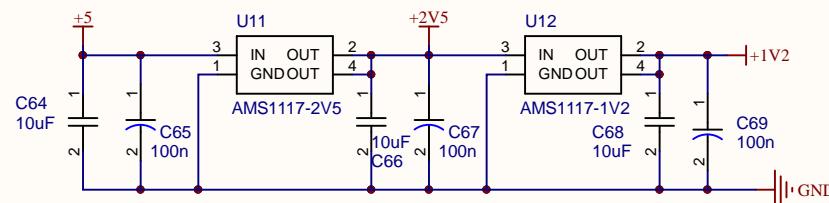
A



B

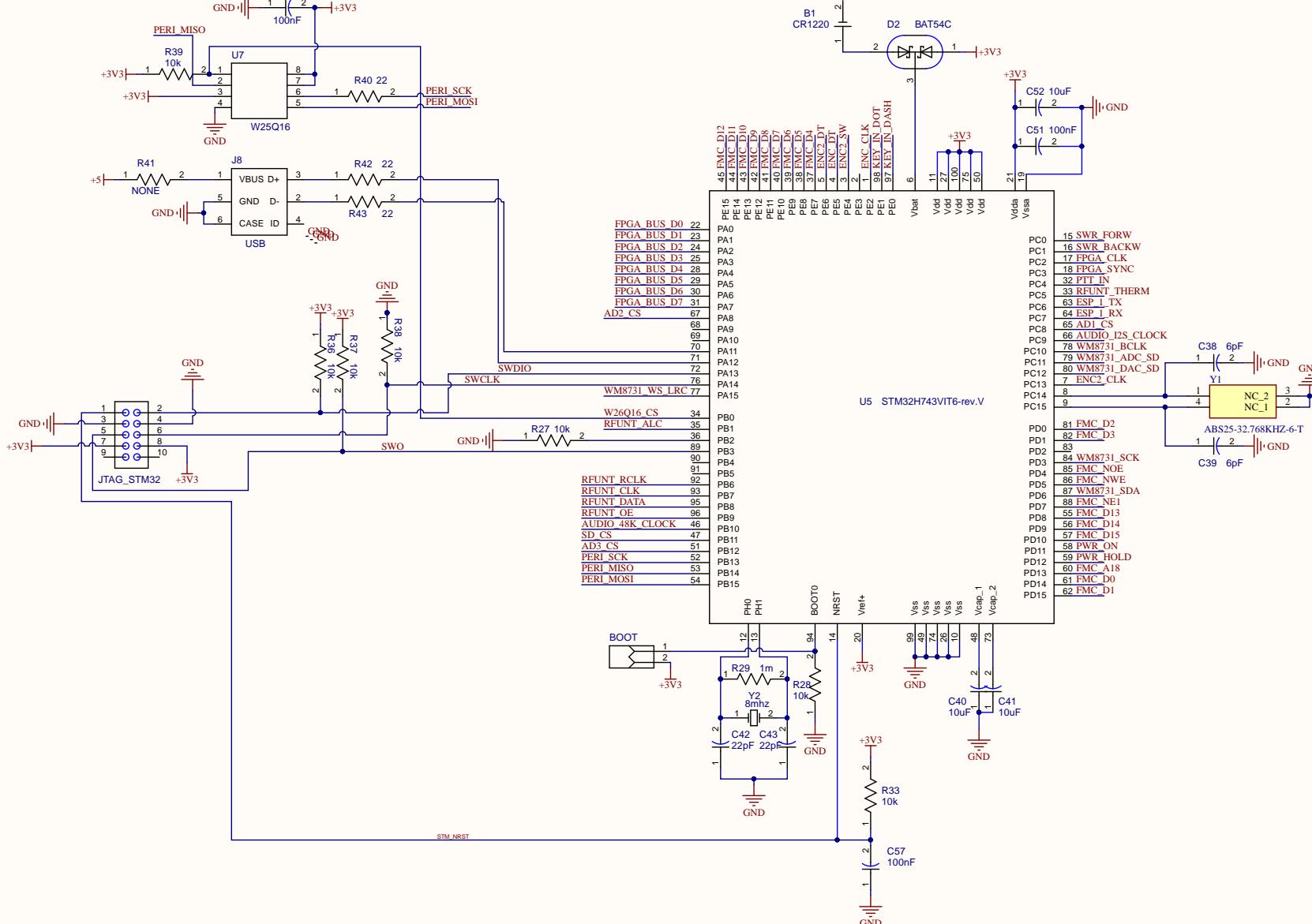


C



Title POWER			*
Size: A4	Number: 10	Revision: 2.0	*
Date: 20.11.2020	Time: 0:43:53	Sheet 10 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\POWER.SchDoc			*

STM32

Title **STM32**

Size: A3 Number:11 Revision:2.0

Date: 20.11.2020 Time: 0:43:53 Sheet 11 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM DESIGNER\MOTHERBOARD\STM32.SchDoc



A

A

B

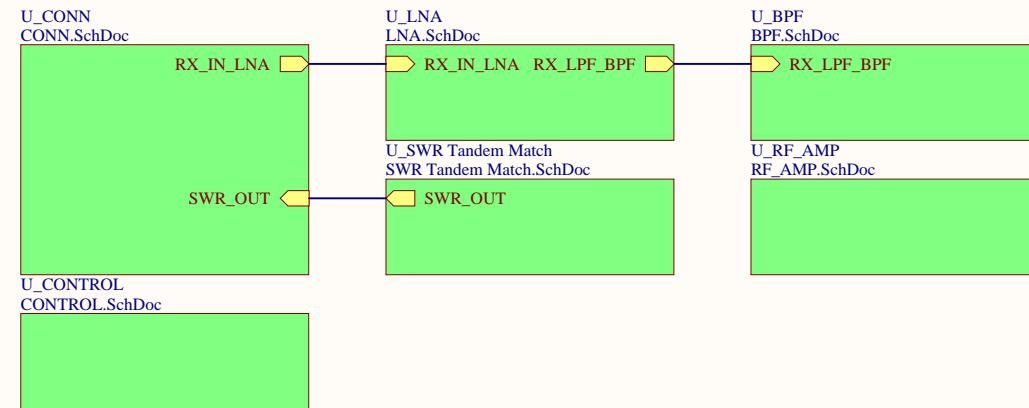
B

C

C

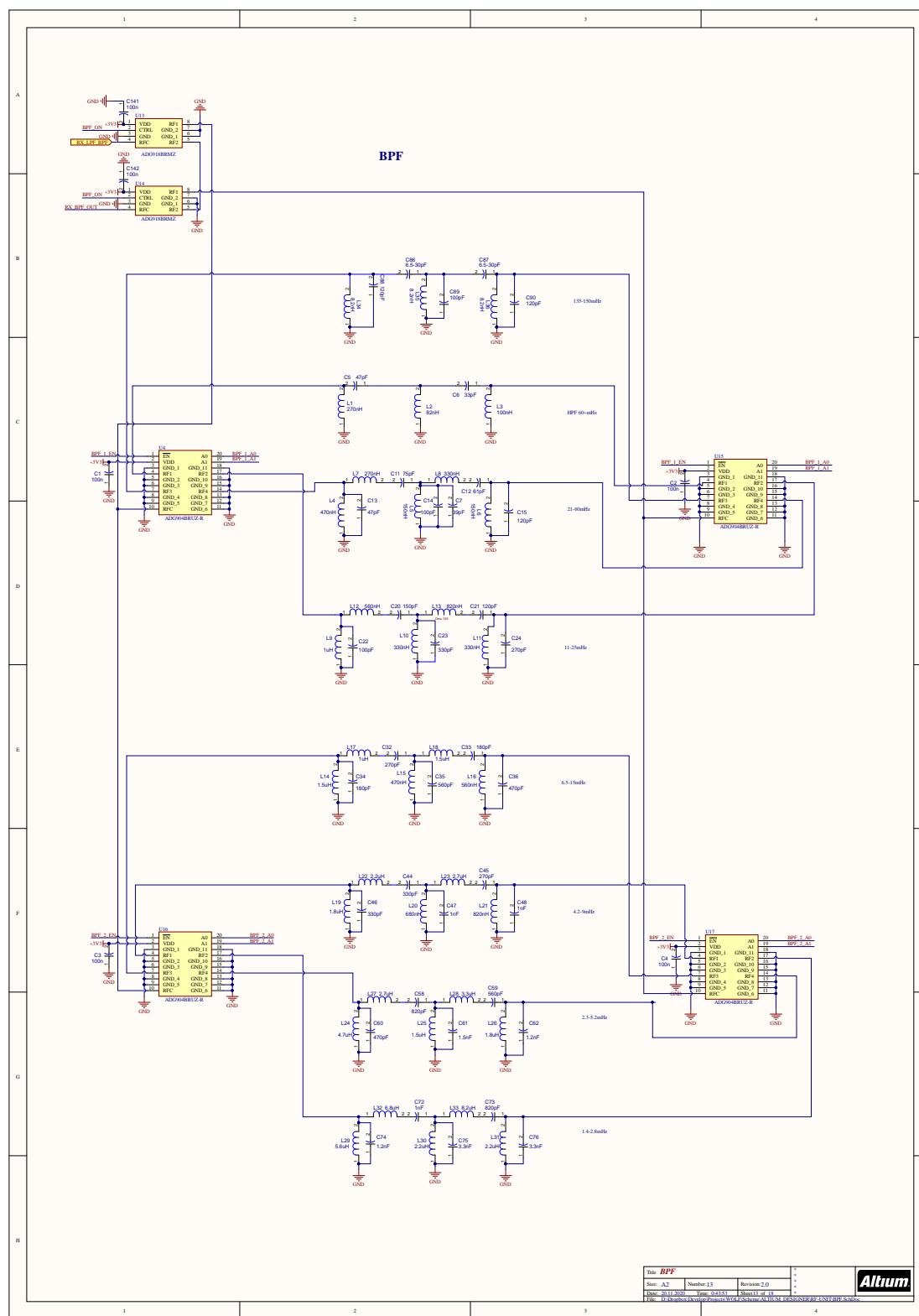
D

D



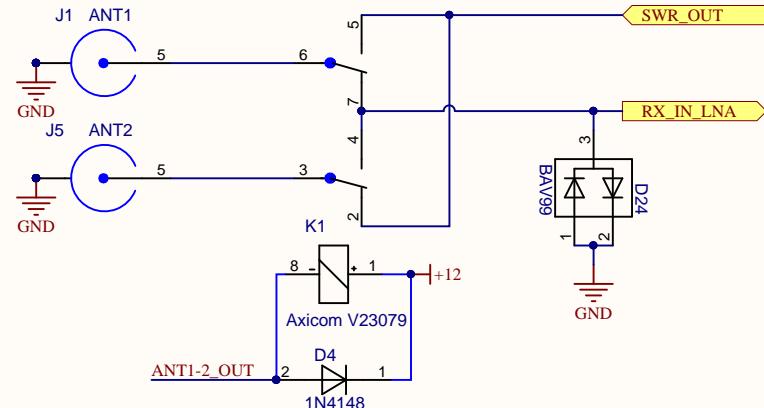
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Size: A4	Number:12	Revision:2.0	*
Date: 20.11.2020	Time: 0:43:53	Sheet 12 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\RF-UNIT.SCHDOC			*

Altium

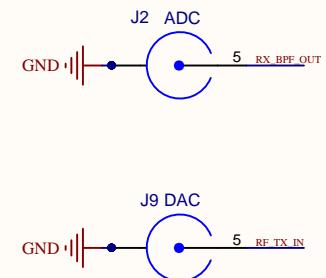


A

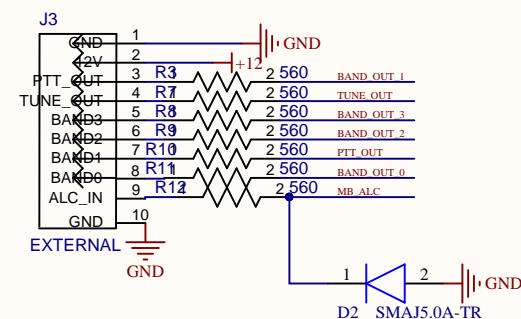
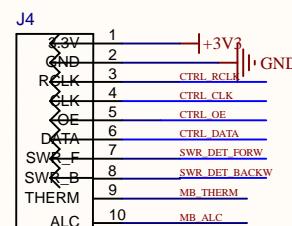
TXRX_COMUTATOR



CONN



B

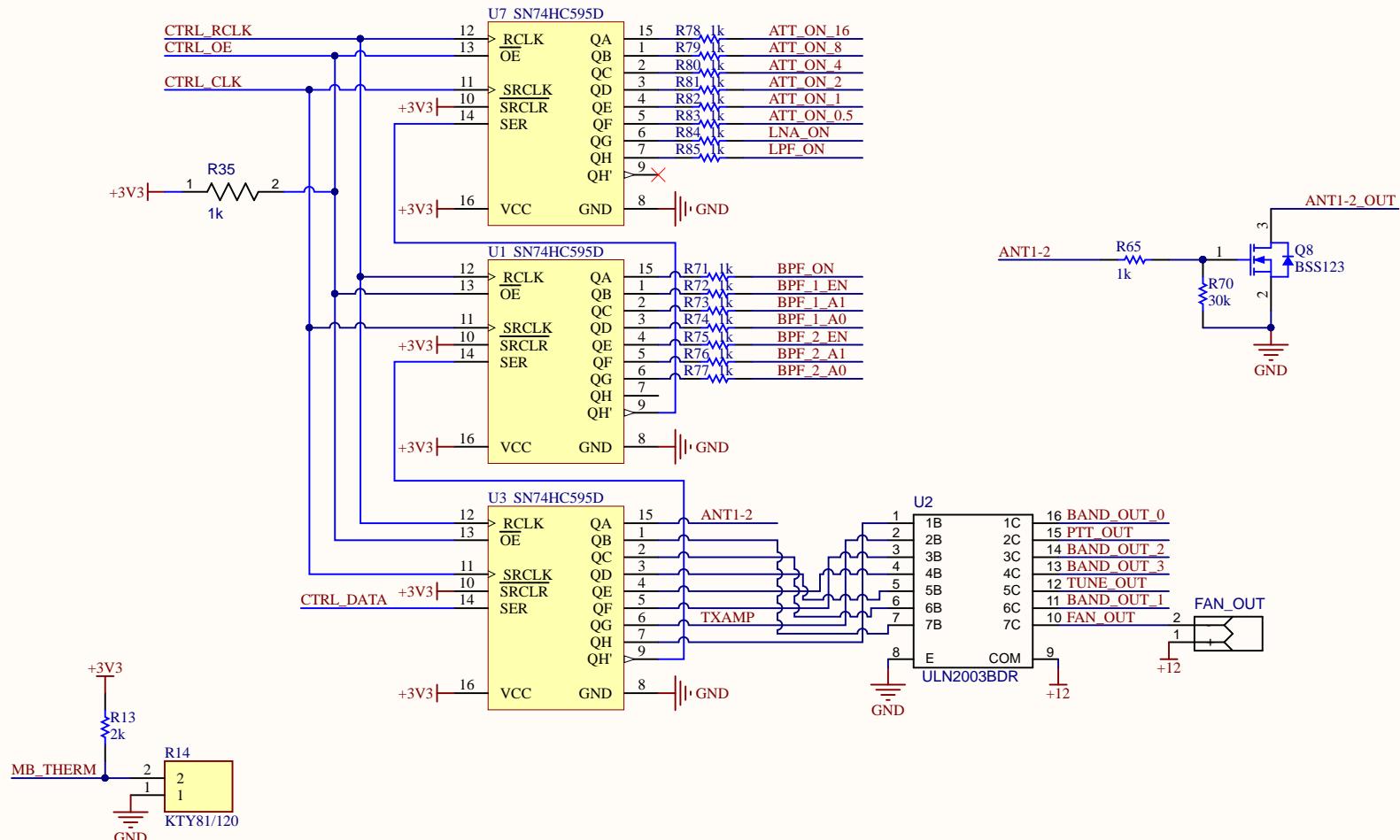
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Size: A4 Number:14 Revision:2.0

Date: 20.11.2020 Time: 0:43:54 Sheet 14 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONN.SchDoc

A

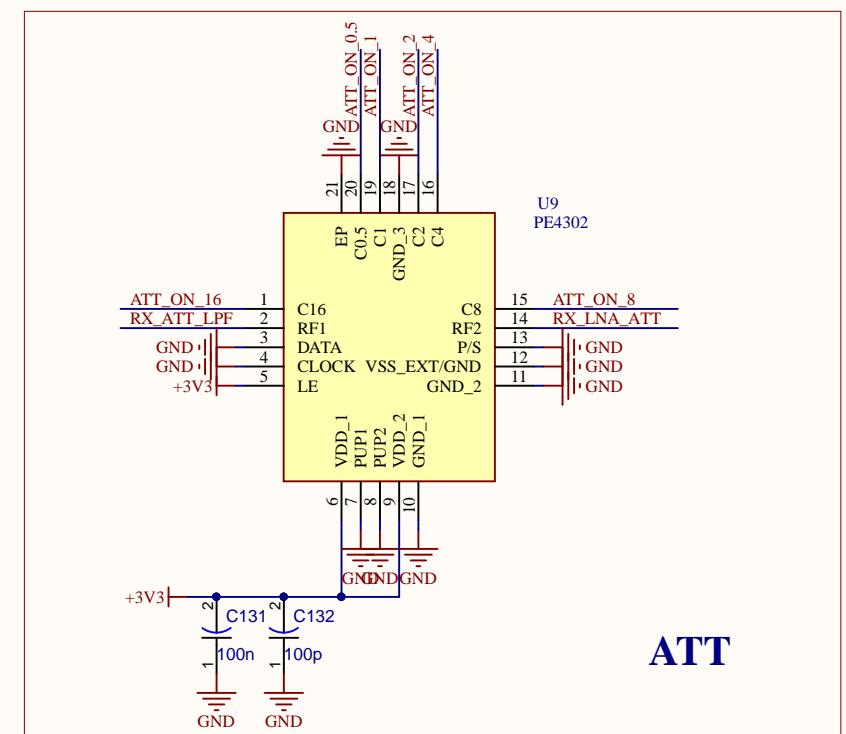
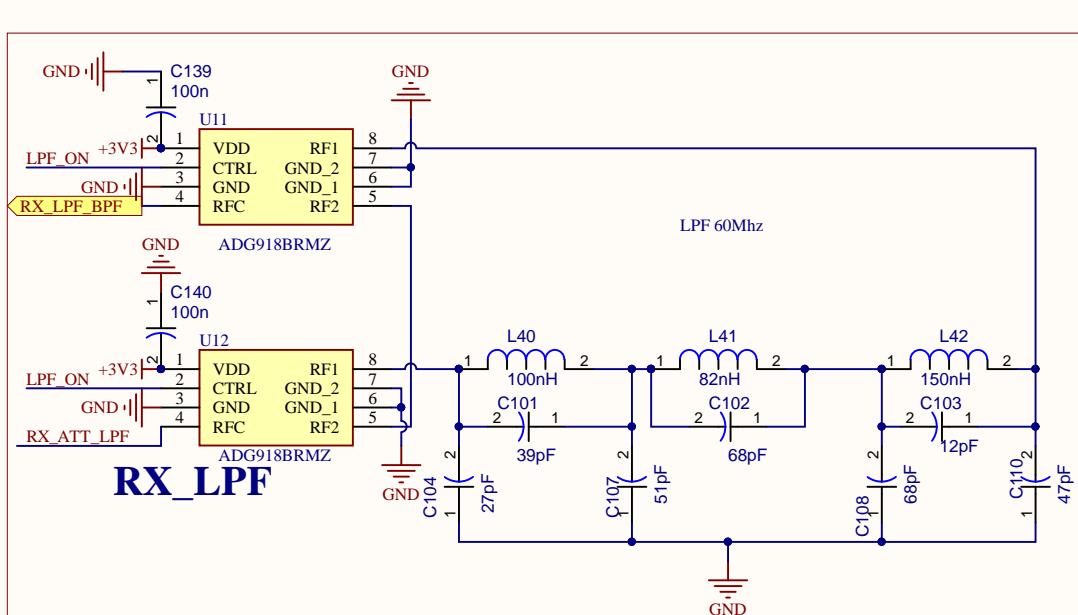
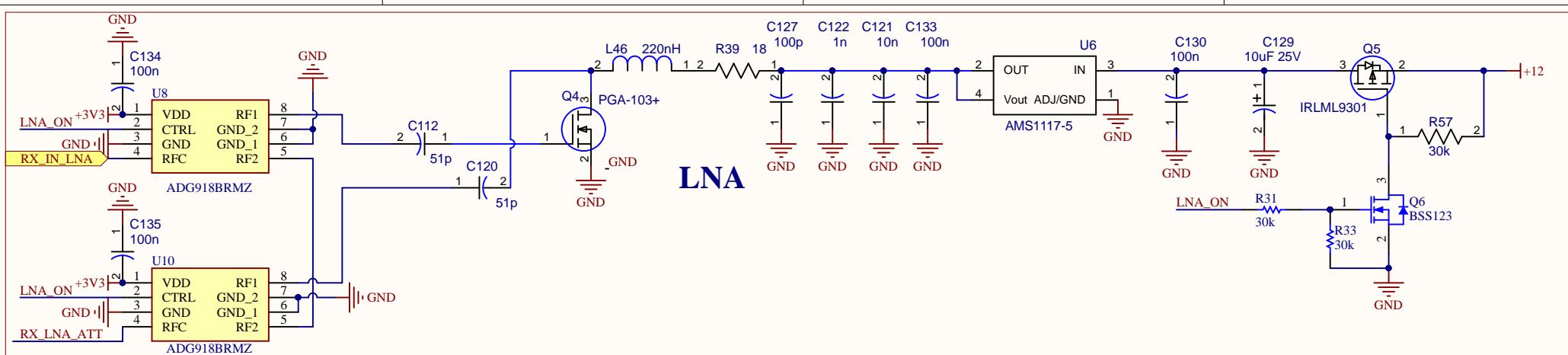
Title **CONTROL**

Size: A4 Number:15 Revision:2.0

Date: 20.11.2020 Time: 0:43:54 Sheet 15 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONTROL.SchDoc

Altium



Title LNA			*	*
Size: A4	Number: 16	Revision: 2.0	*	*
Date: 20.11.2020	Time: 0:43:54	Sheet 16 of 18	*	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\LNA.SchDoc			*	

Altium

