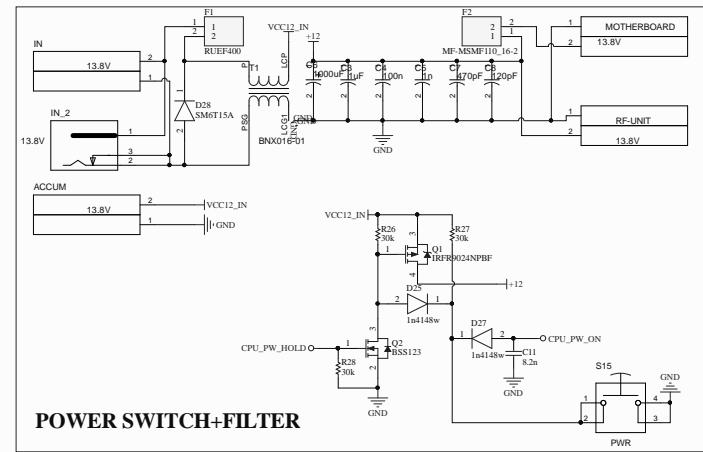
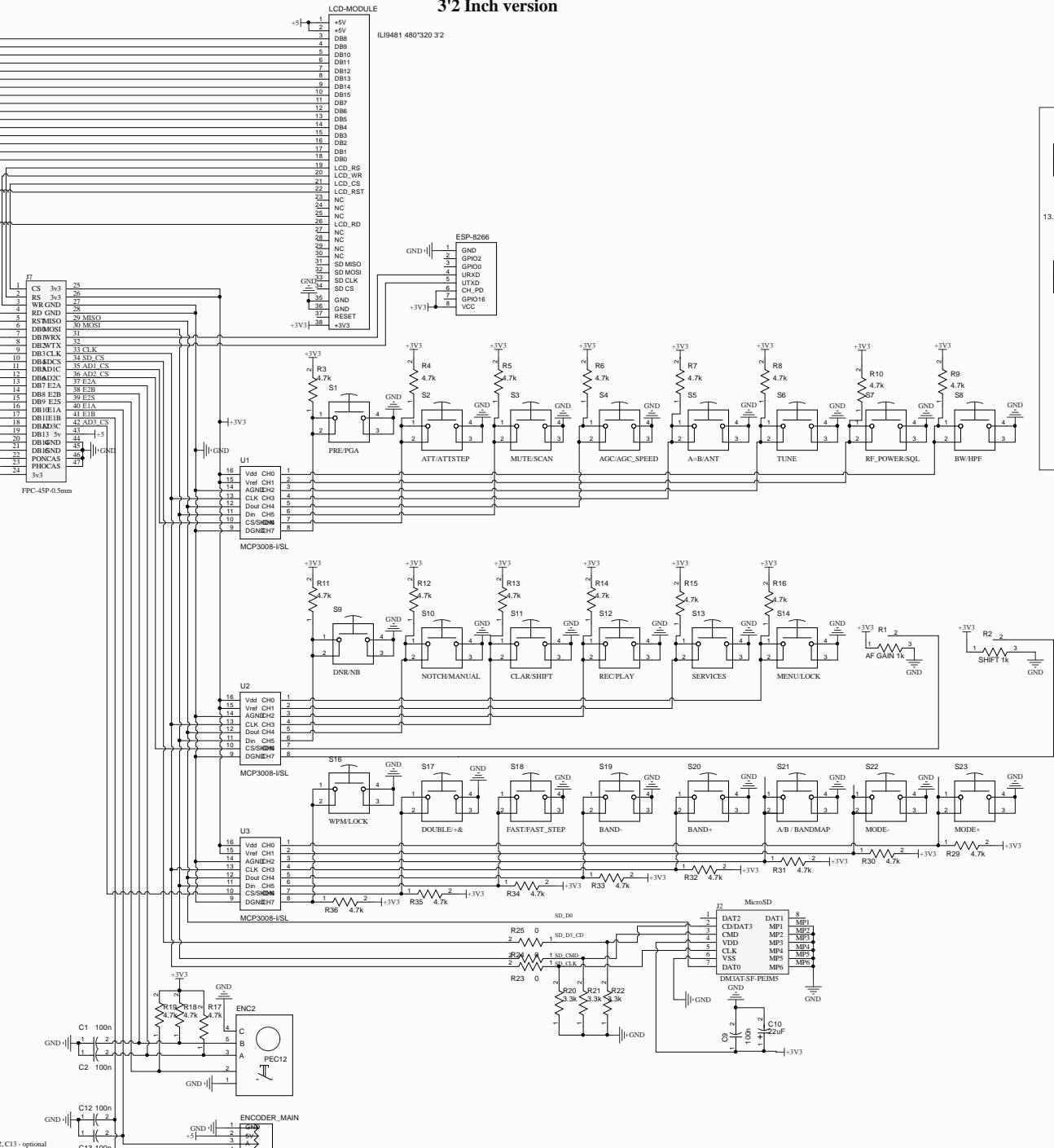
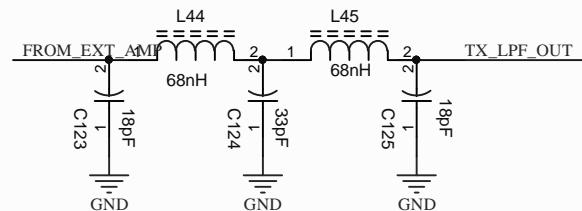


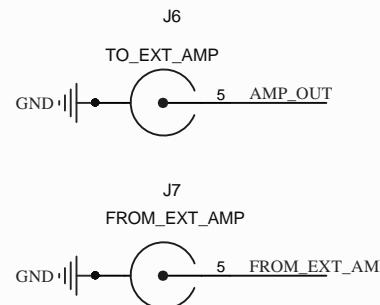
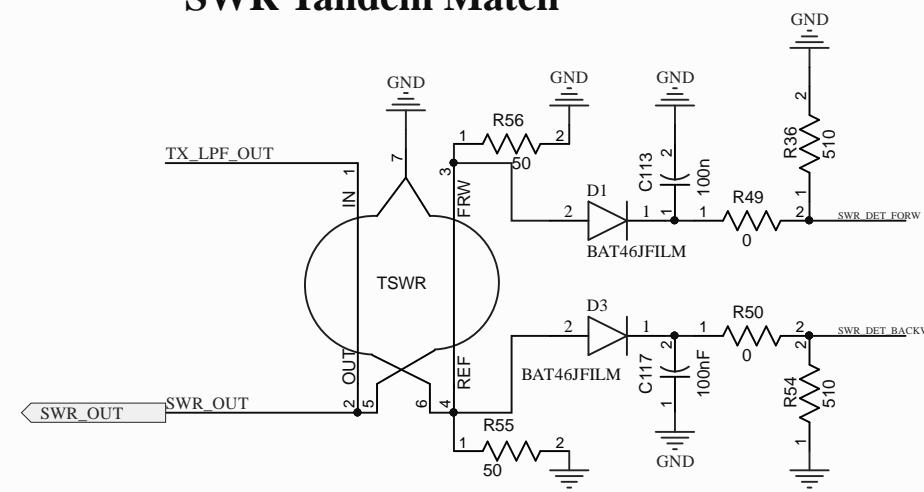
3'2 Inch version

TX_LPF

LPF 170Mhz



Катушки намотаны проводом 0.5мм на оправке диаметром 5мм, 3.5 витка

**SWR Tandem Match**

2 провода 0.9мм пропущено через бинокль

по диагонали пропущено и намотано по 10 витков 0.5мм

Title **SWR Tandem Match**

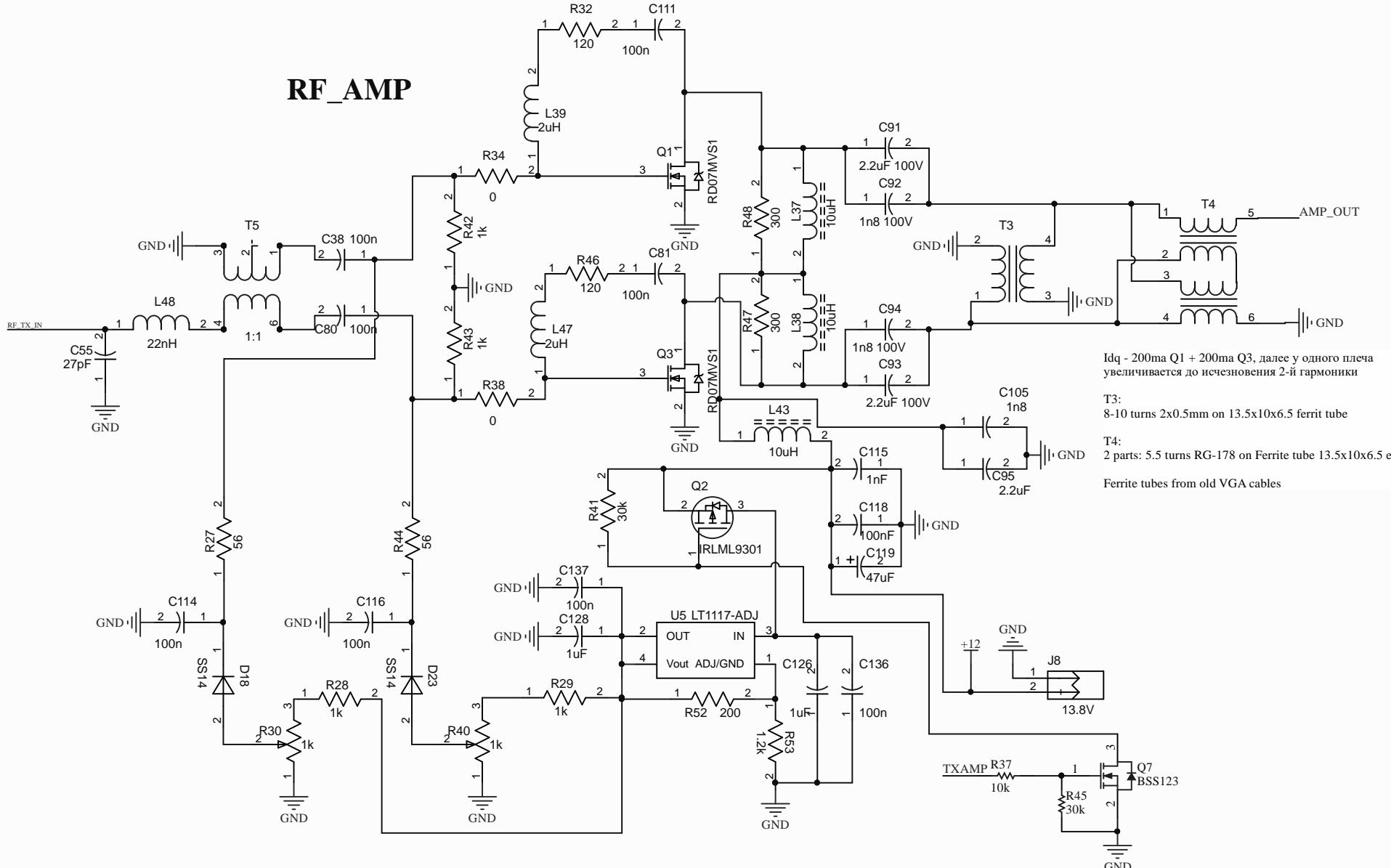
Size: A4 Number:18 Revision:2.0

Date: 22.12.2020 Time: 21:36:12 Sheet:18 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\SWR Tandem Match.SchDoc

Altium

A

RF_AMP

Idq - 200mA Q1 + 200mA Q3, далее у одного плеча
увеличивается до исчезновения 2-й гармоники

T3:
8-10 turns 2x0.5mm on 13.5x10x6.5 ferrite tube

T4:
2 parts: 5.5 turns RG-178 on Ferrite tube 13.5x10x6.5 each
Ferrite tubes from old VGA cables

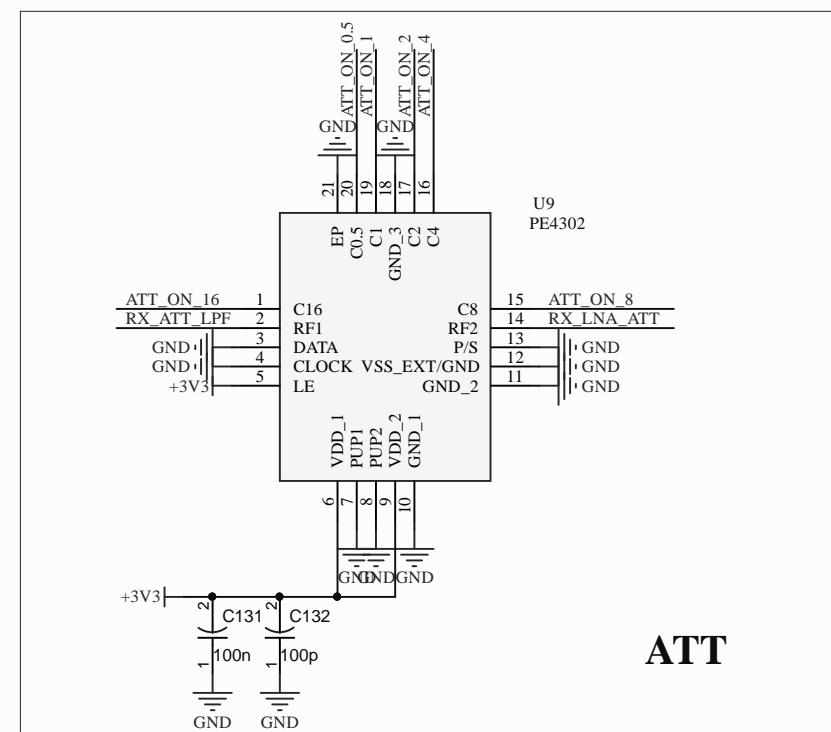
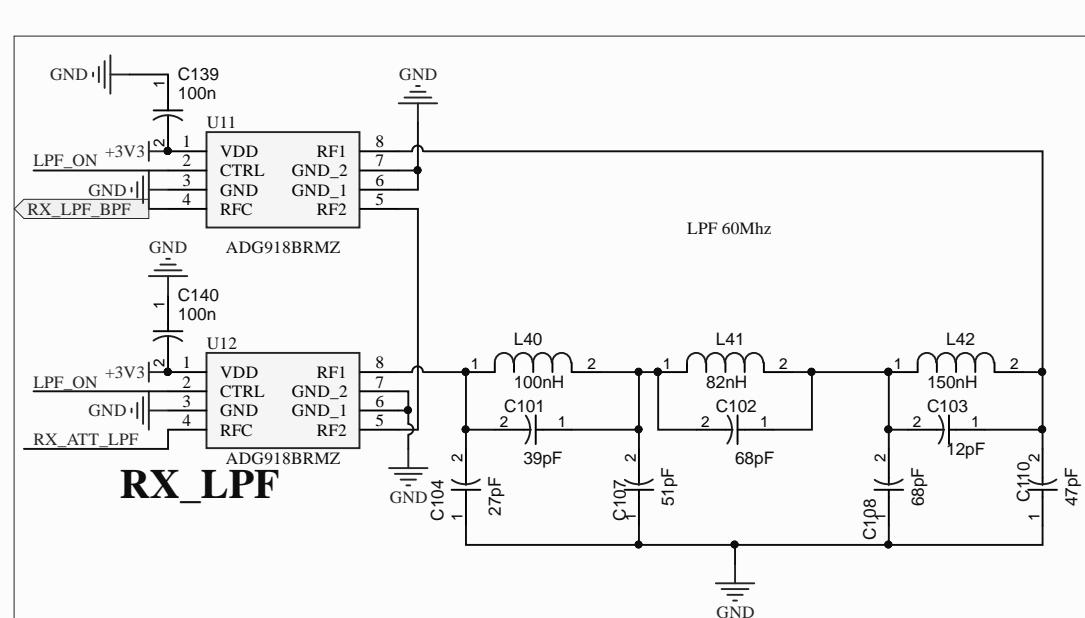
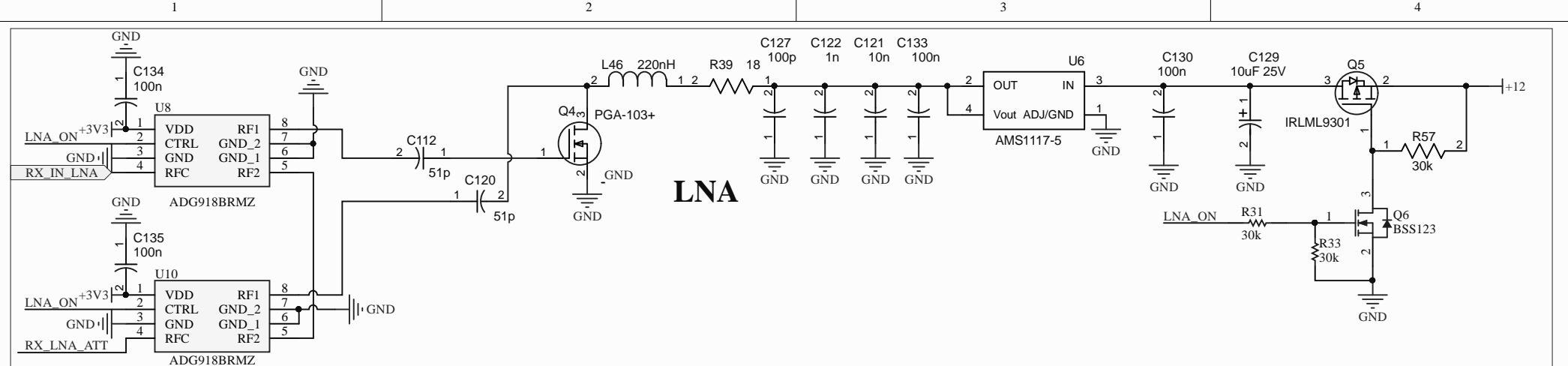
Title **RF_AMP**

Size: A4 Number:17 Revision:2.0

Date: 22.12.2020 Time: 21:36:16 Sheet:17 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\RF_AMP.SchDoc

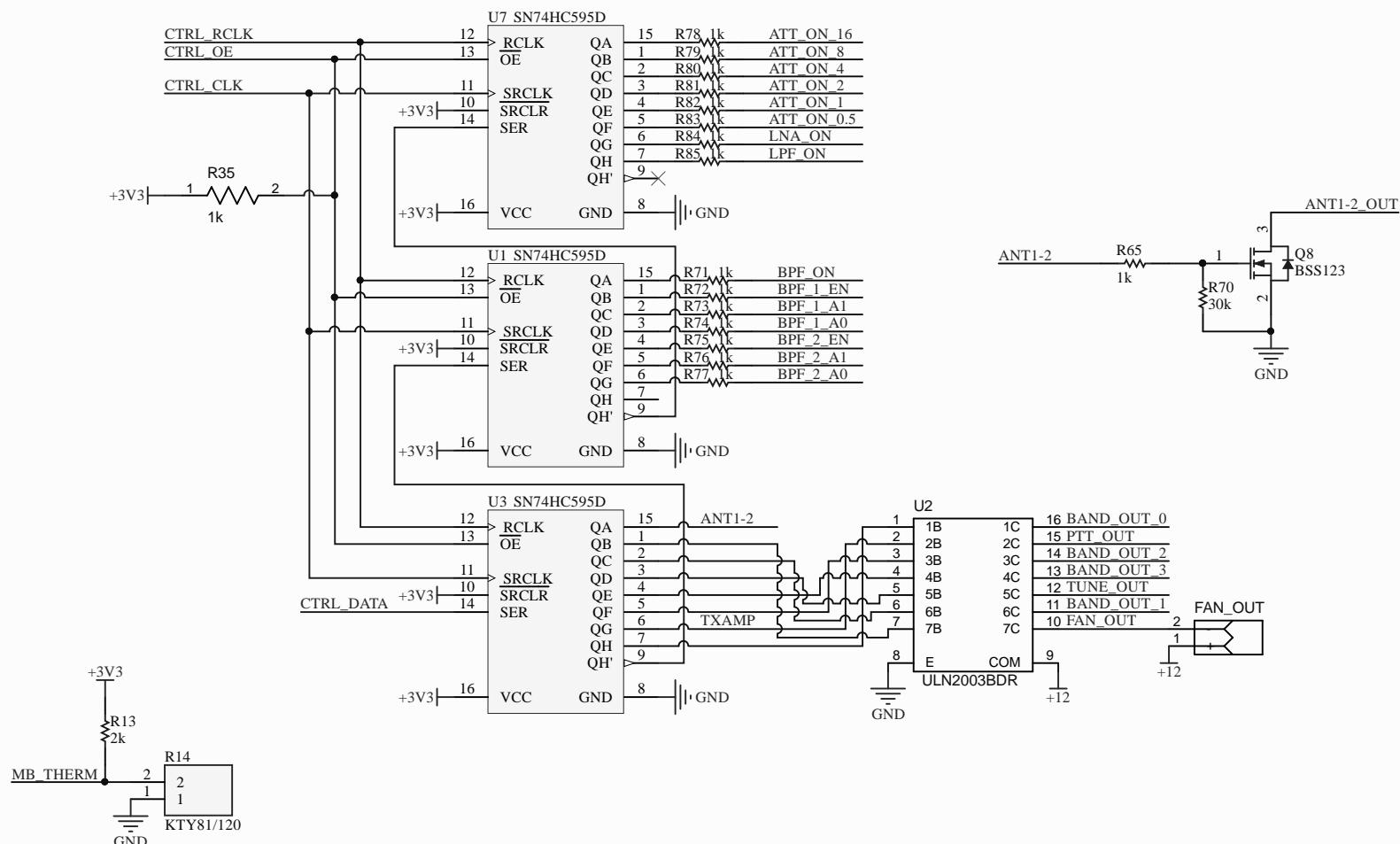
Altium



Title LNA			*
Size: A4	Number: 16	Revision 2.0	*
Date: 22.12.2020	Time: 21:36:21	Sheet 16 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\LNA.SchDoc			*

Altium

A

Title **CONTROL**

Size: A4 Number:15 Revision:2.0

Date: 22.12.2020 Time: 21:36:24 Sheet 15 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONTROL.SchDoc

Altium

A

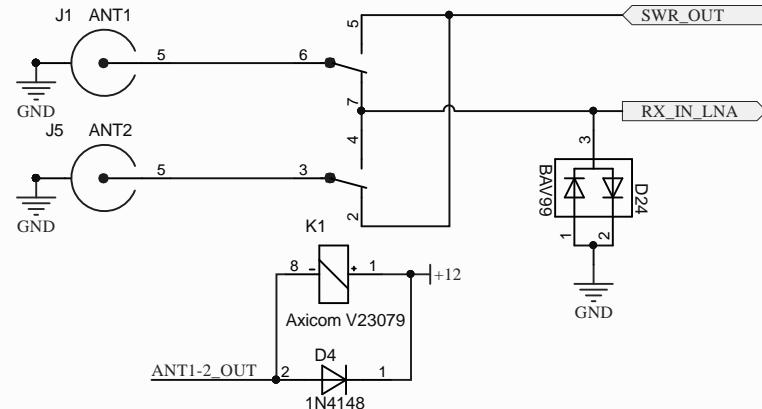
B

C

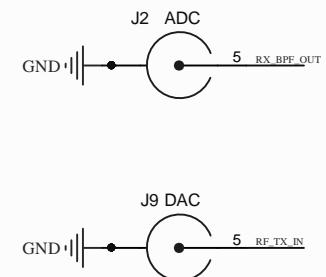
D

A

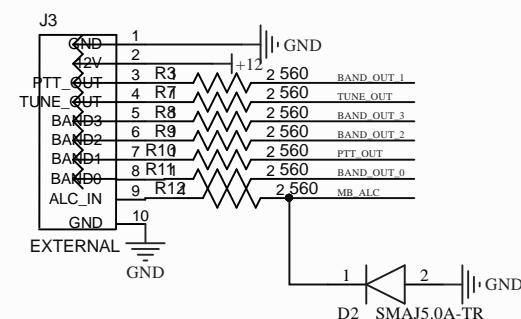
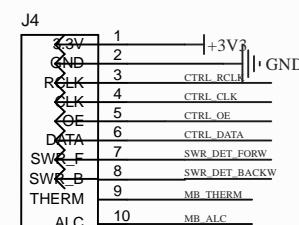
TXRX_COMUTATOR



CONN



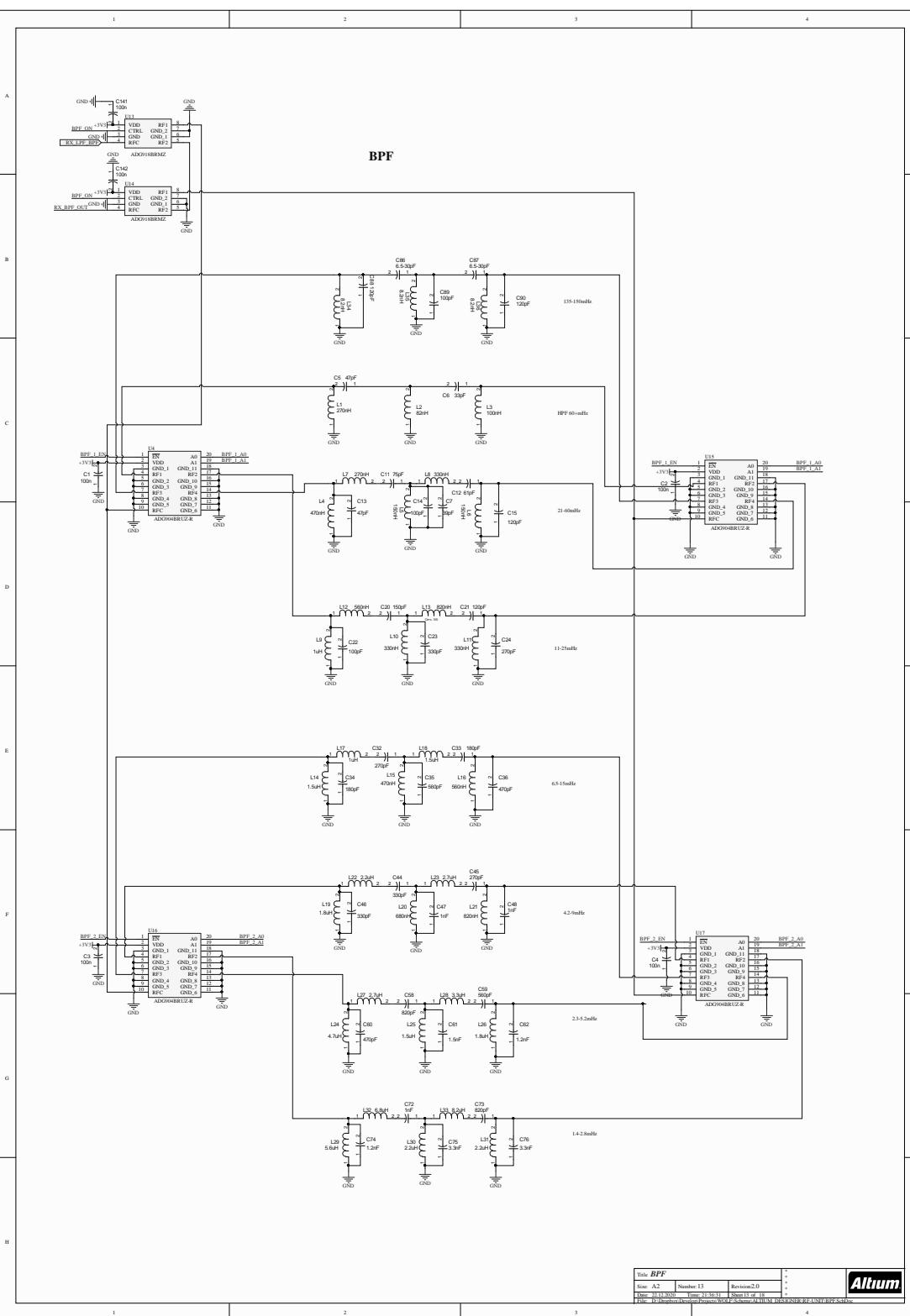
B

Title **CONN**

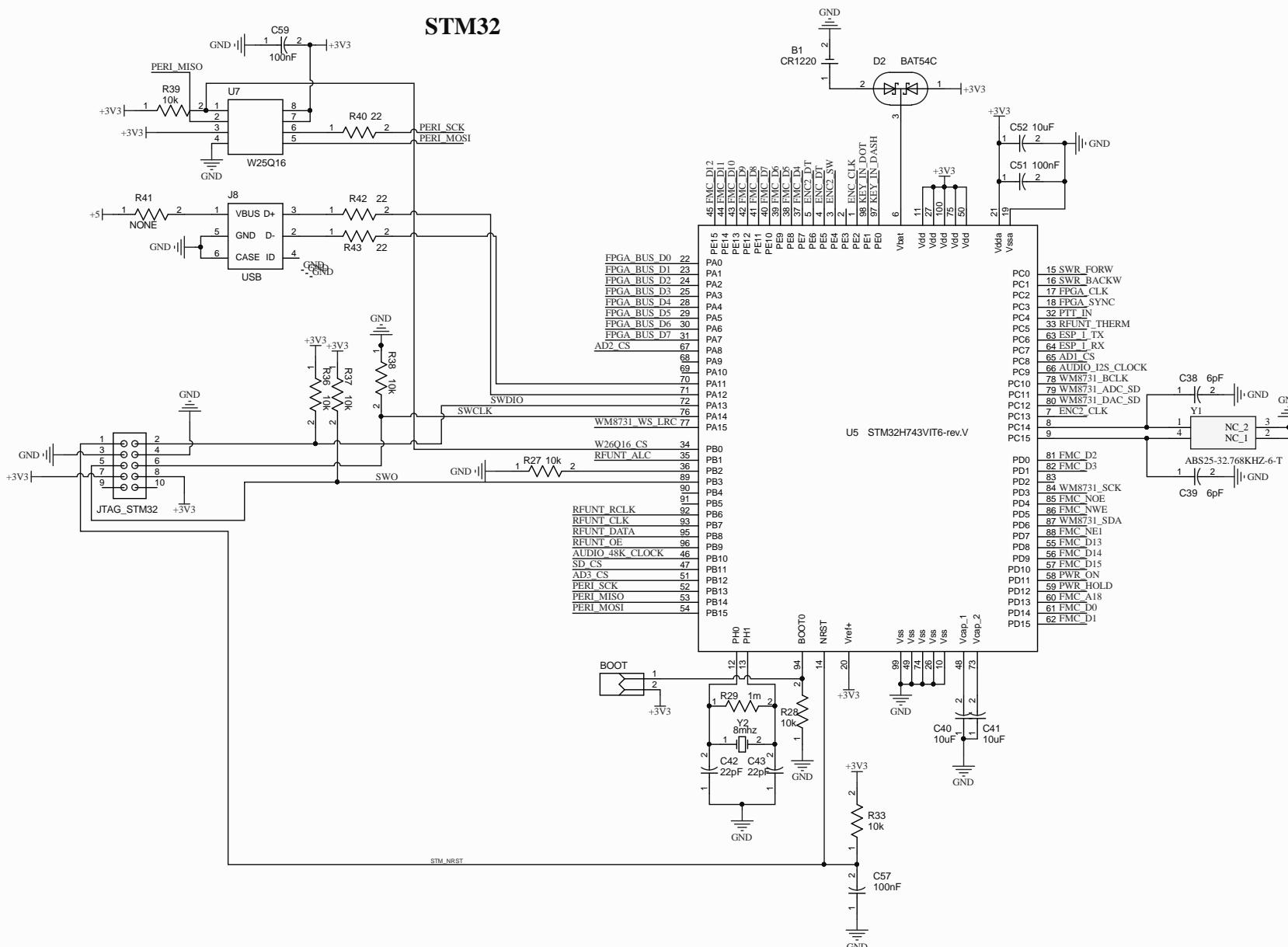
Size: A4 Number:14 Revision2.0

Date: 22.12.2020 Time: 21:36:27 Sheet14 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\RF-UNIT\CONN.SchDoc

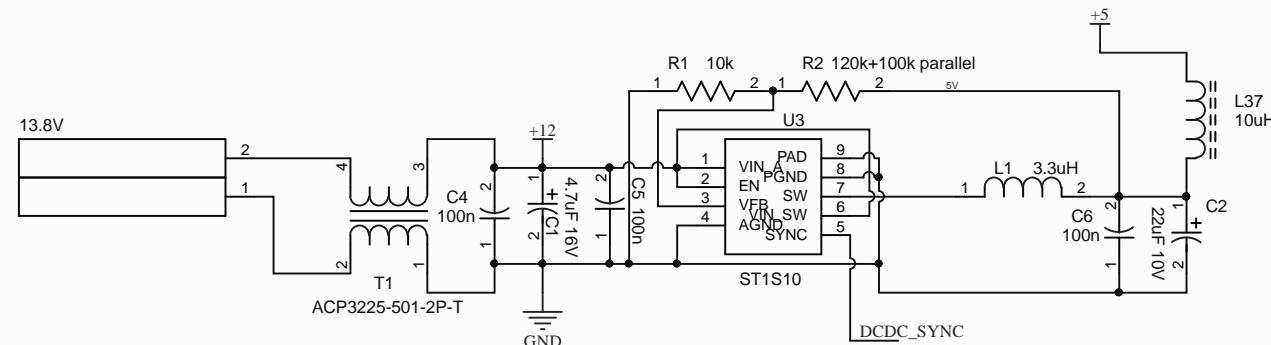


STM32

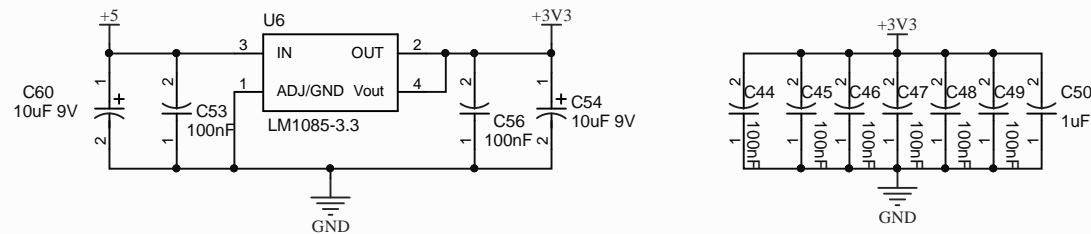
Title **STM32**

Size: A3	Number: 11	Revision: 2.0
Date: 22.12.2020	Time: 21:36:35	Sheet 11 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM DESIGNER\MOTHERBOARD\STM32.SchDoc		

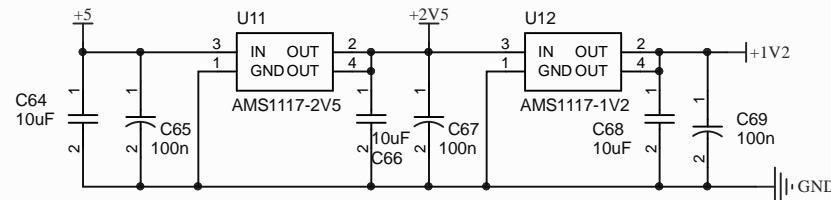
A



B

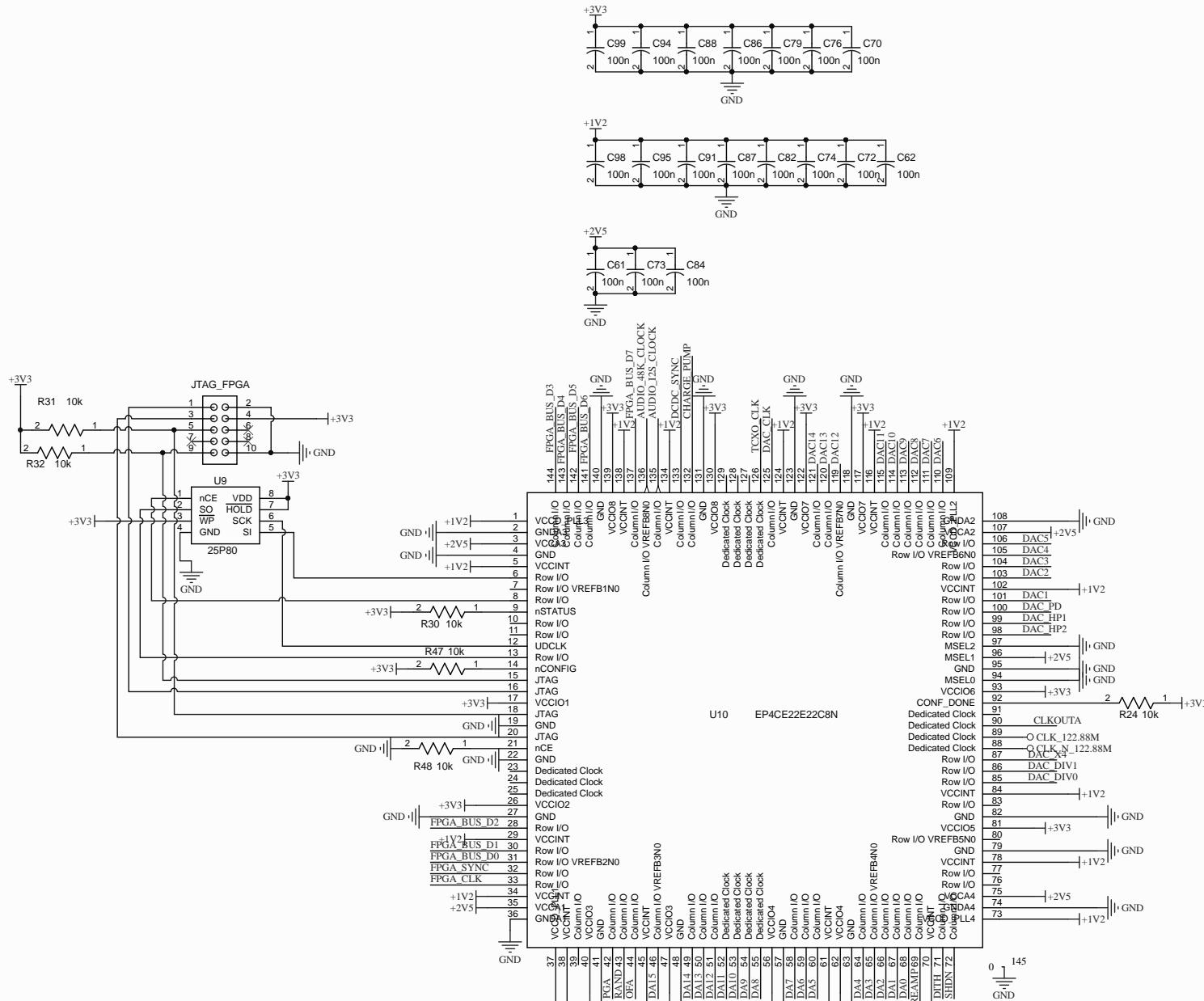


C



D

Title POWER			*
Size: A4	Number:10	Revision2.0	*
Date: 22.12.2020	Time: 21:36:40	Sheet10 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\POWER.SchDoc			

Title **FPGA**

Size: A3

Number: 9

Revision: 2.0

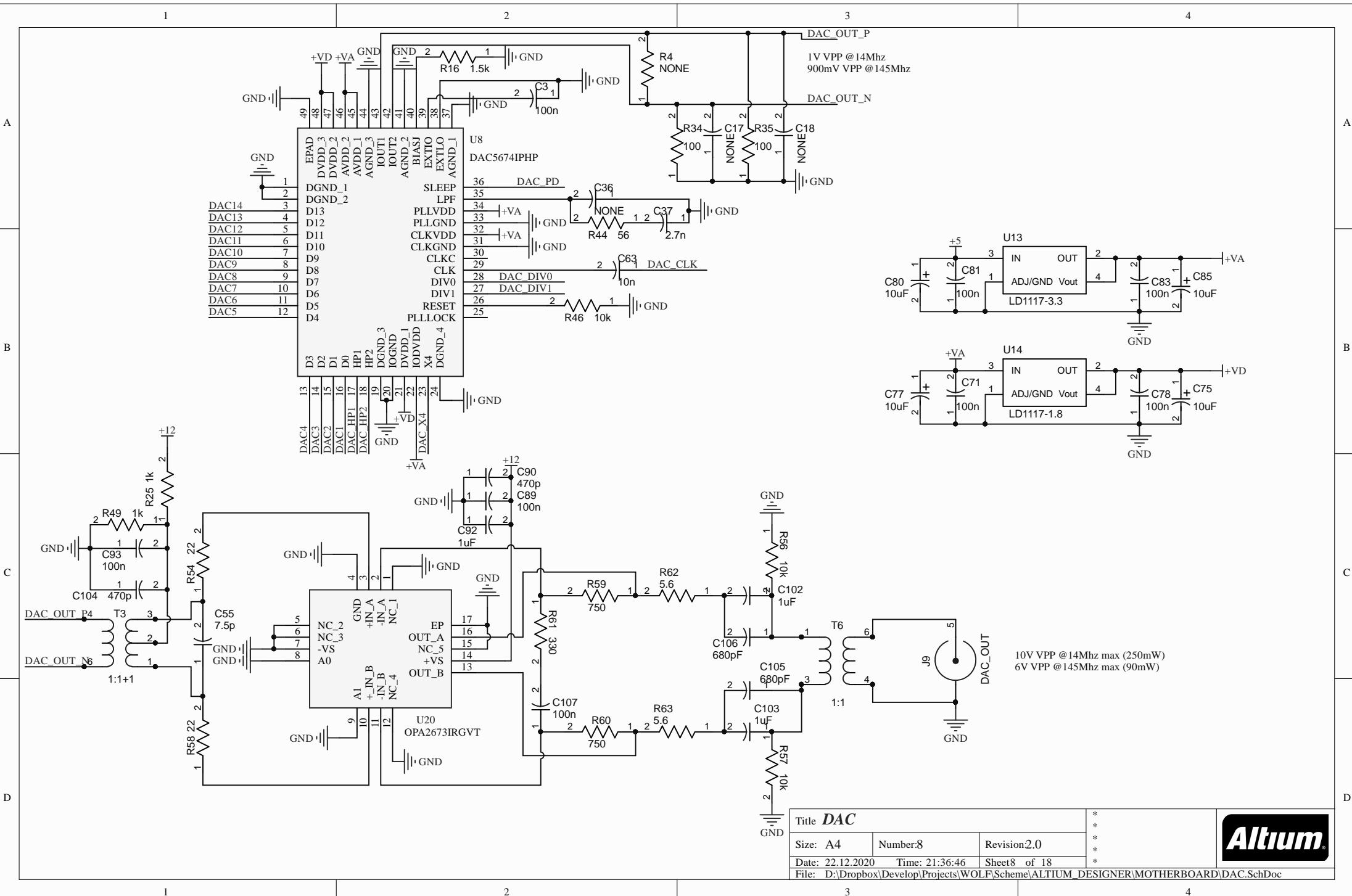
Date: 22.12.2020

Time: 21:36:42

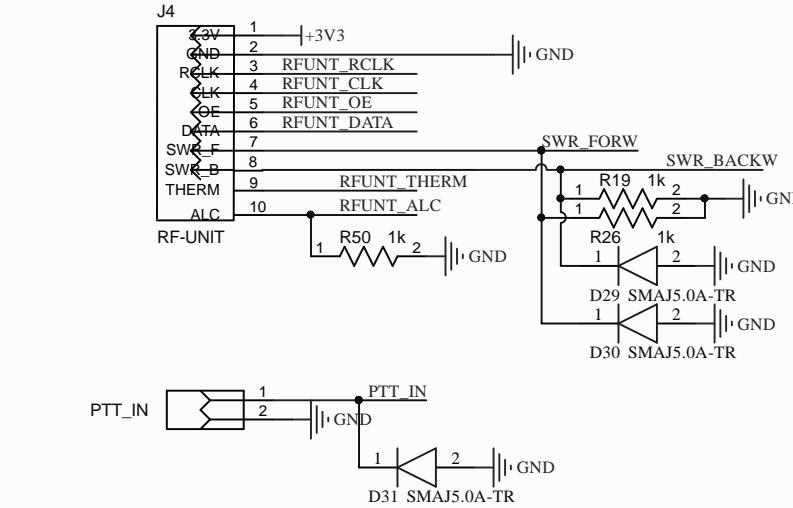
Sheet 9 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM DESIGNER\MOTHERBOARD\FPGA.SchDoc

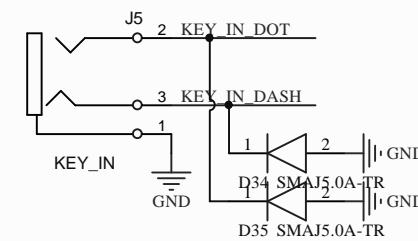




A



B



C

J6

FMC_NE1	1	CS 3v3
FMC_A18	2	RS 3v3
FMC_NWE	3	WR GND
FMC_NOE	4	RD GND
STM_NRST	5	RST'MISO
FMC_D0	6	30 PERI_MOSI
FMC_D1	7	DB0'MOSI
FMC_D2	8	DB1WRX
FMC_D3	9	DB2WTX
FMC_D4	10	DB3CLK
FMC_D5	11	DB\$DCS
FMC_D6	12	DB\$AD1C
FMC_D7	13	DB\$AD2C
FMC_D8	14	DB7 E2A
FMC_D9	15	DB8 E2B
FMC_D10	16	DB9 E2S
FMC_D11	17	DB1IE1A
FMC_D12	18	DB1IE1B
FMC_D13	19	DBAD3C
FMC_D14	20	DB13 5v
FMC_D15	21	DB1GND
PWR_ON	22	DB1GND
PWR_HOLD	23	PONCAS
	24	PHOCAS
		3v3

FPC-45P-0.5mm

D

Title CONNECTIONS

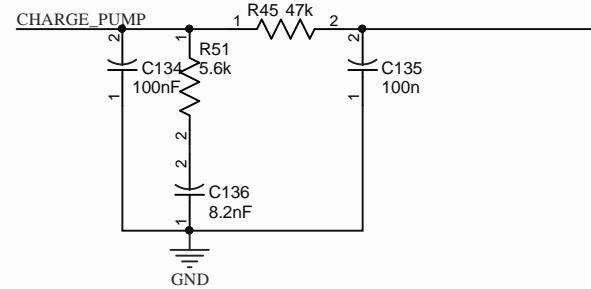
Size: A4 | Number:7 | Revision2.0

Date: 22.12.2020 | Time: 21:36:49 | Sheet7 of 18

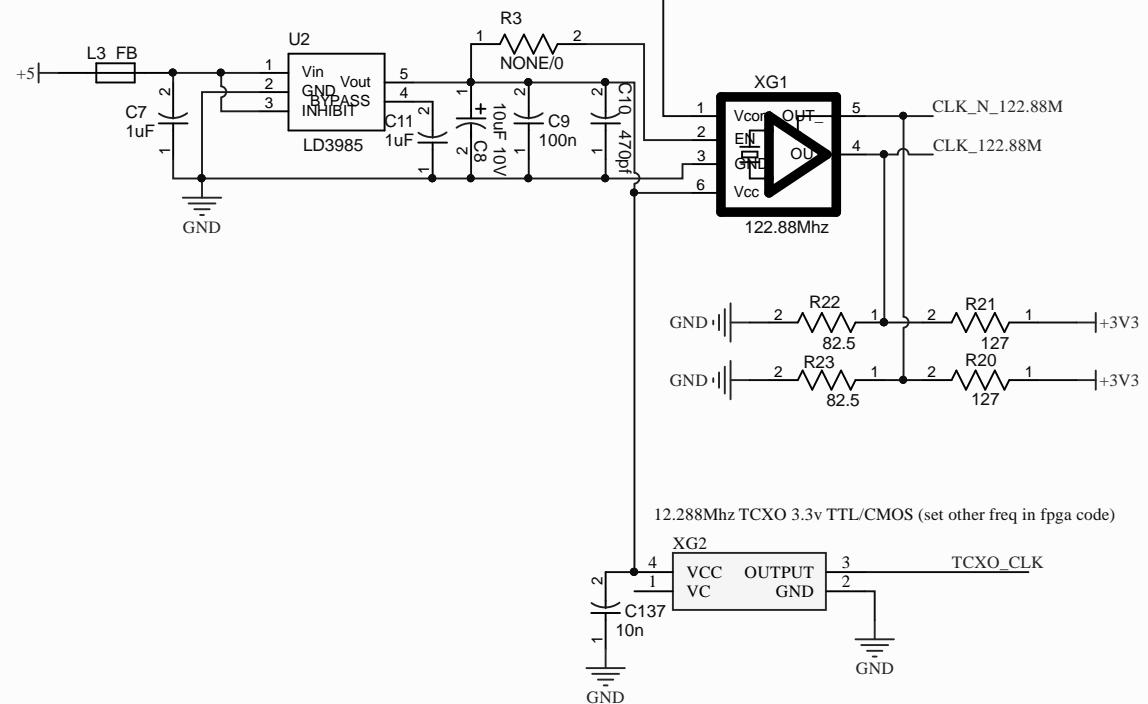
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\CONNECTIONS.SchDoc

Altium

A



B



C

D

Title **CLOCK_GENERATOR**

Size: A4 Number:6 Revision 2.0

Date: 22.12.2020 Time: 21:36:53 Sheet 6 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\MOTHERBOARD\CLOCK_GENERATOR.SchDoc

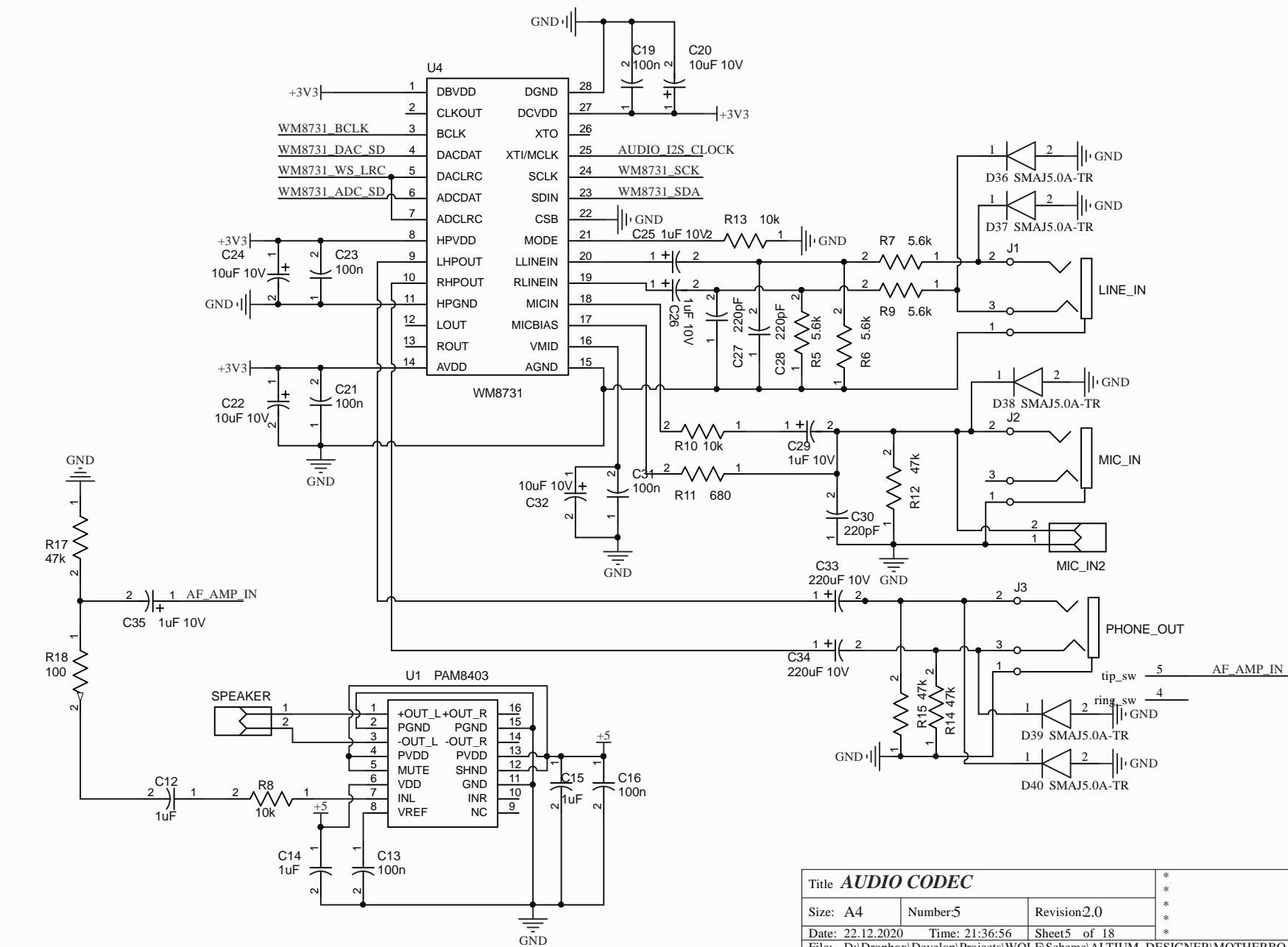
Altium

A

B

C

D

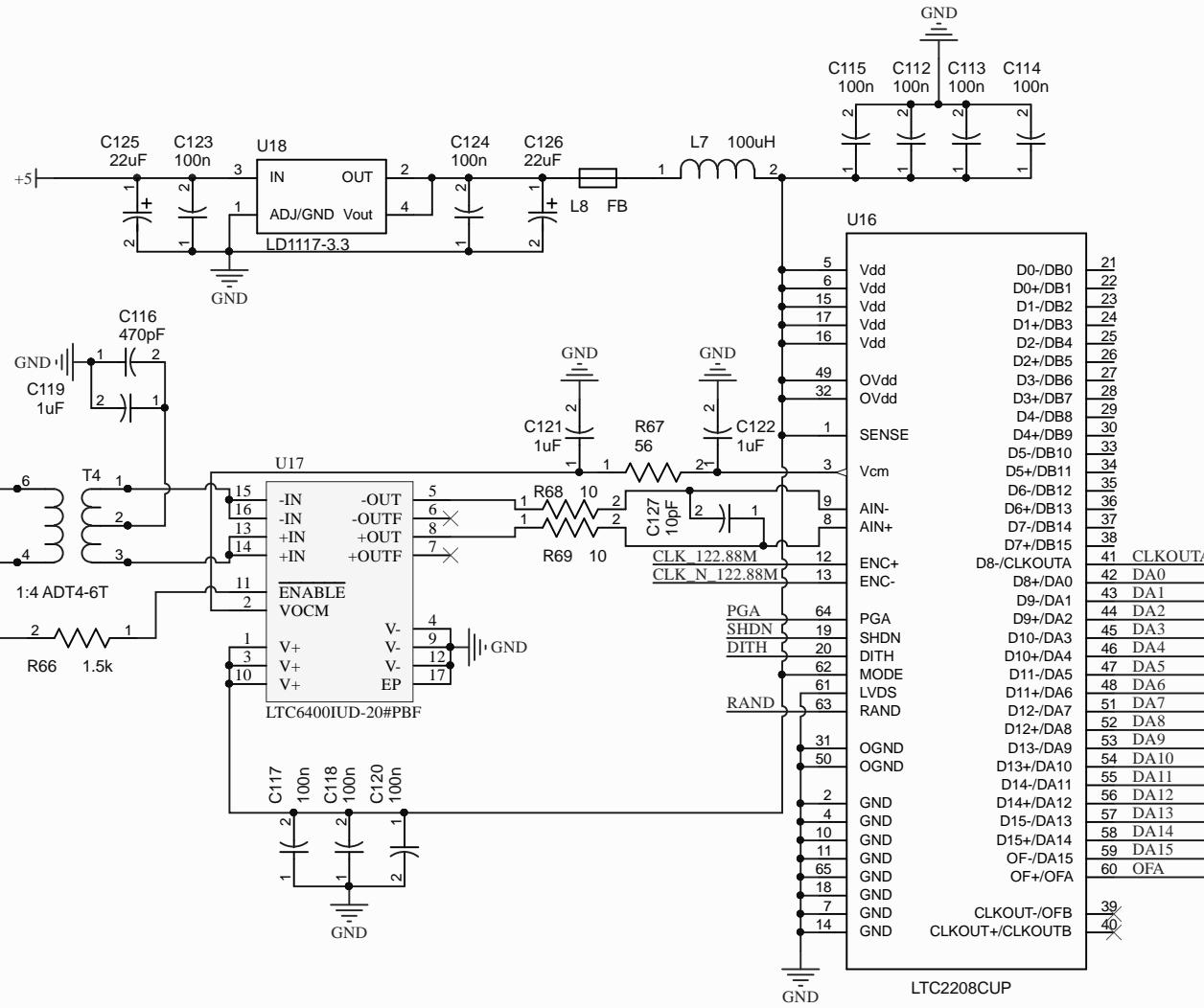
Title **AUDIO CODEC**

Size: A4 Number:5 Revision2.0

Date: 22.12.2020 Time: 21:36:56 Sheet5 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\MOTHERBOARD\AUDIO_CODEC.SchDoc

Altium

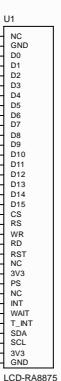
Title **ADC**

Size: A4 Number:3 Revision 2.0

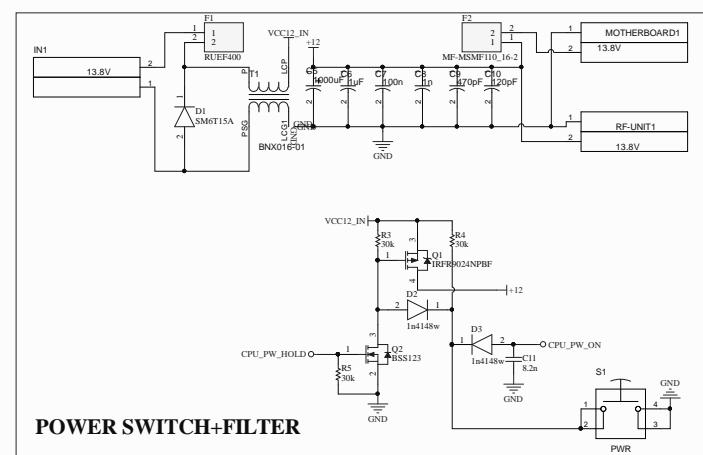
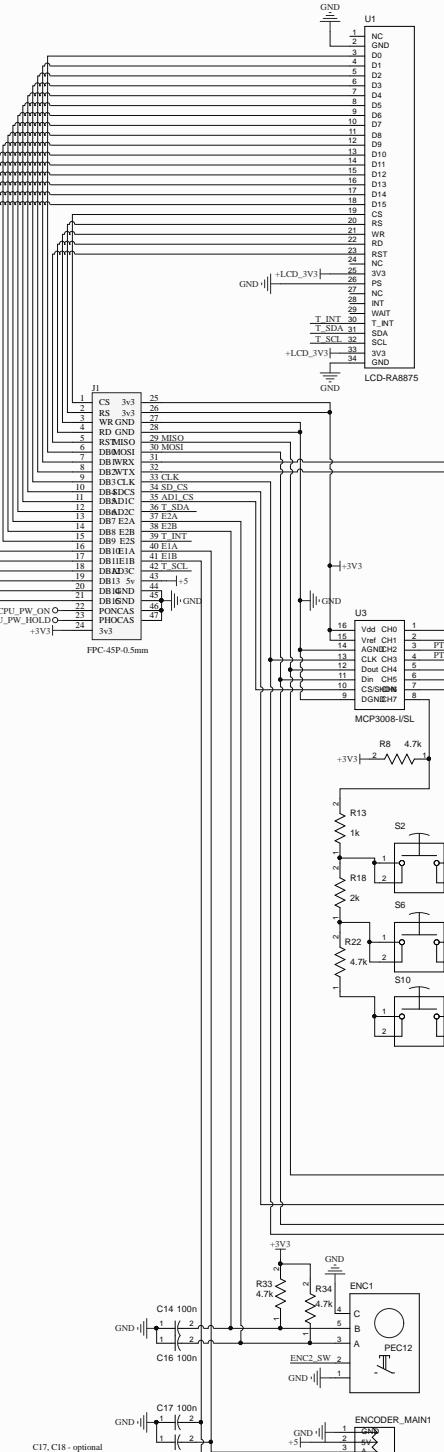
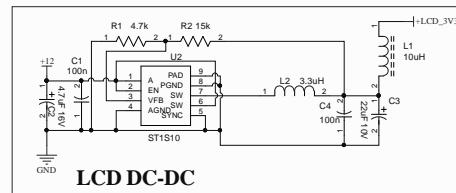
Date: 22.12.2020 Time: 21:37:00 Sheet 3 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\ADC.SchDoc

Altium



7Inch version



POWER SWITCH+FILTER

