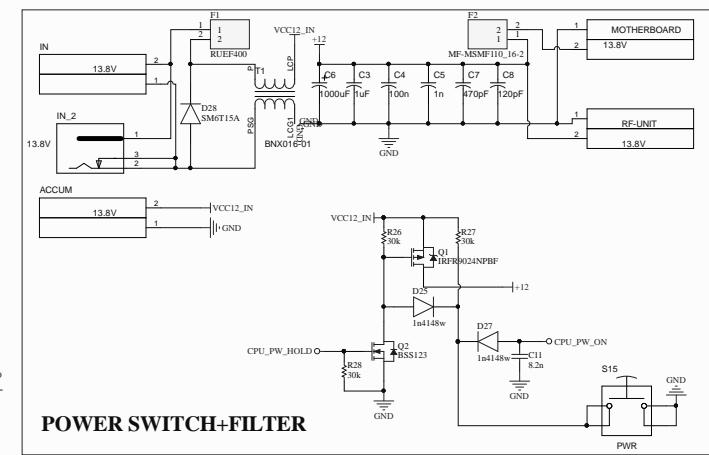
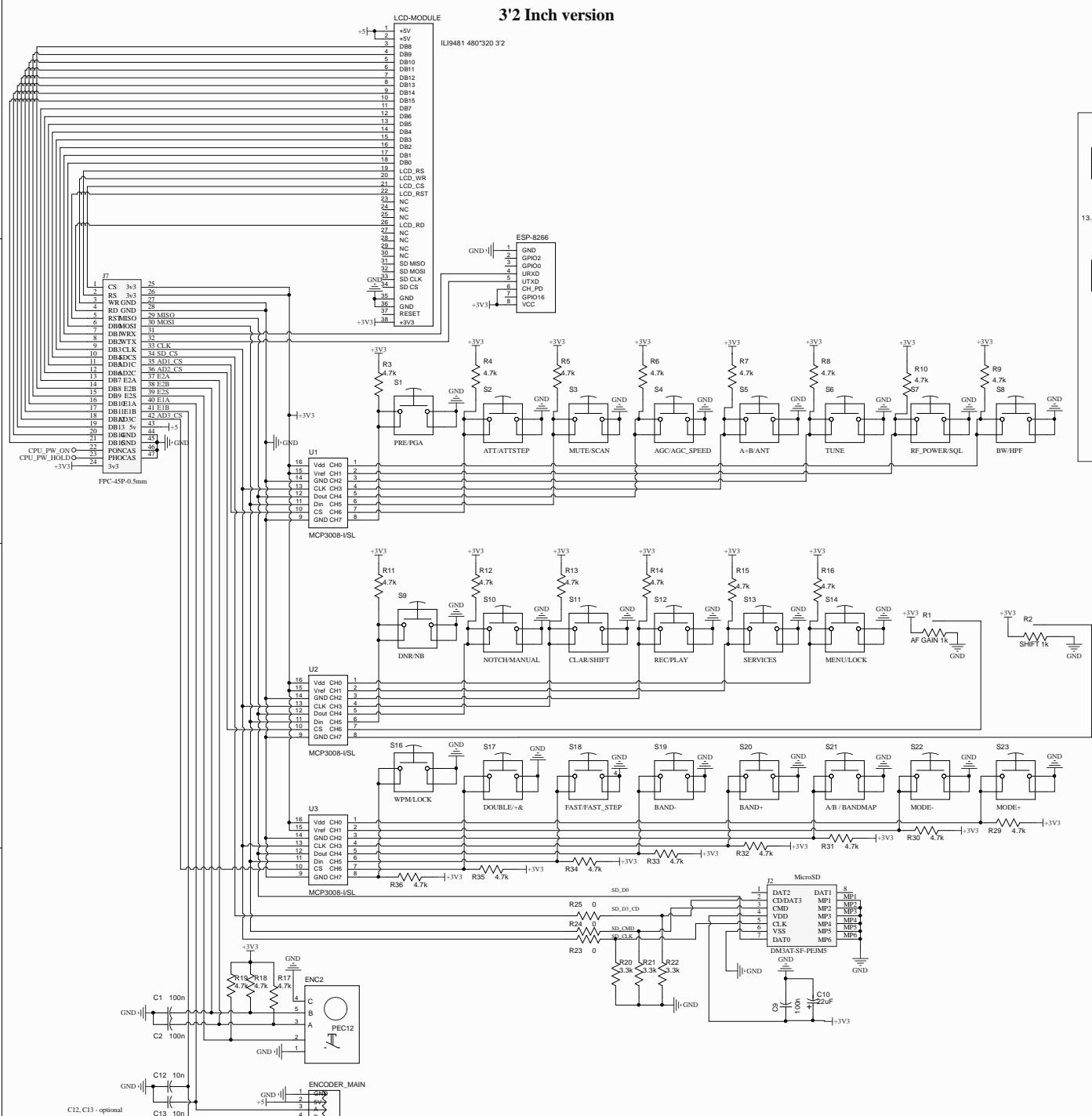
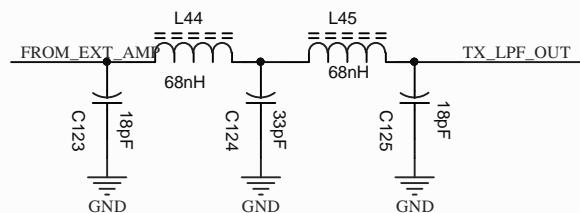


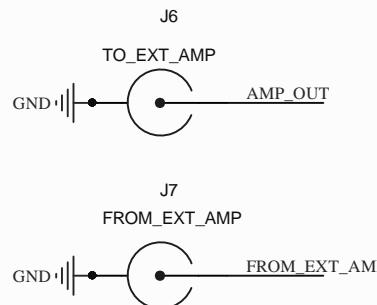
3'2 Inch version

TX_LPF

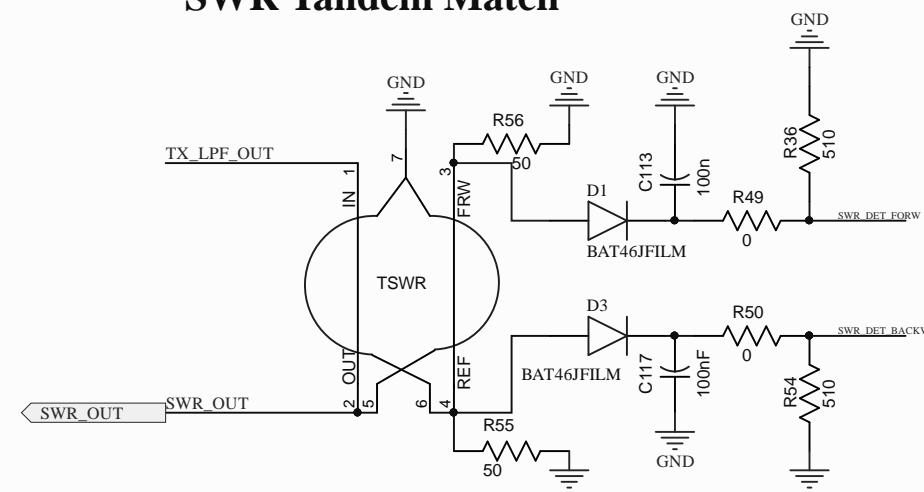
LPF 170Mhz



Катушки намотаны проводом 0.5мм на оправке диаметром 5мм, 3.5 витка



SWR Tandem Match



2 провода 0.9мм пропущено через бинокль

по диагонали пропущено и намотано по 10 витков 0.5мм

Title **SWR Tandem Match**

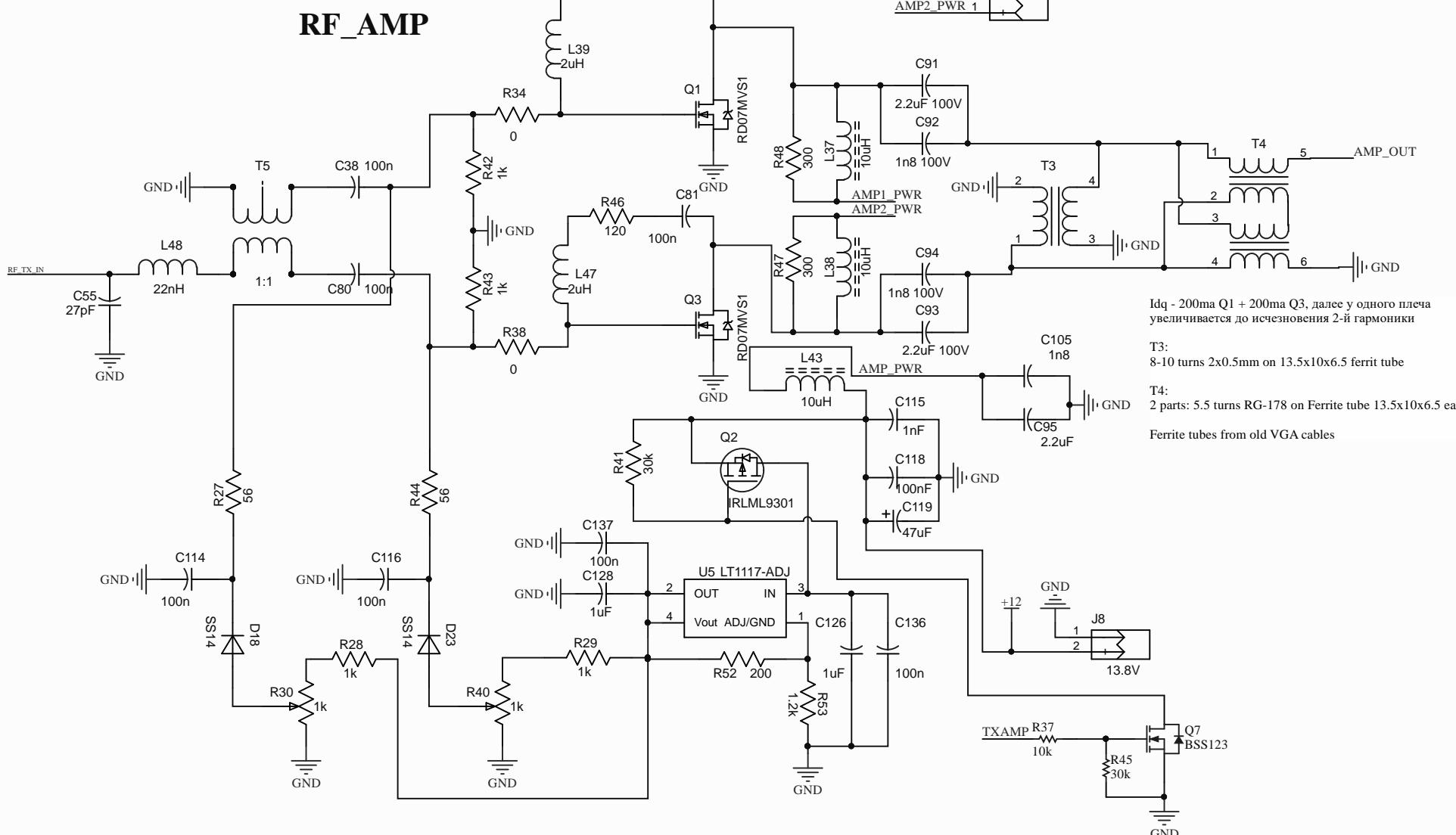
Size: A4	Number:18	Revision2.0
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Date: 15.04.2021	Time: 22:59:46	Sheet18 of 18
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File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\RF-UNIT\SWR Tandem Match.SchDoc	*
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Altium

A

RF_AMP

*замкнуть после измерения тока каскадов

Idq - 200mA Q1 + 200mA Q3, далее у одного плеча увеличивается до исчезновения 2-й гармоники

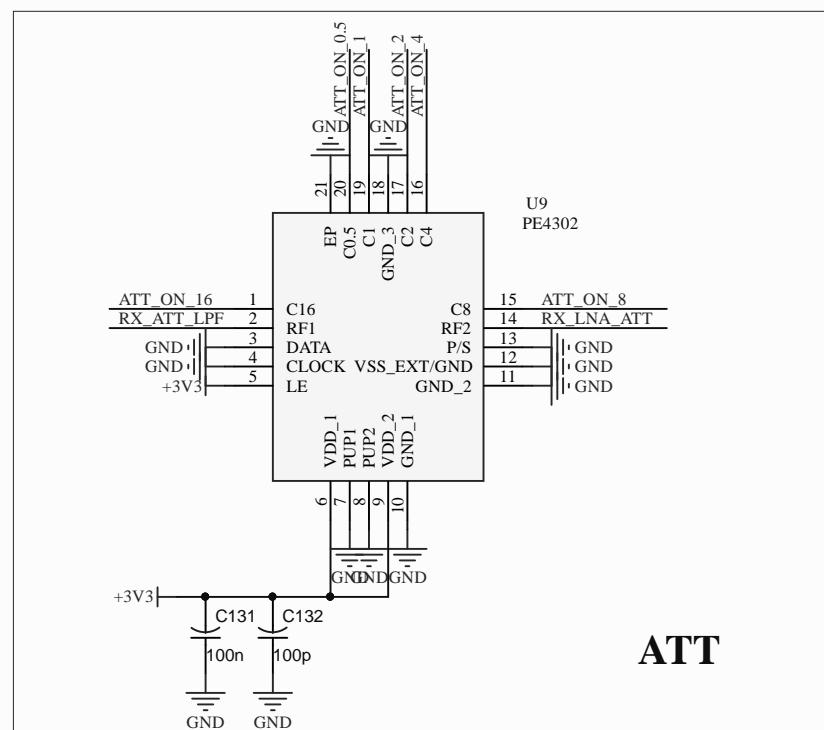
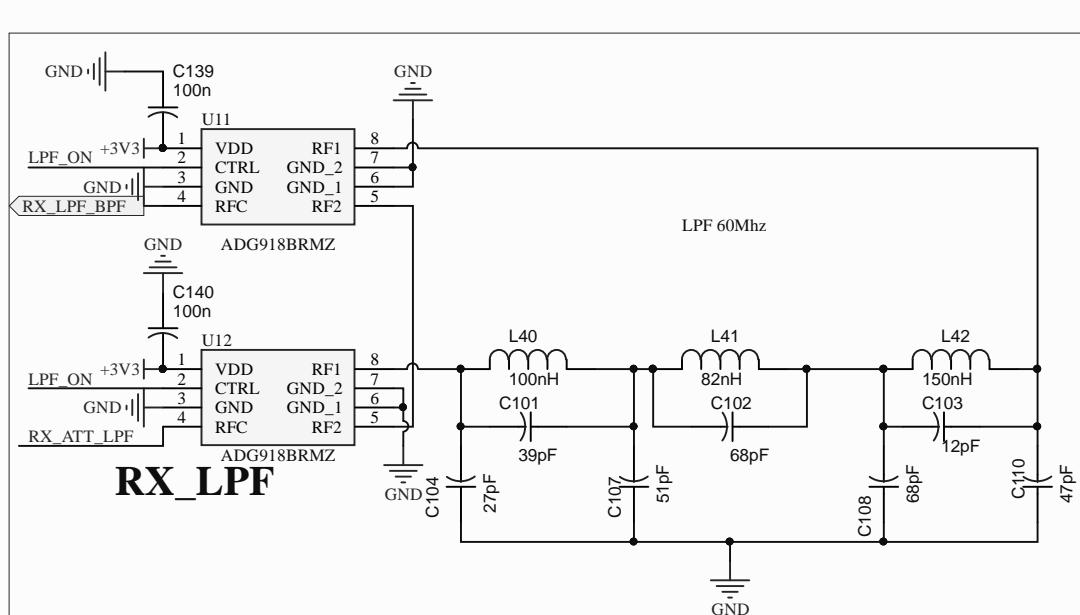
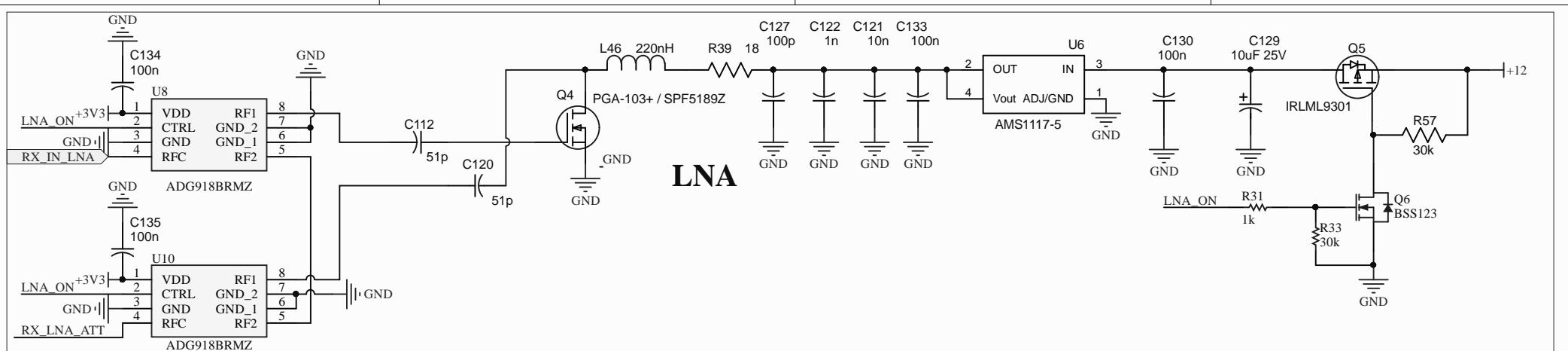
T3:
8-10 turns 2x0.5mm on 13.5x10x6.5 ferrite tubeT4:
2 parts: 5.5 turns RG-178 on Ferrite tube 13.5x10x6.5 each
Ferrite tubes from old VGA cablesTitle **RF_AMP**

Size: A4 Number:17 Revision:2.0

Date: 15.04.2021 Time: 22:59:48 Sheet:17 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\RF_AMP.SchDoc

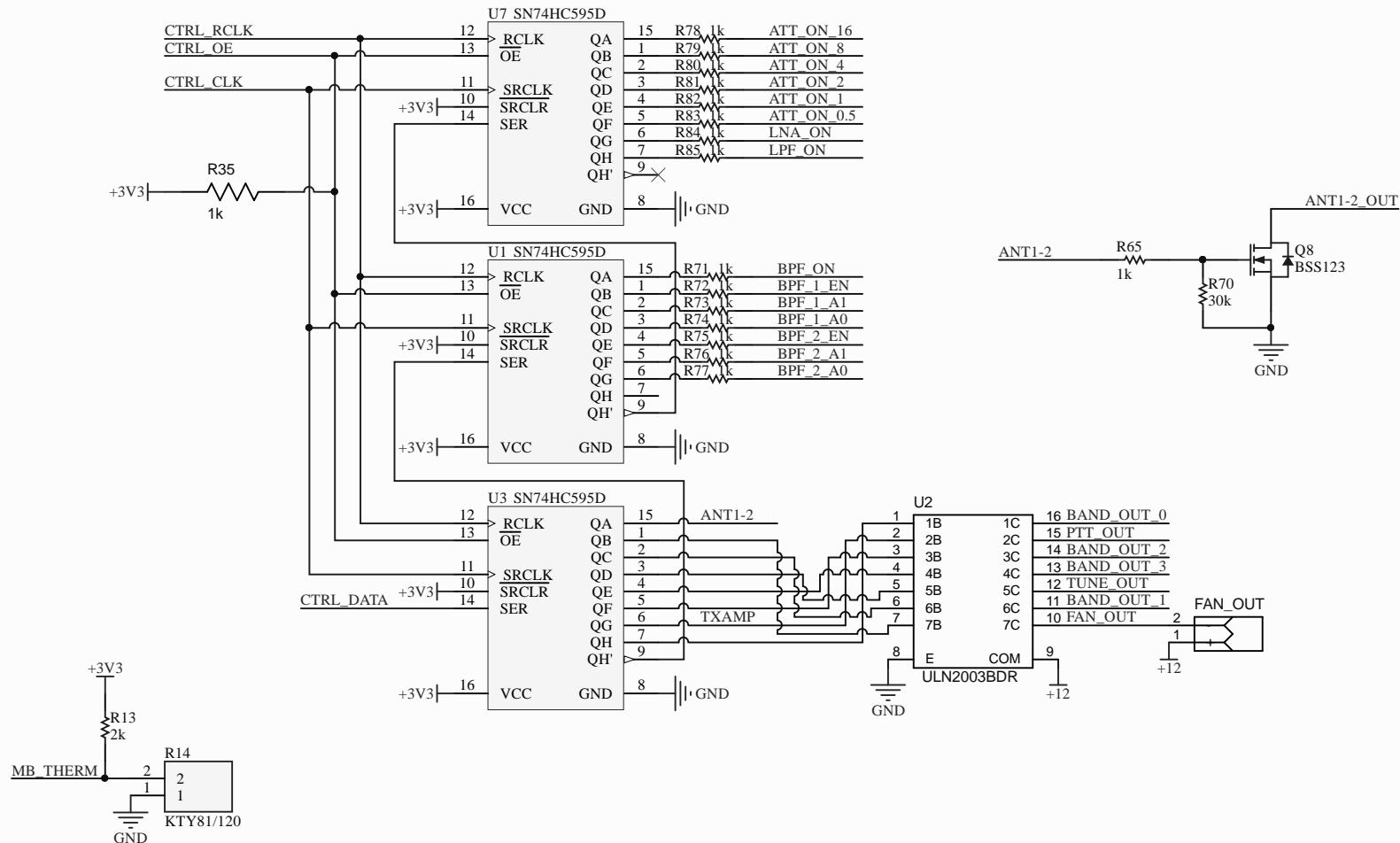
Altium



Title LNA			*
Size: A4	Number: 16	Revision: 2.0	*
Date: 15.04.2021	Time: 22:59:49	Sheet 16 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\LNA.SchDoc			

Altium

A

Title **CONTROL**

Size: A4 Number:15 Revision:2.0

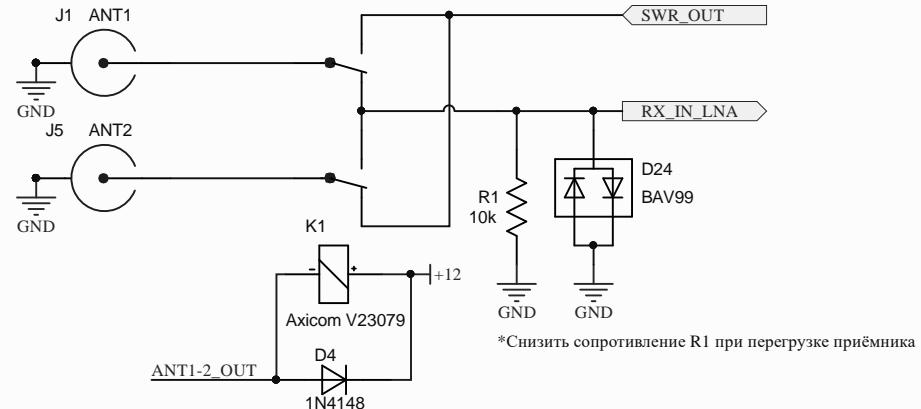
Date: 15.04.2021 Time: 22:59:51 Sheet 15 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONTROL.SchDoc

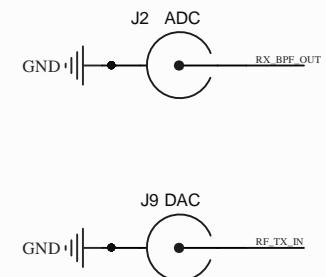
Altium

A

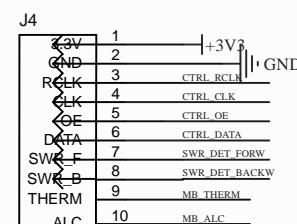
TXRX_COMUTATOR



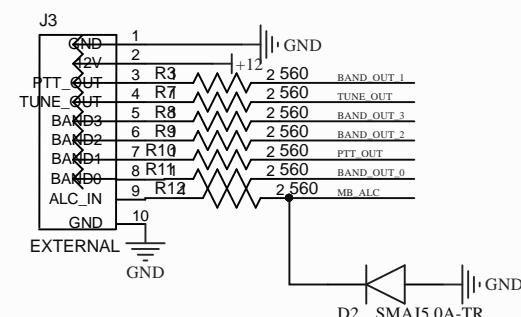
CONN



B



MOTHERBOARD



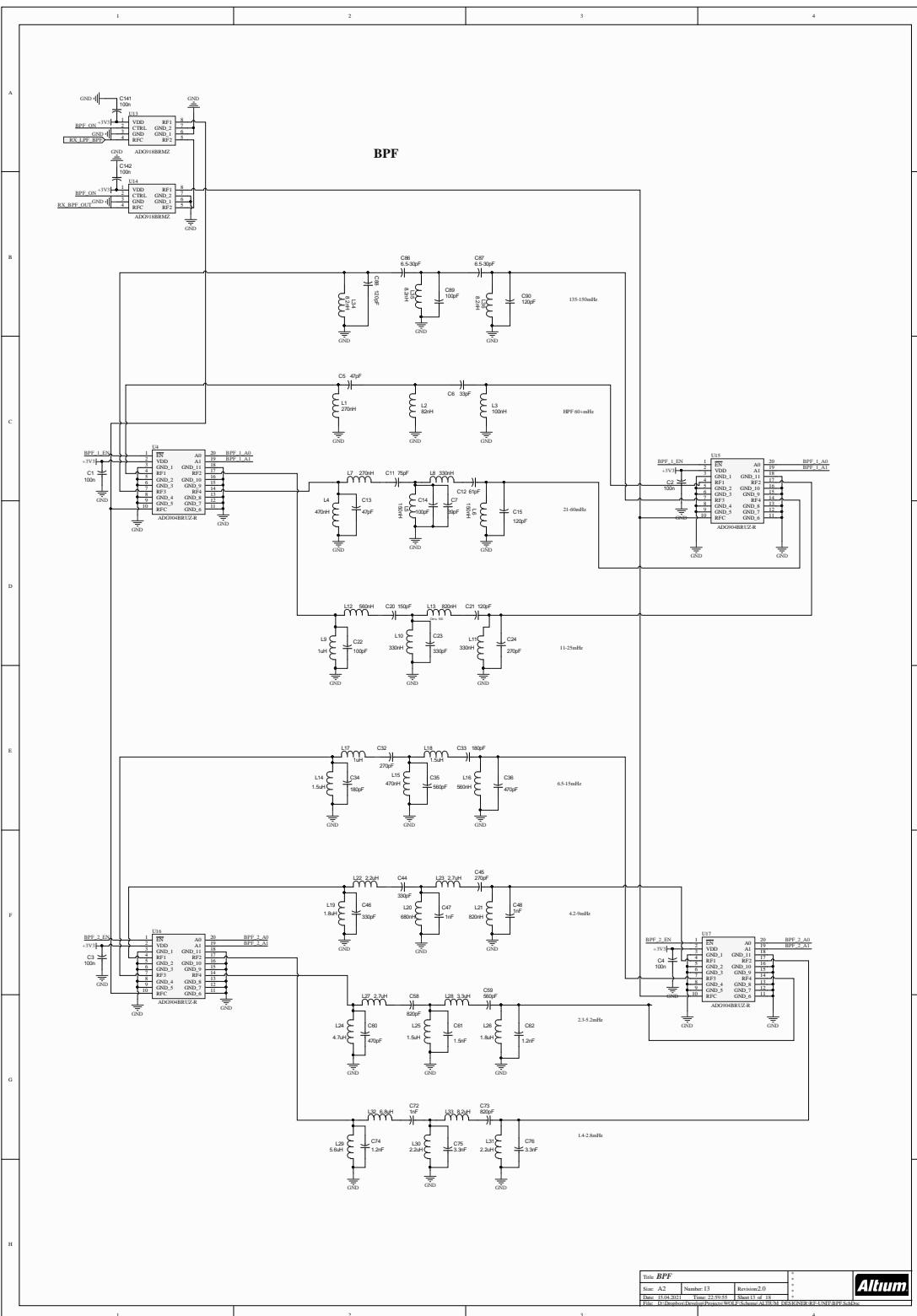
C

Title **CONN**

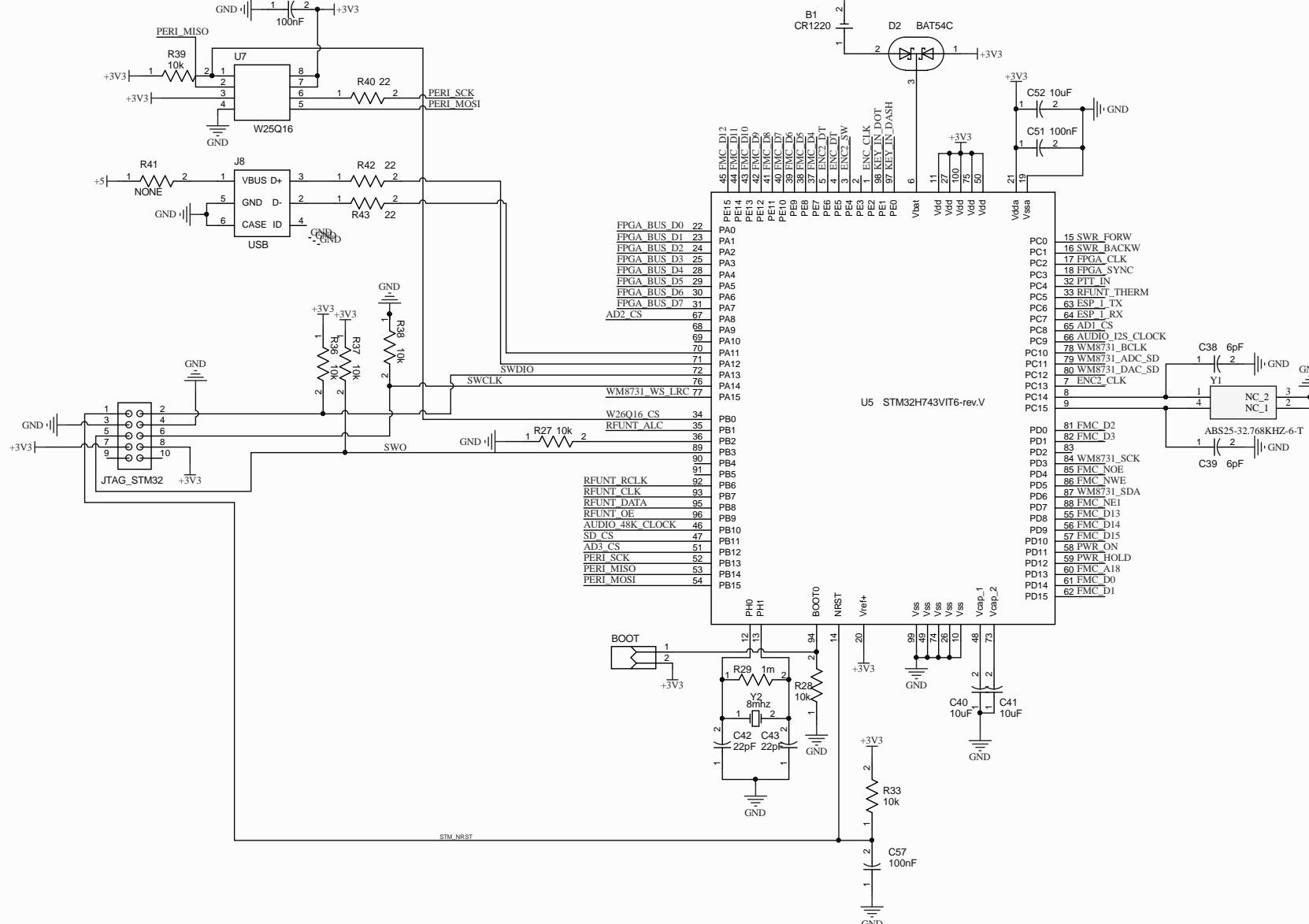
Size: A4 Number:14 Revision2.0

Date: 15.04.2021 Time: 22:59:53 Sheet14 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONN.SchDoc



STM32

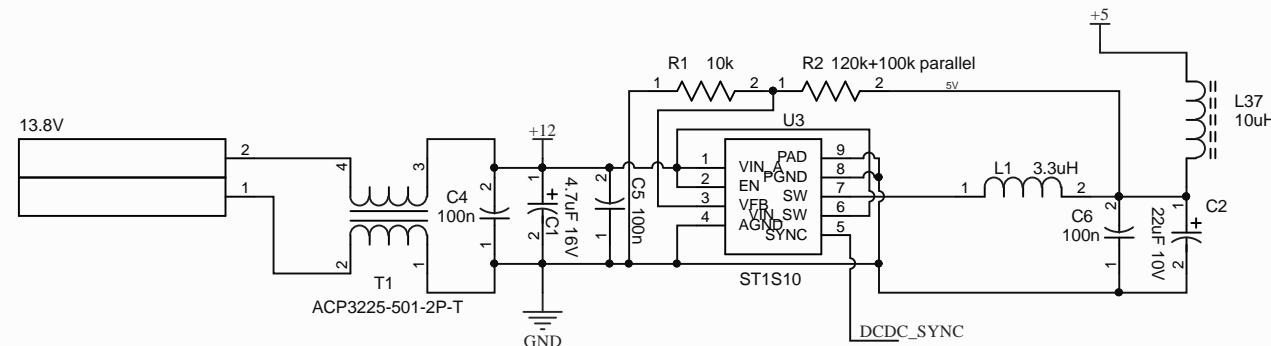


Title STM32

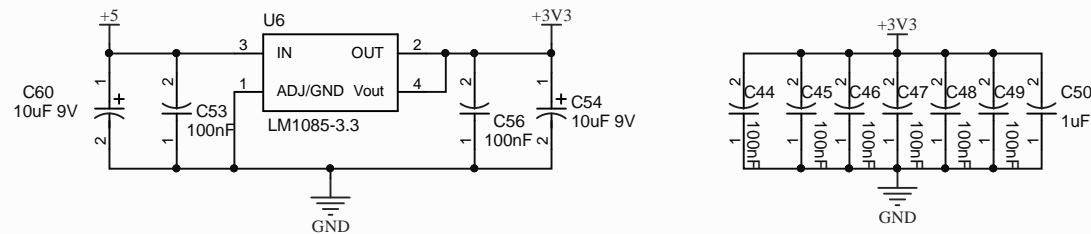
Size: A3 Number: 11 Revision 2.0
Date: 15.04.2021 Time: 22:59:57 Sheet 11 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM DES

Altium

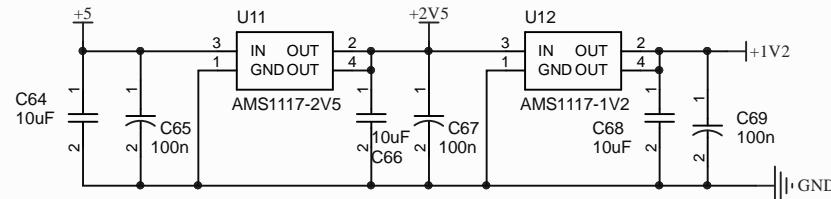
A



B

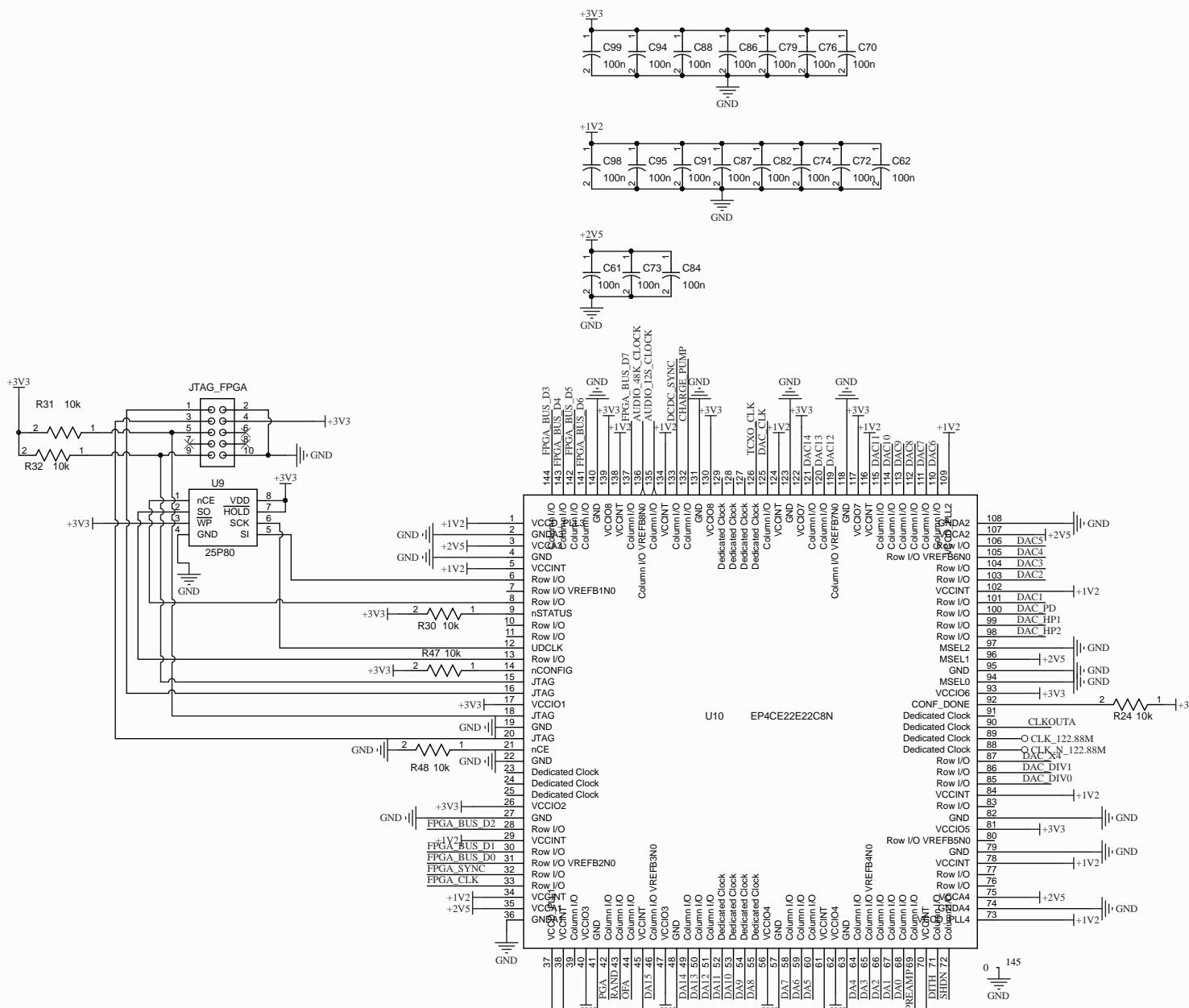


C



D

Title POWER			*
Size: A4	Number:10	Revision2.0	*
Date: 15.04.2021	Time: 23:00:00	Sheet10 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\POWER.SchDoc			Altium®

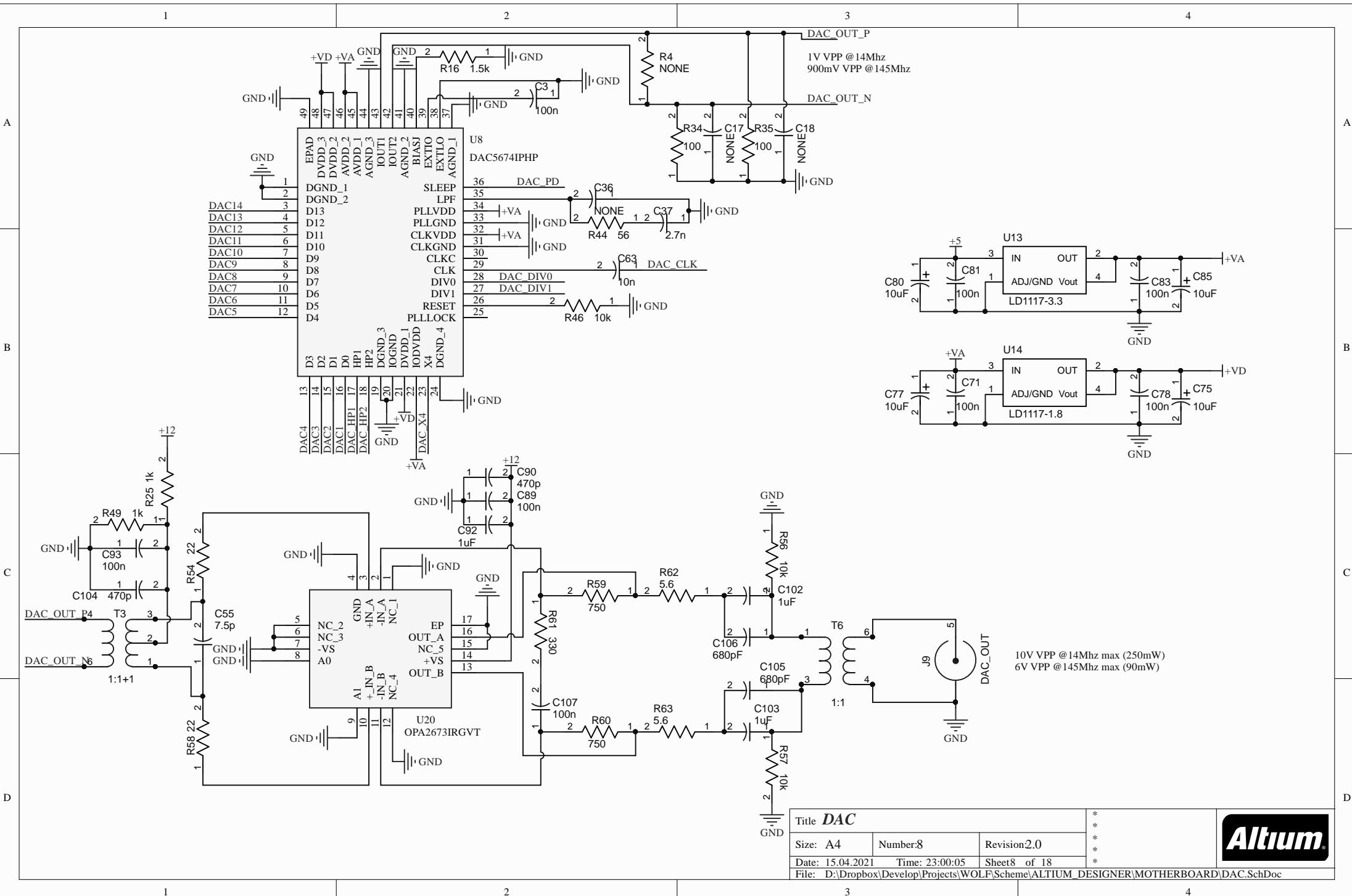


Title *FPGA*

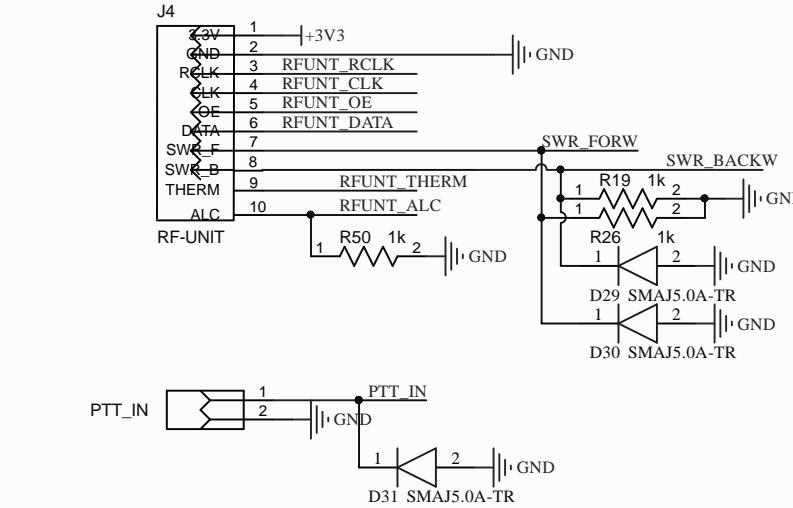
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Number:9 Revision:2.0
Time: 23:00:02 Sheet9 of 18

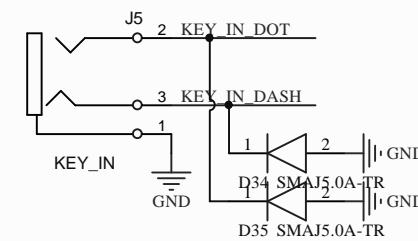
Altium



A



B



C

J6

FMC_NE1	1	CS 3v3
FMC_A18	2	RS 3v3
FMC_NWE	3	WR GND
FMC_NOE	4	RD GND
STM_NRST	5	RST_MISO
FMC_D0	6	PERI_MOSI
FMC_D1	7	DB0_MOSI
FMC_D2	8	DB1_WRX
FMC_D3	9	DB2_WTX
FMC_D4	10	DB3_CLK
FMC_D5	11	DB4\$DCS
FMC_D6	12	DB5\$AD1C
FMC_D7	13	DB6\$AD2C
FMC_D8	14	DB7_E2A
FMC_D9	15	DB8_E2B
FMC_D10	16	DB9_E2S
FMC_D11	17	DB10\$E1A
FMC_D12	18	DB11\$E1B
FMC_D13	19	DB12\$AD3C
FMC_D14	20	DB13_5v
FMC_D15	21	DB14\$GND
PWR_ON	22	DB15\$GND
PWR_HOLD	23	PONCAS
	24	PHOCAS
		3v3

FPC-45P-0.5mm

D

D

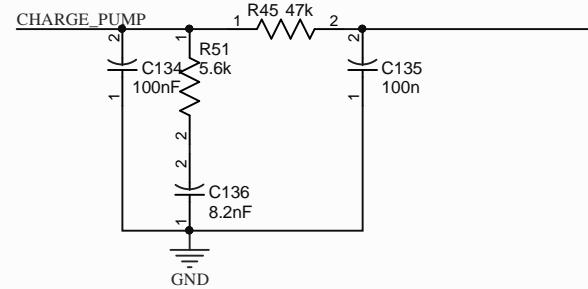
Title **CONNECTIONS**

Size: A4	Number:7	Revision 2.0
Date: 15.04.2021	Time: 23:00:07	Sheet 7 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\CONNECTIONS.SchDoc		

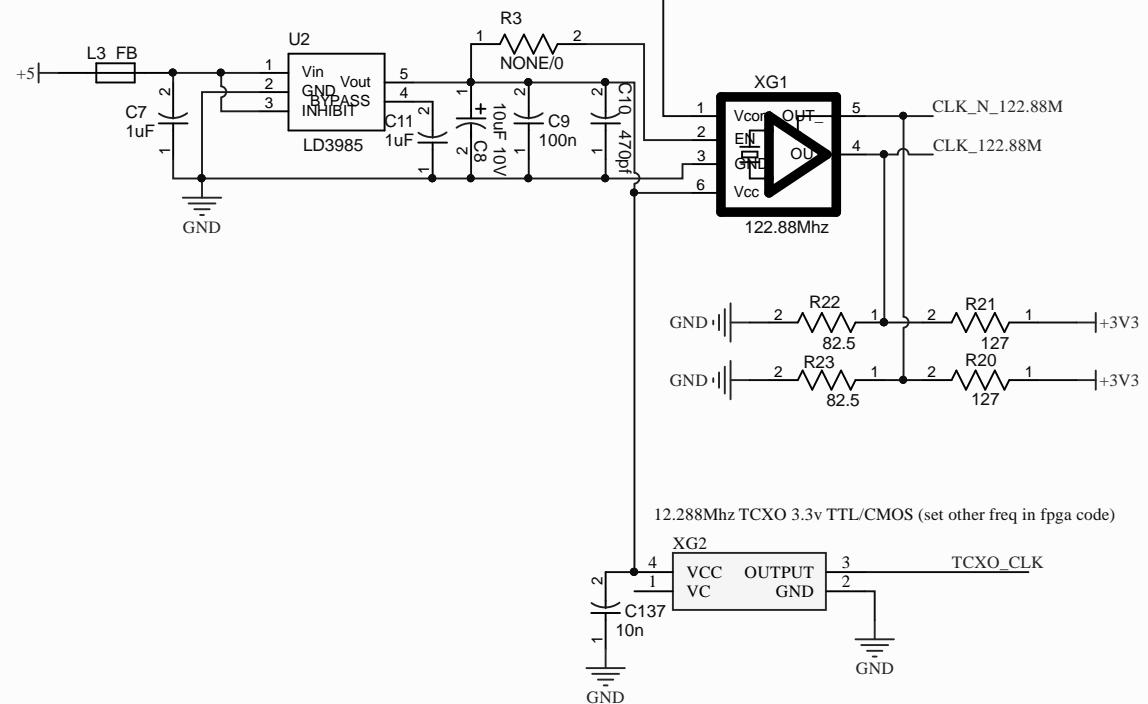
Altium

D

A



B



C

D

Title **CLOCK_GENERATOR**

Size: A4 Number:6 Revision 2.0

Date: 15.04.2021 Time: 23:00:09 Sheet 6 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\CLOCK_GENERATOR.SchDoc

Altium

A

B

C

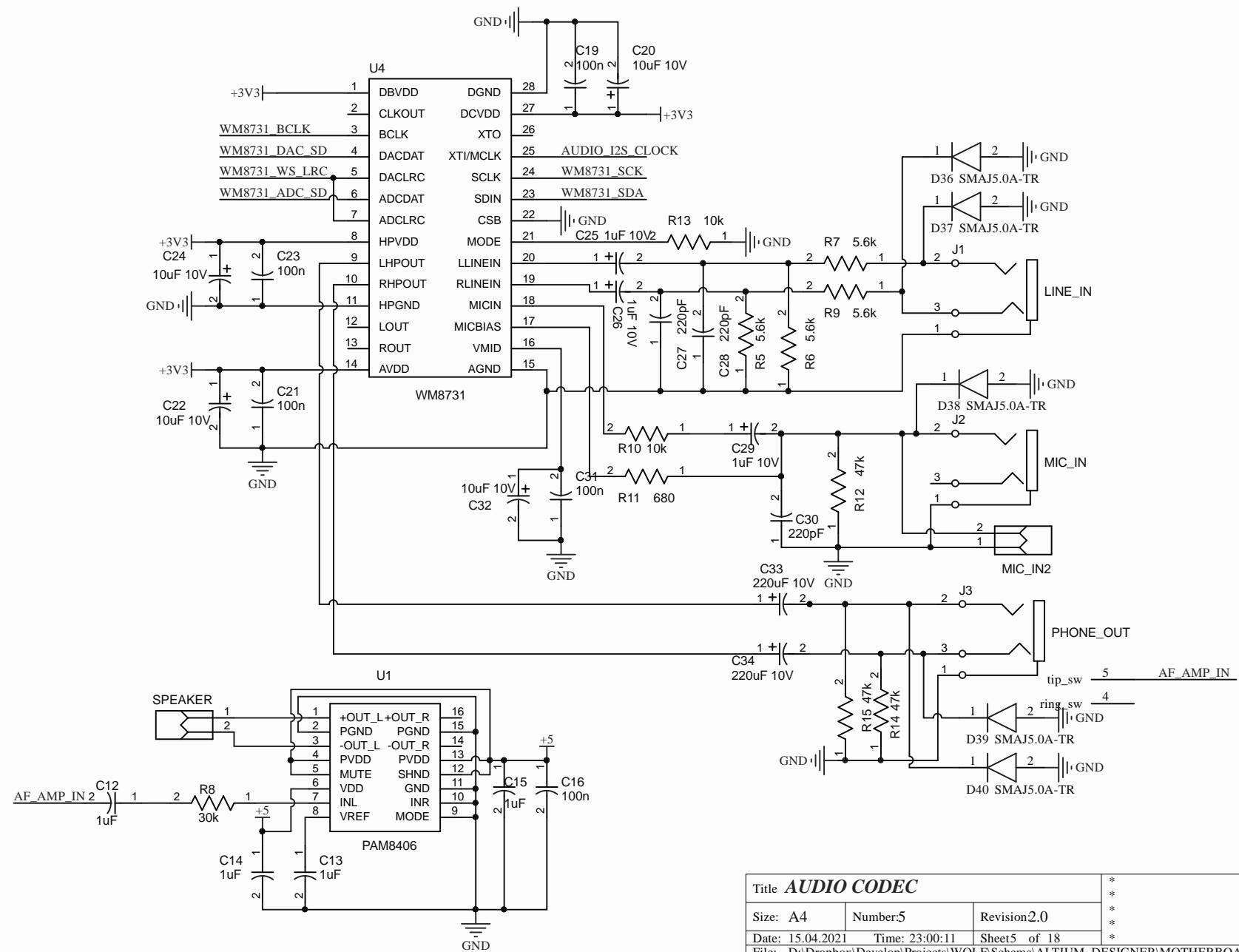
D

A

B

C

D

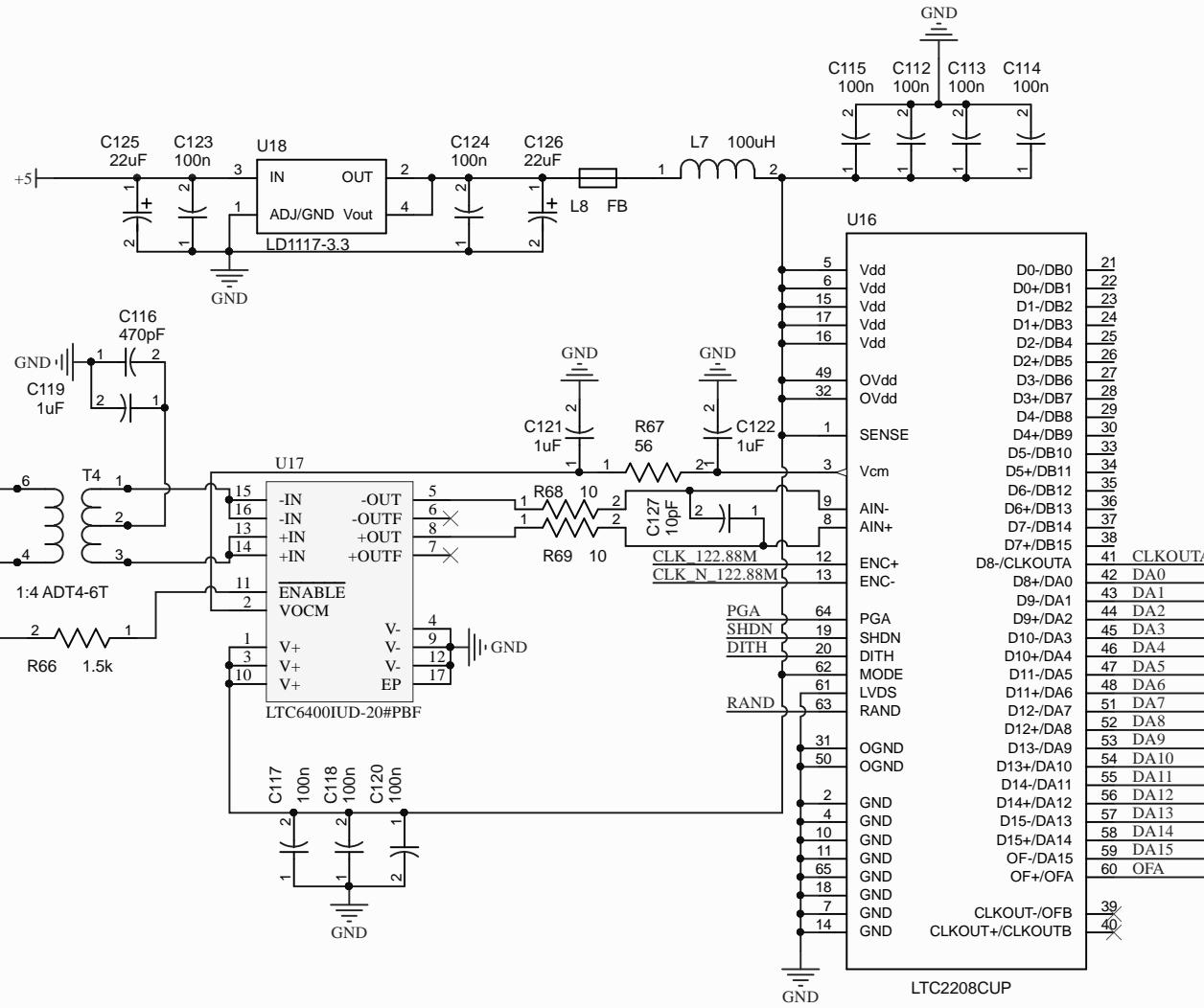
Title **AUDIO CODEC**

Size: A4 Number:5 Revision:2.0

Date: 15.04.2021 Time: 23:00:11 Sheet 5 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\AUDIO_CODEC.SchDoc

Altium

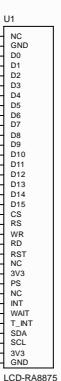
Title **ADC**

Size: A4 Number:3 Revision 2.0

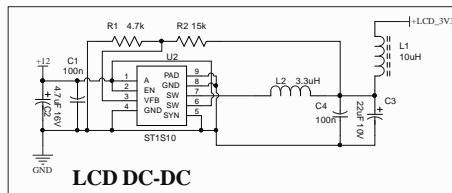
Date: 15.04.2021 Time: 23:00:13 Sheet 3 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\ADC.SchDoc

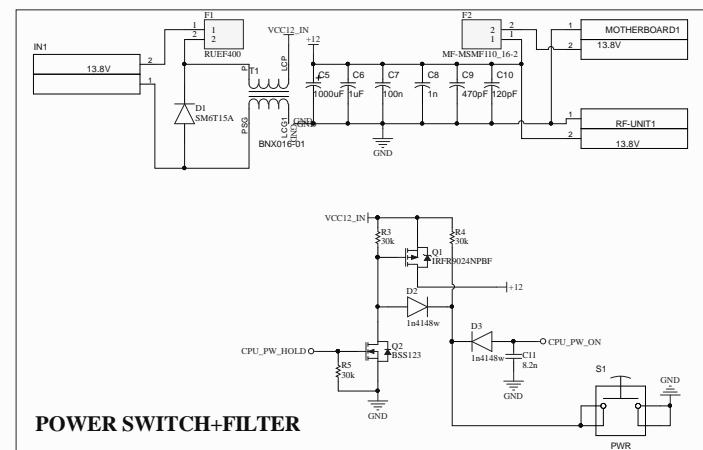
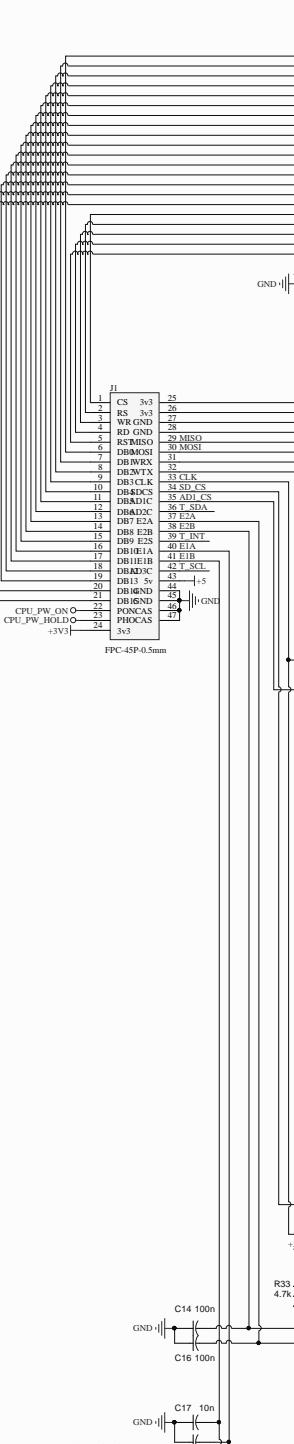
Altium



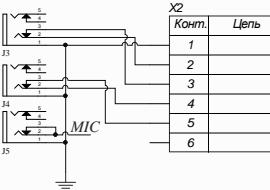
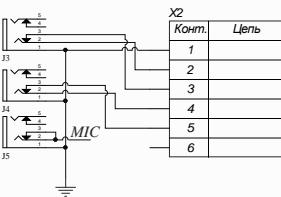
7Inch version



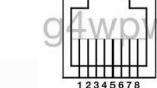
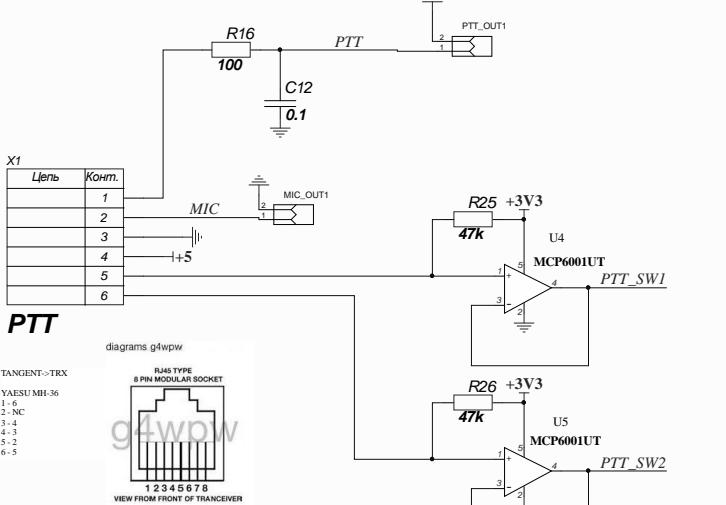
LCD DC-DC



POWER SWITCH+FILTER



TANGENT



Title FRONT-UNIT-7INCH	
Size: A2	Number: 1
Date: 15.04.2021	Revision: 2.0
Time: 23:00:16	Sheet 1 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM DESIGNER\FRONT-UNIT-7INCH\FRONT-UNIT-7INCH.CDDOC	