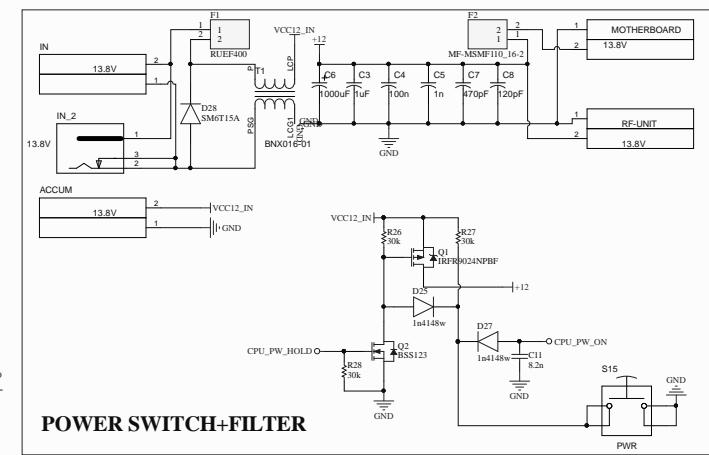
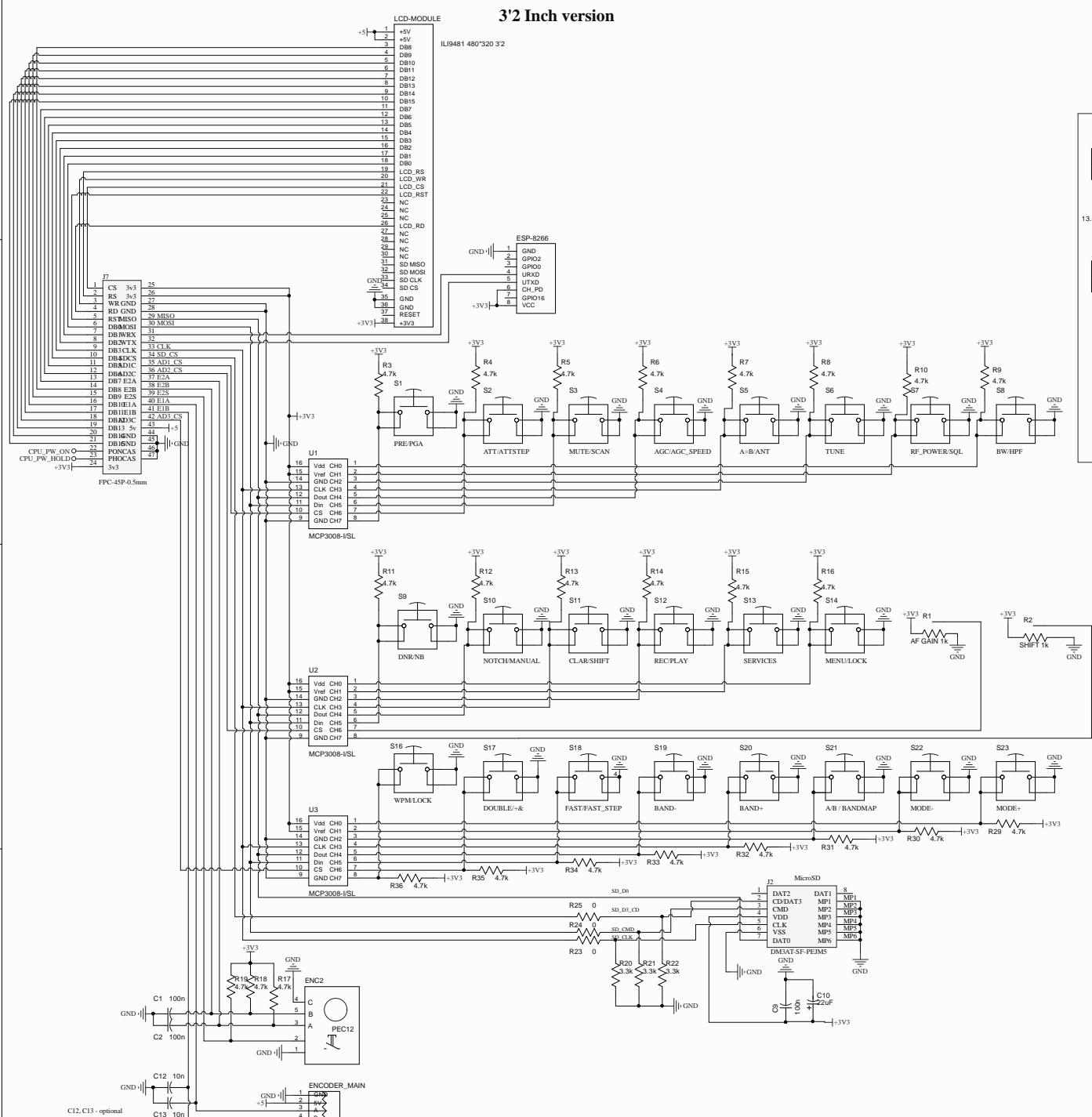
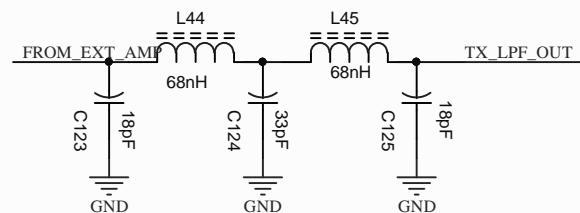


3'2 Inch version

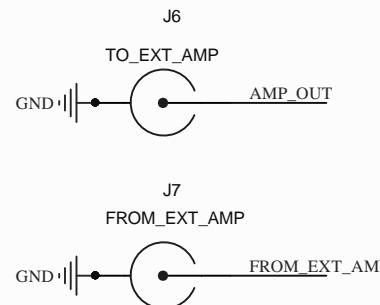
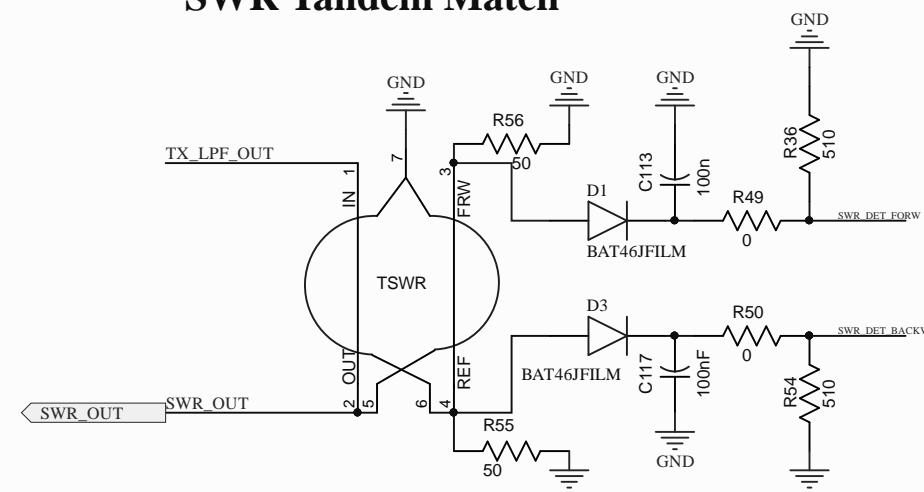


TX_LPF

LPF 170Mhz



Катушки намотаны проводом 0.5мм на оправке диаметром 5мм, 3.5 витка

**SWR Tandem Match**

2 провода 0.9мм пропущено через бинокль

по диагонали пропущено и намотано по 10 витков 0.5мм

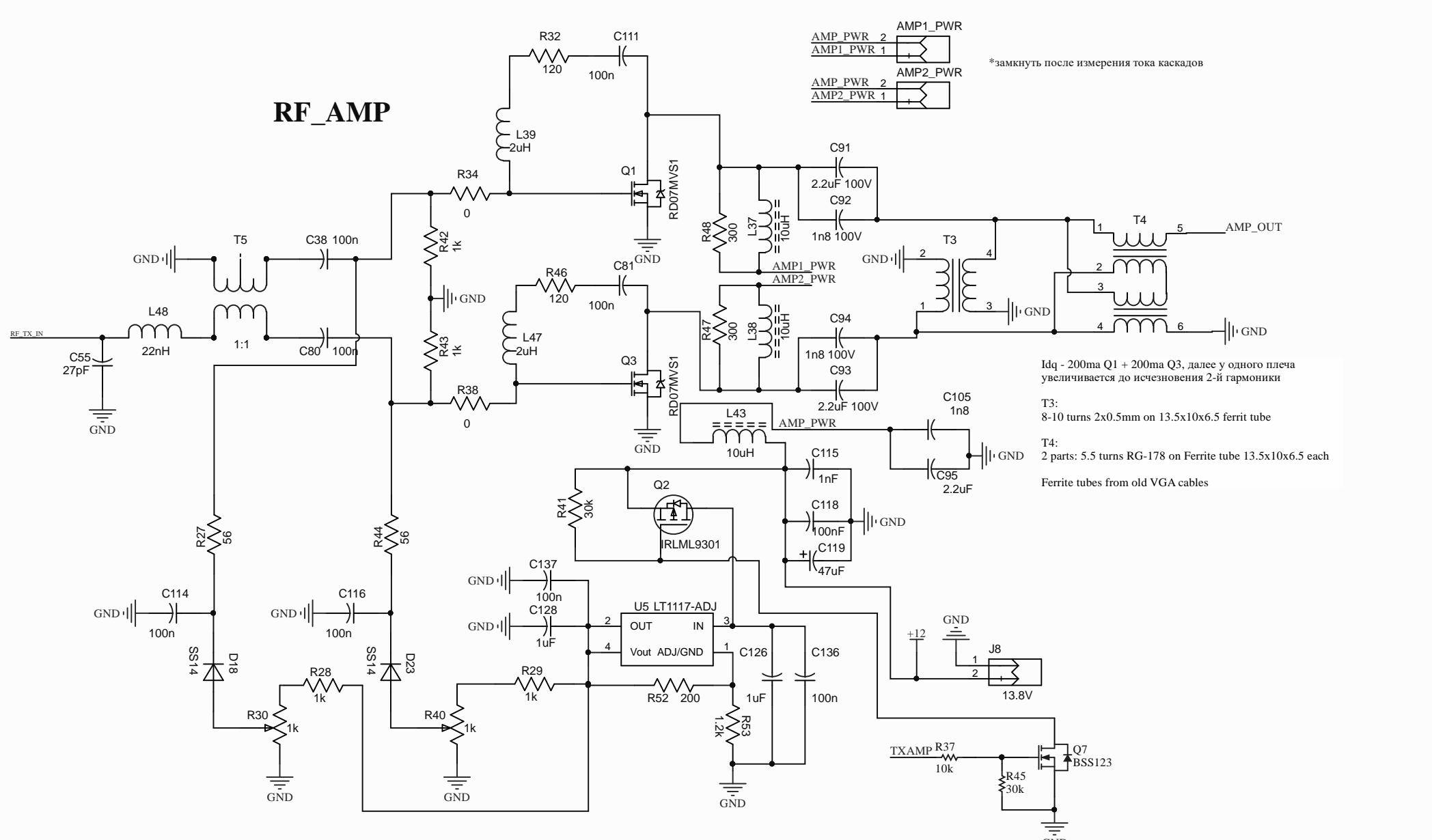
Title **SWR Tandem Match**

Size: A4 Number:18 Revision:2.0

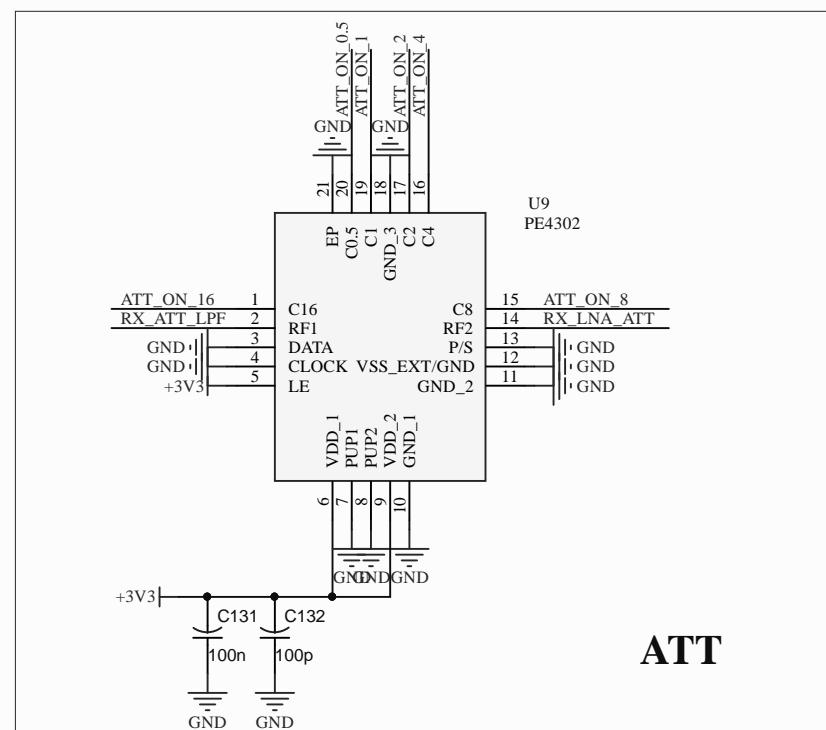
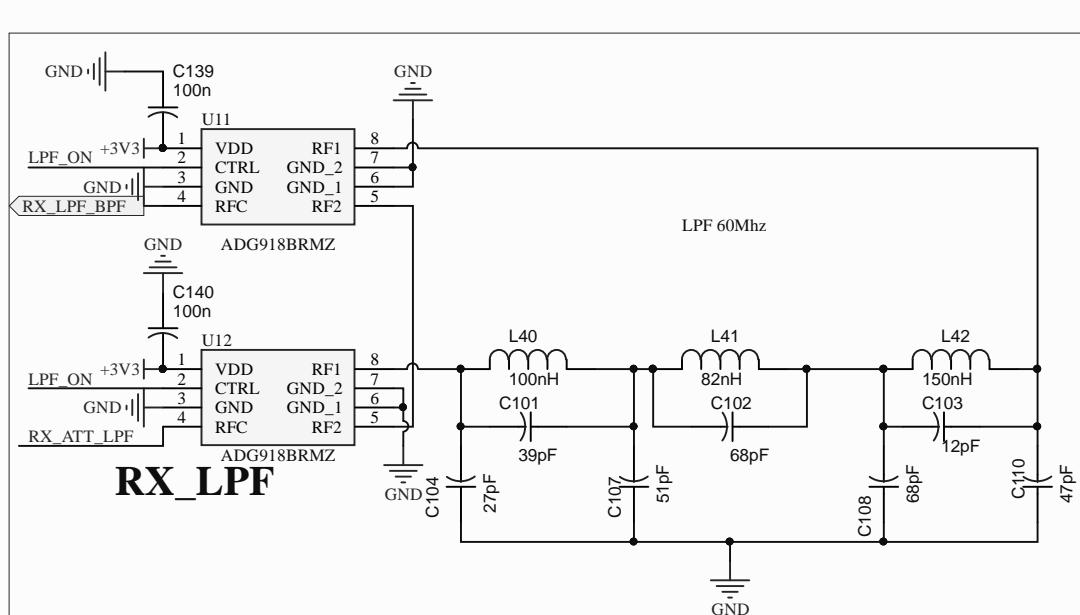
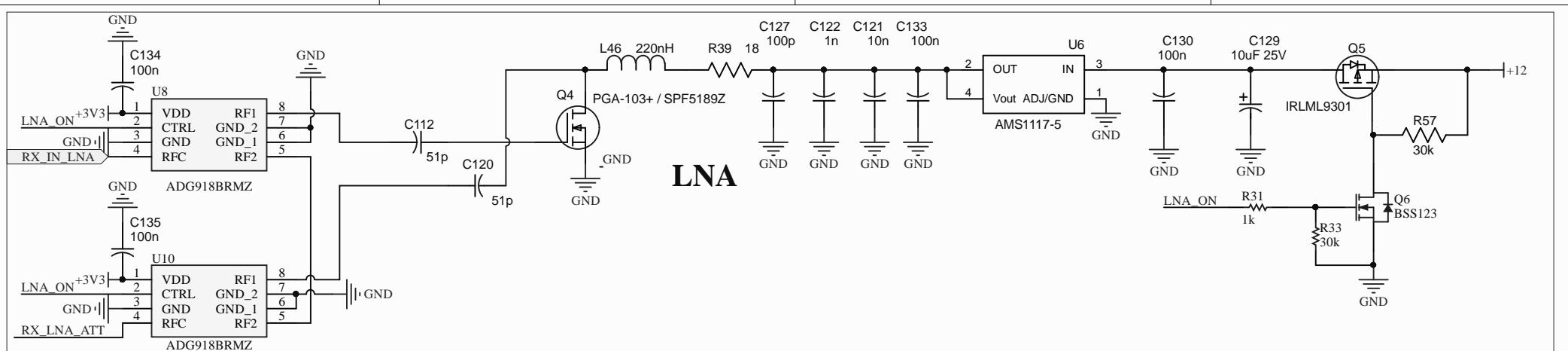
Date: 01.06.2021 Time: 0:18:33 Sheet:18 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\SWR Tandem Match.SchDoc

Altium



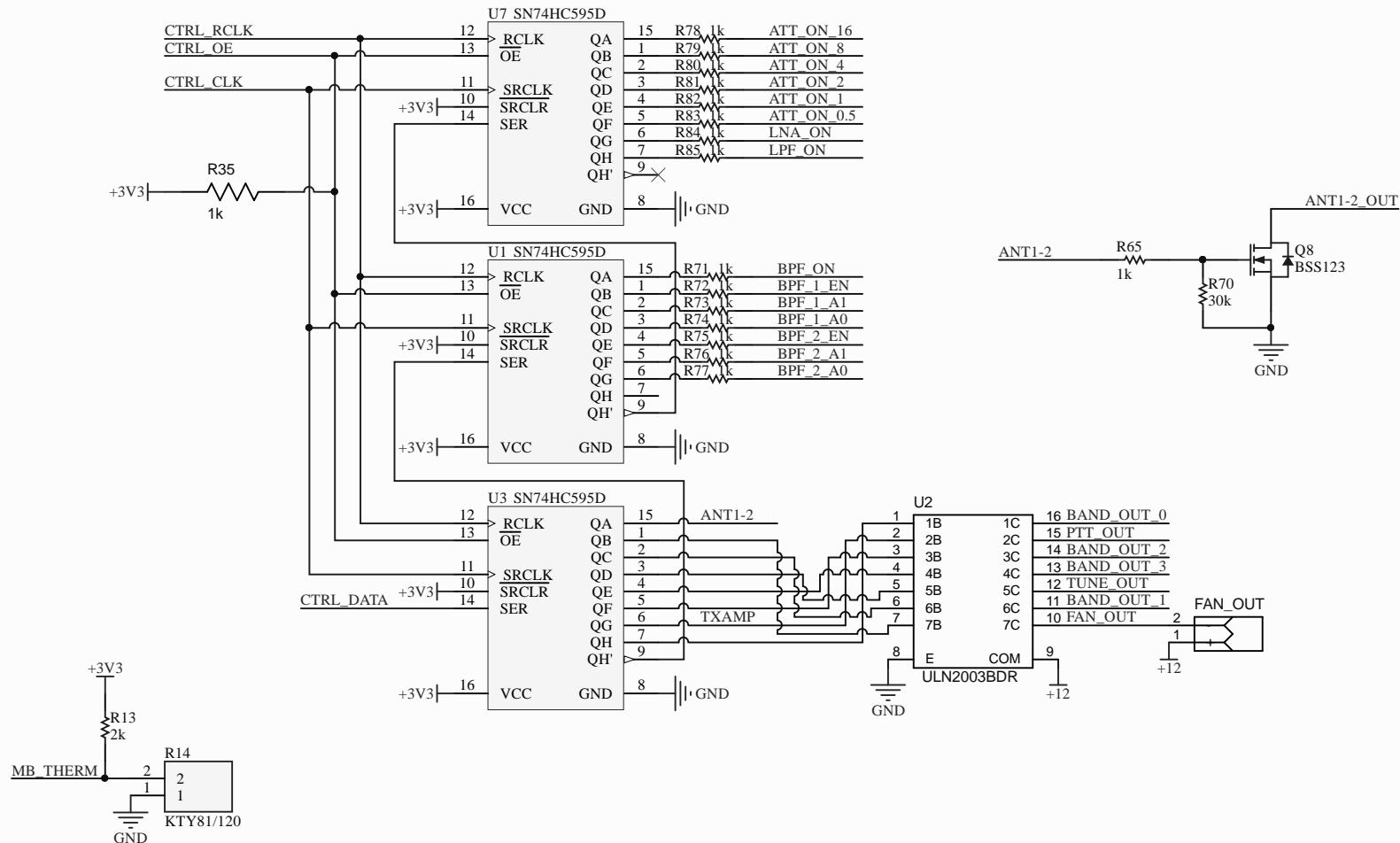
Title <i>RF_AMP</i>	*		
Size: A4	Number:17		Revision2.0
Date: 01.06.2021	Time: 0:18:35		Sheet17 of 18
File: D:\Dropbox\Develon\Projects\WOLF\Scheme\ALTIUM DESIGNER\RF-UNIT\RF_AMP.SchDoc			
*			



Title LNA			*
Size: A4	Number: 16	Revision 2.0	*
Date: 01.06.2021	Time: 0:18:38	Sheet 16 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\LNA.SchDoc			

Altium

A

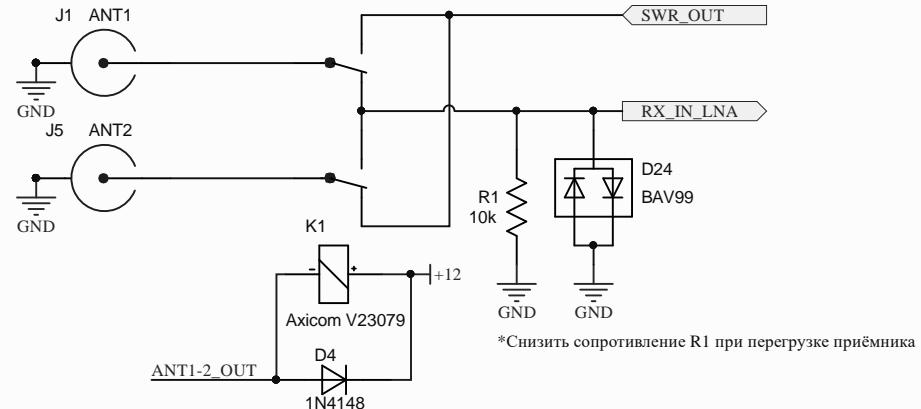
Title **CONTROL**

Size: A4	Number:15	Revision2.0
Date: 01.06.2021	Time: 0:18:40	Sheet15 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONTROL.SchDoc		*

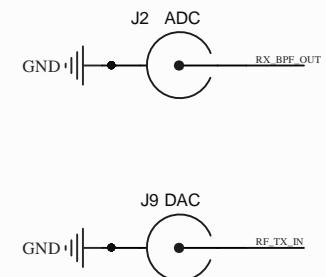
Altium

A

TXRX_COMUTATOR



CONN



B

C

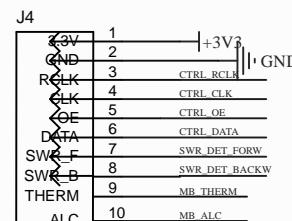
D

A

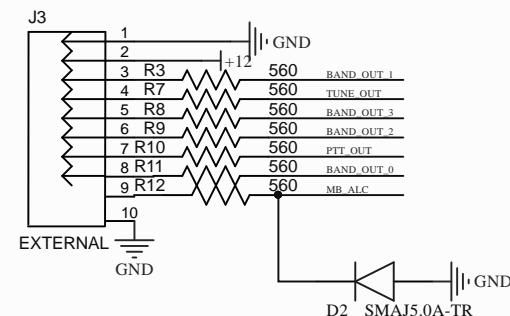
B

C

D



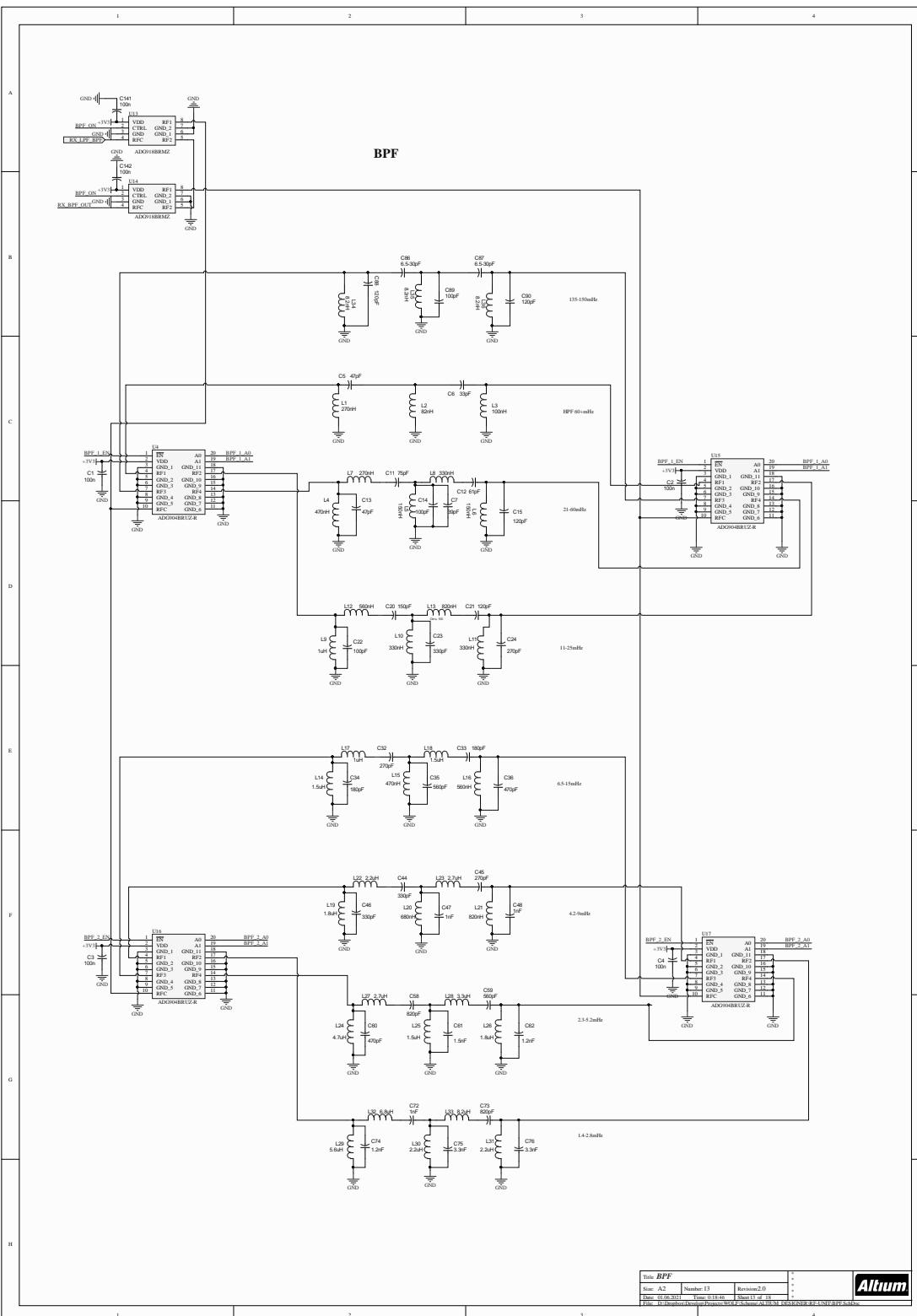
MOTHERBOARD

Title **CONN**

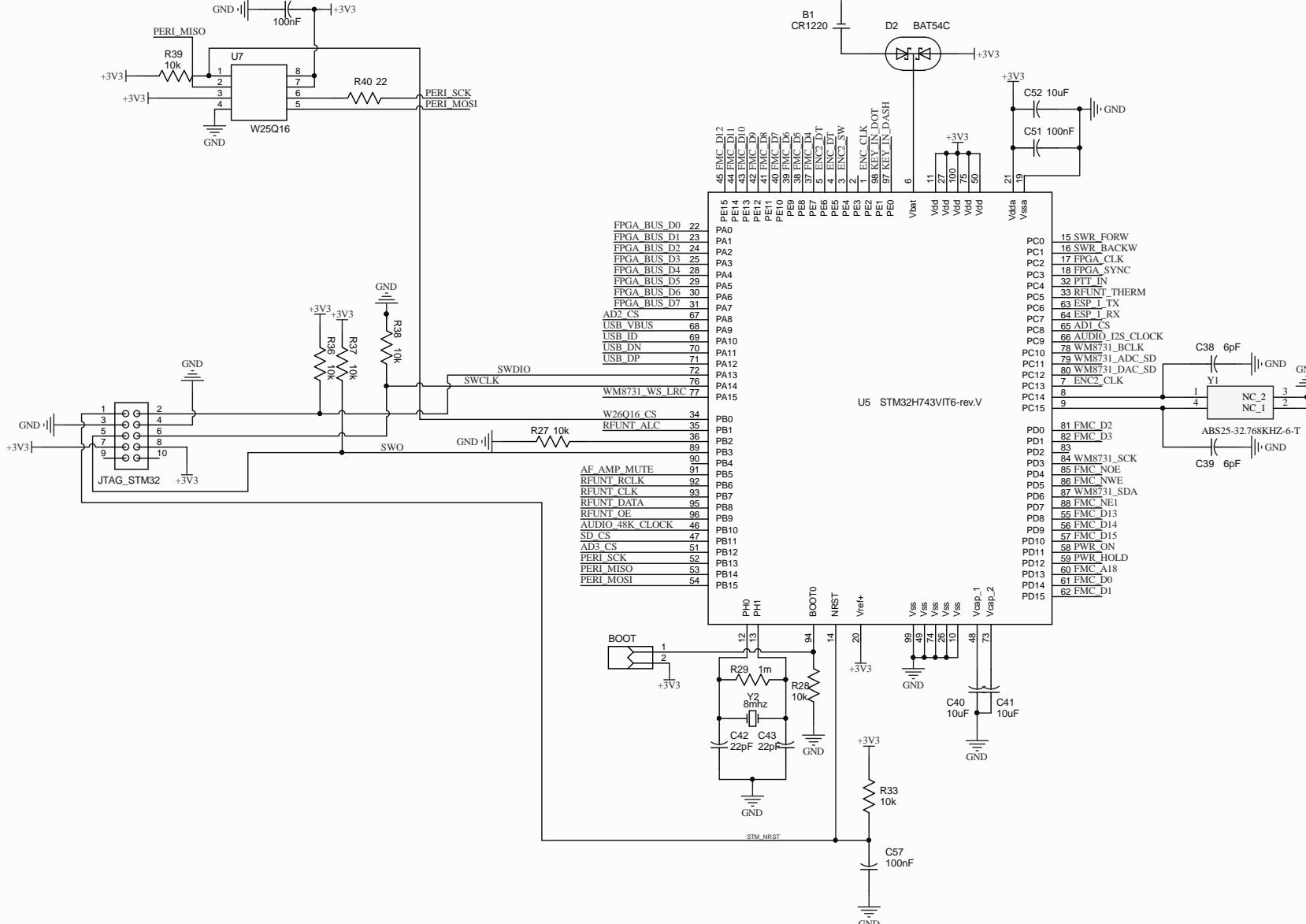
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Date: 01.06.2021 Time: 0:18:43 Sheet:14 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONN.SchDoc



STM32

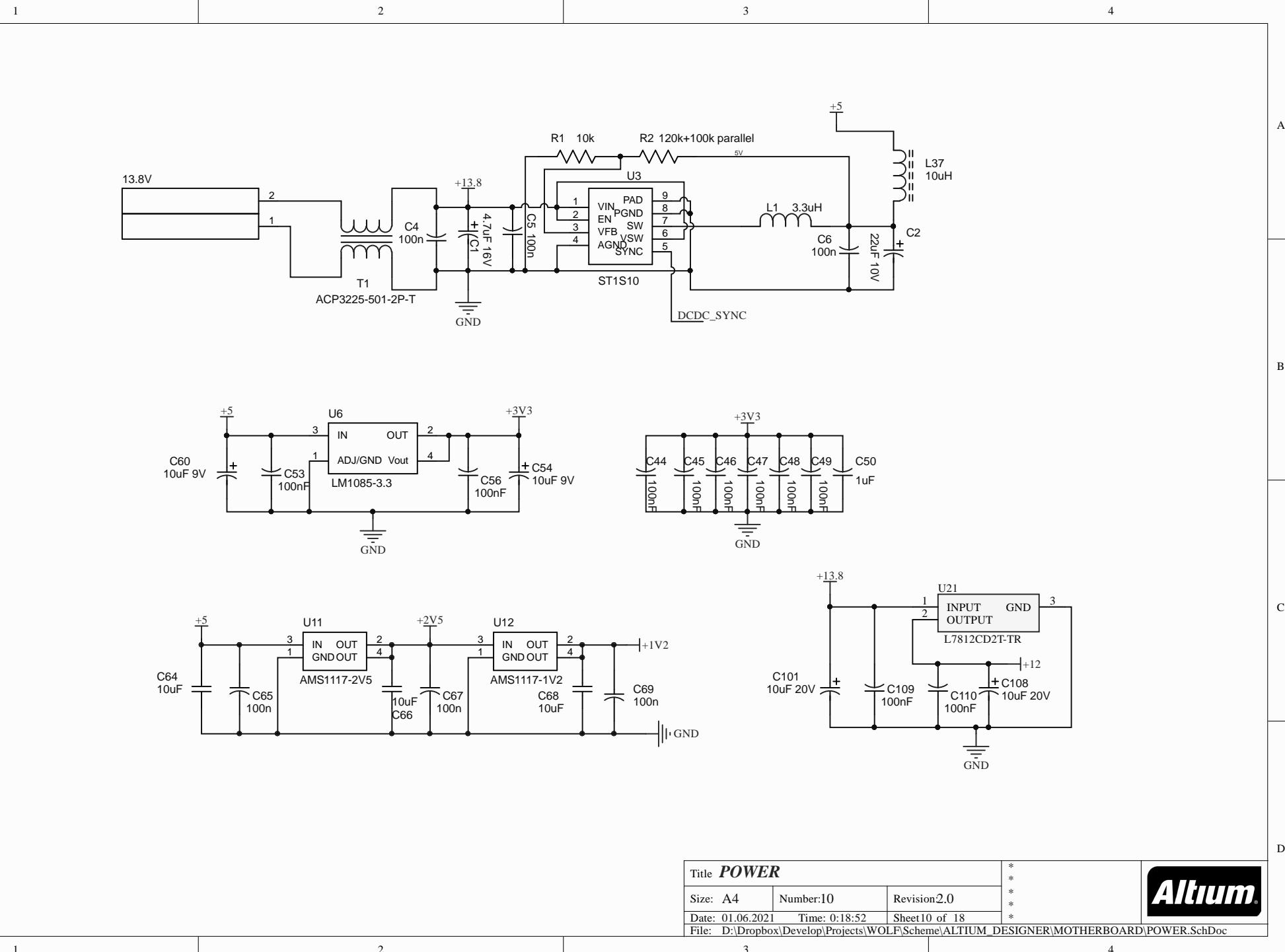
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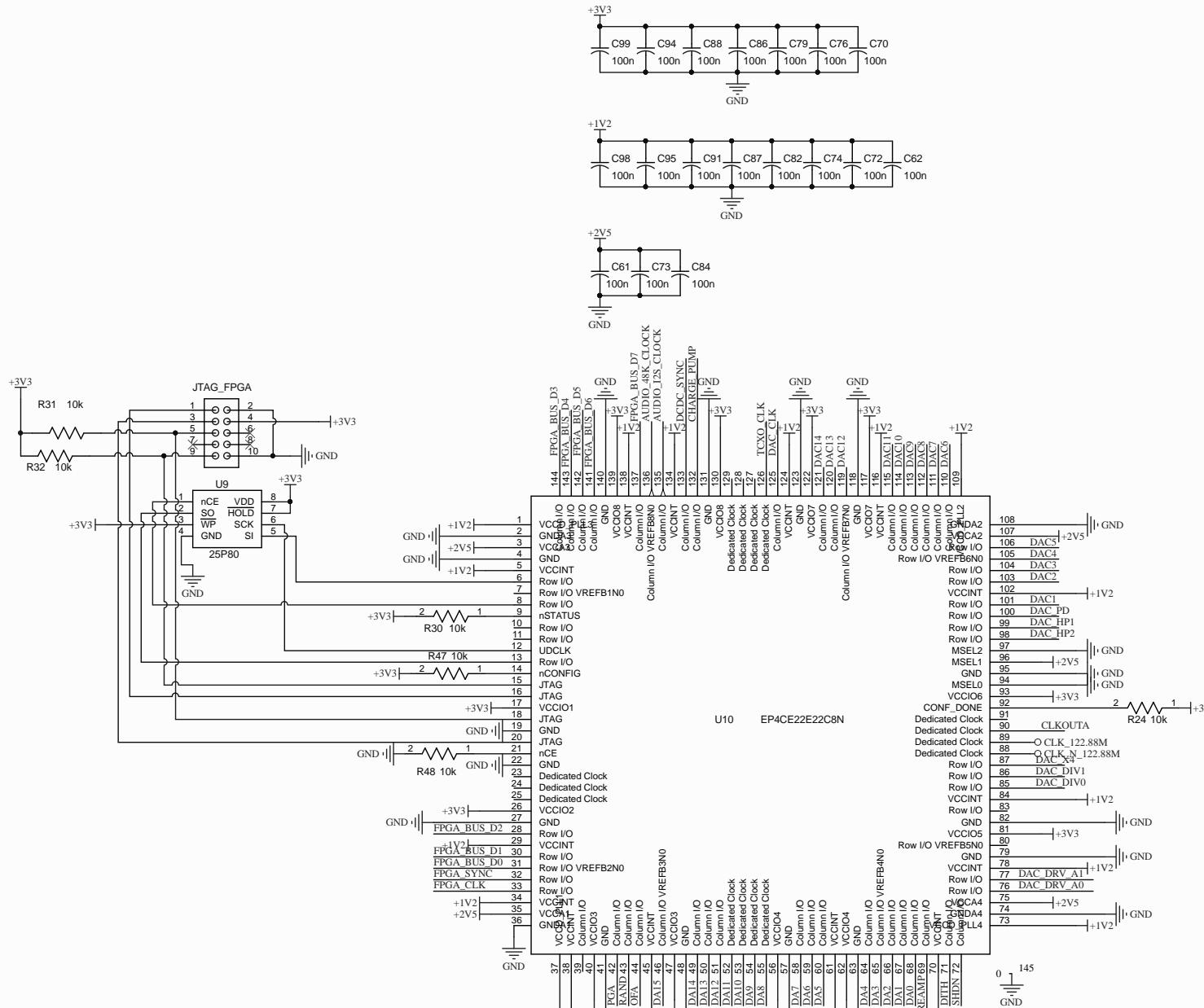
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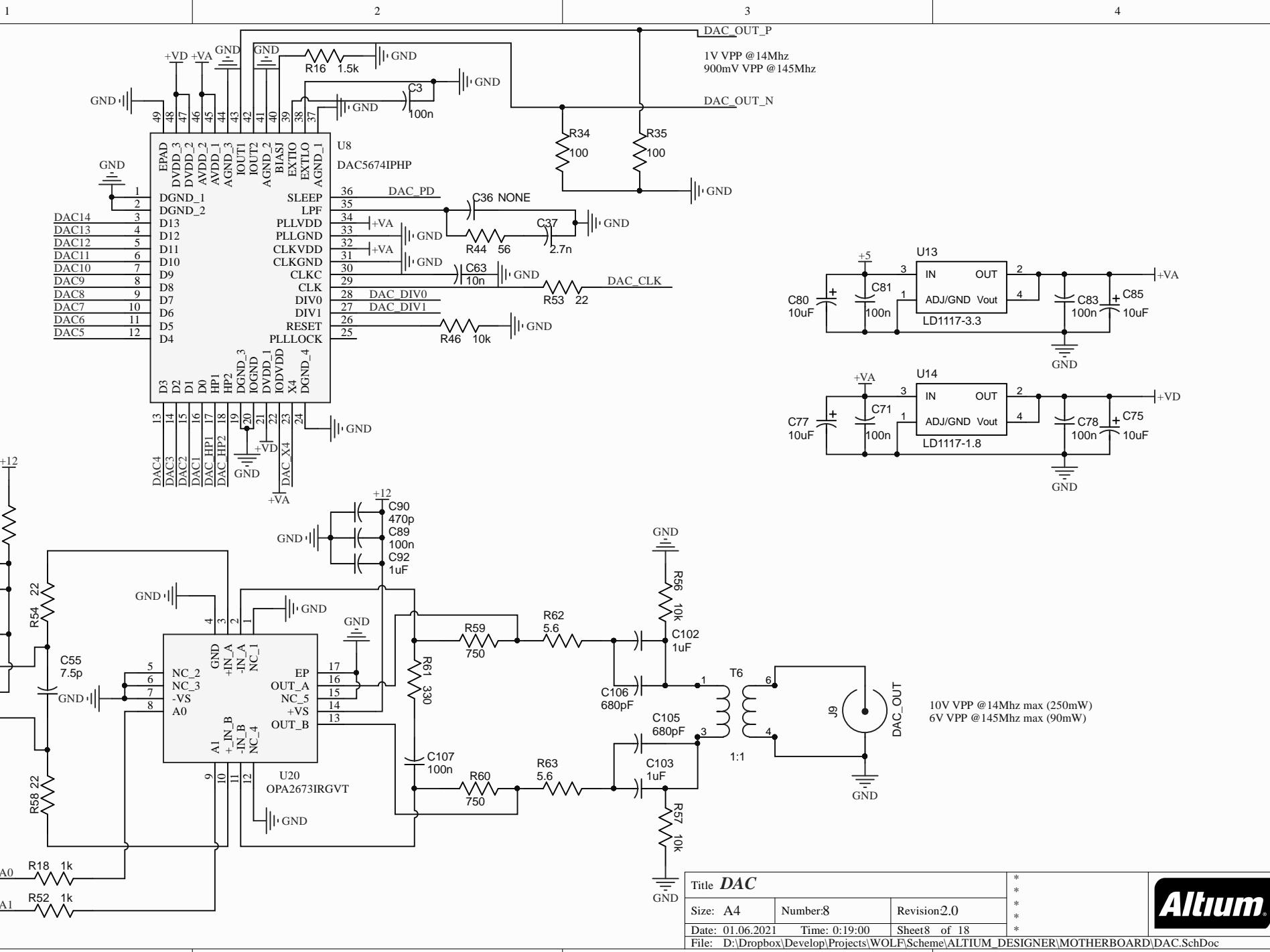
Date: 01.06.2021 Time: 0:18:49 Sheet 11 of 18

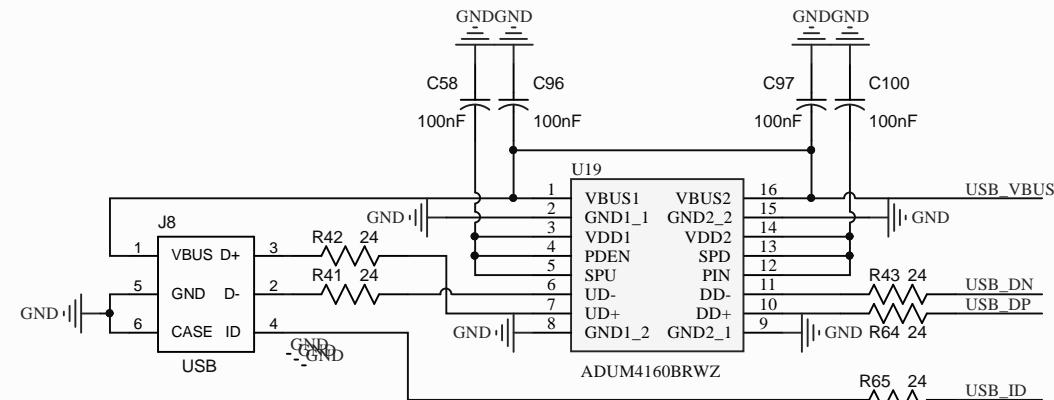
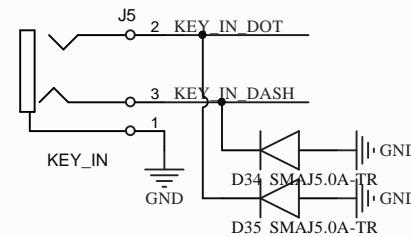
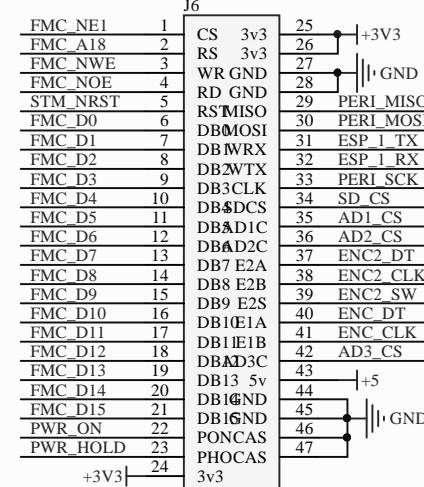
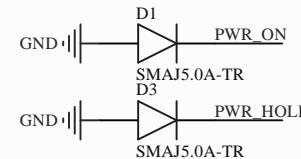
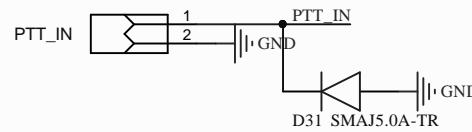
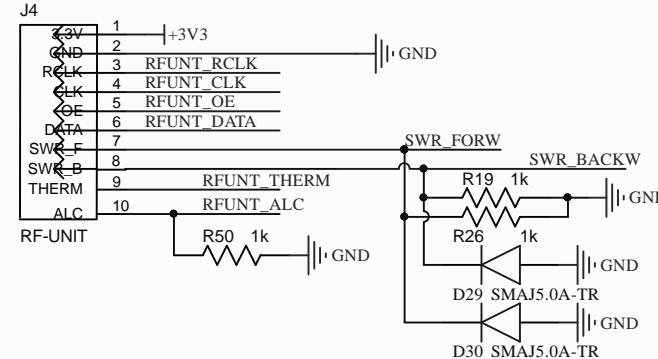
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Title **CONNECTIONS**

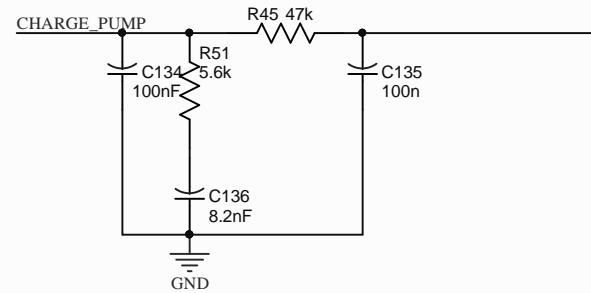
Size: A4 Number: 7 Revision 2.0

Date: 01.06.2021 Time: 0:19:05 Sheet 7 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\MOTHERBOARD\CONNECTIONS.SchDoc

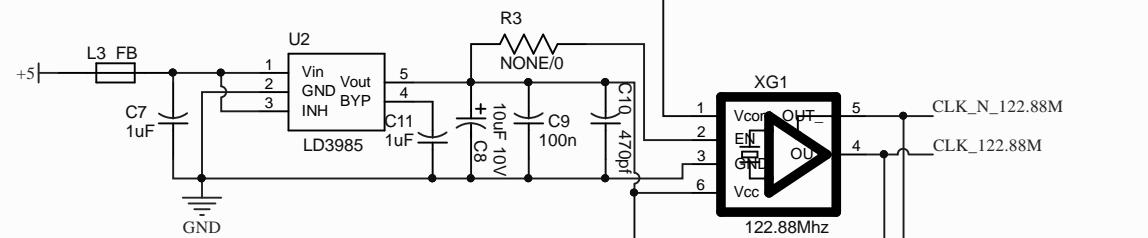
Altium

A



A

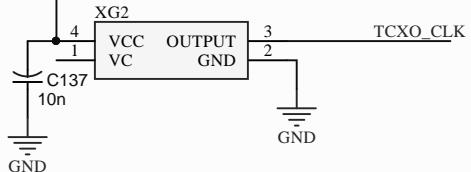
B



B

C

12.288Mhz TCXO 3.3v TTL/CMOS (set other freq in fpga code)



C

D

Title **CLOCK_GENERATOR**

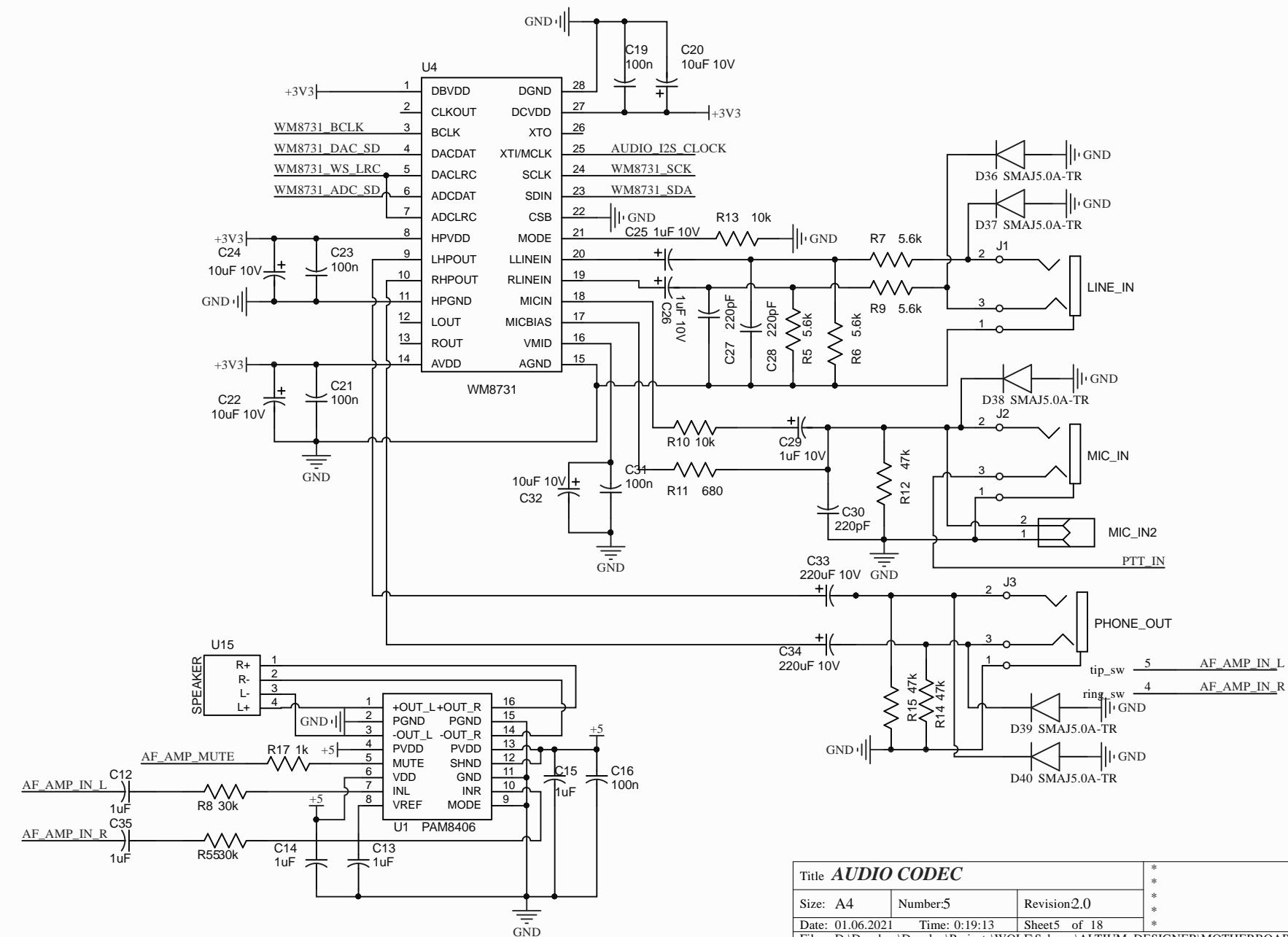
Size: A4 Number:6 Revision:2.0

Date: 01.06.2021 Time: 0:19:09 Sheet: 6 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\CLOCK_GENERATOR.SchDoc

Altium

A

Title **AUDIO CODEC**

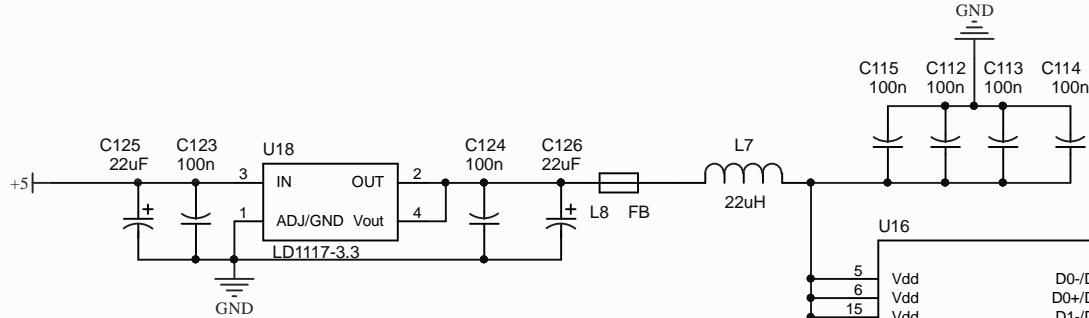
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Date: 01.06.2021 Time: 0:19:13 Sheet 5 of 18

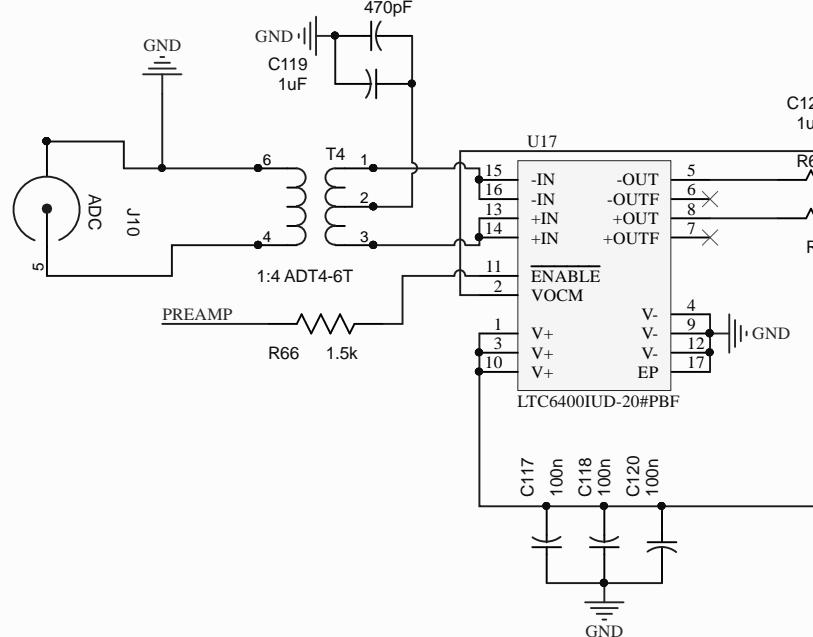
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Altium

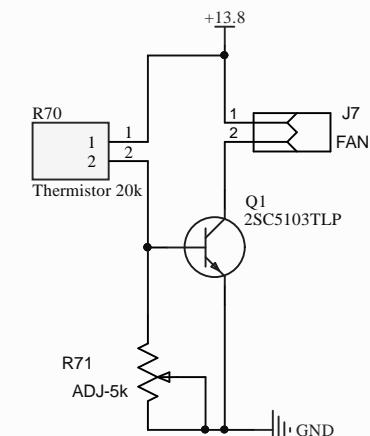
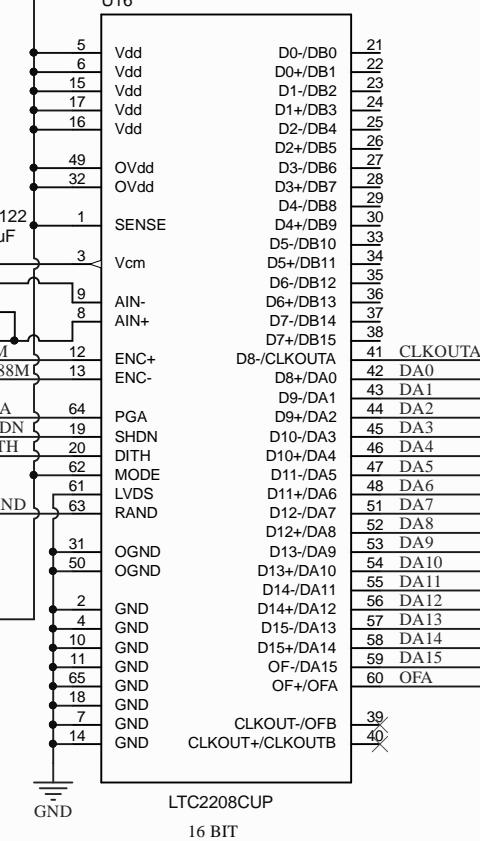
A



B



C

Title **ADC**

Size: A4 Number:3 Revision 2.0

Date: 01.06.2021 Time: 0:19:18 Sheet 3 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\MOTHERBOARD\ADC.SchDoc

Altium

