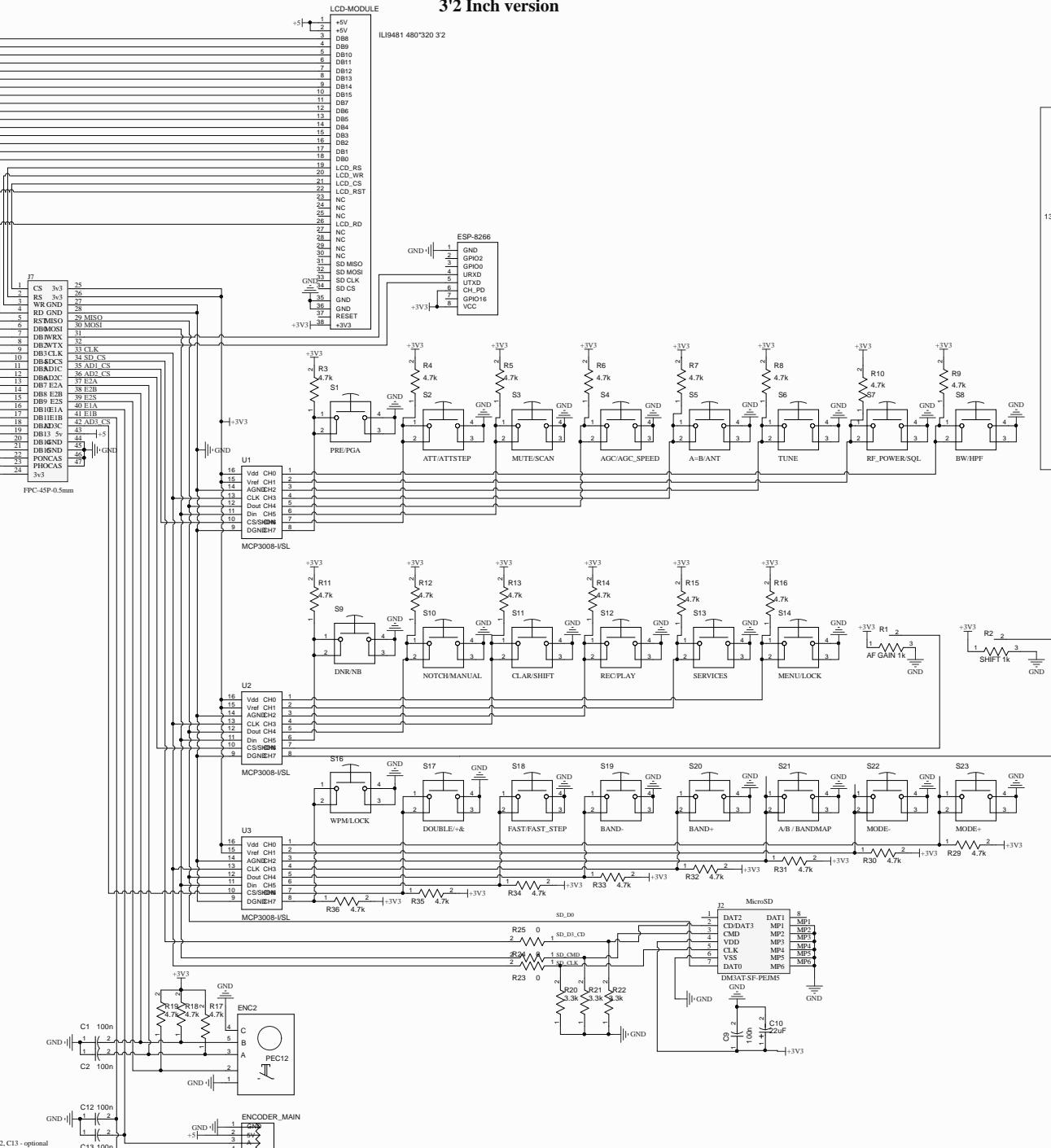
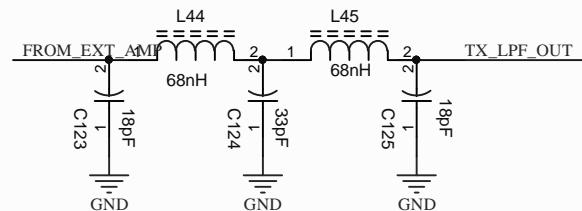


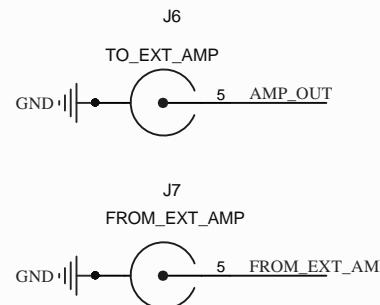
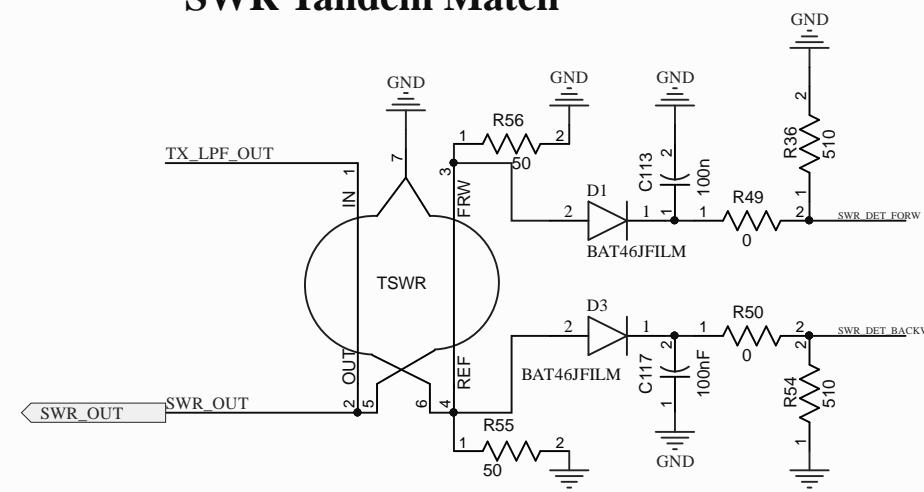
3'2 Inch version

TX_LPF

LPF 170Mhz



Катушки намотаны проводом 0.5мм на оправке диаметром 5мм, 3.5 витка

**SWR Tandem Match**

2 провода 0.9мм пропущено через бинокль

по диагонали пропущено и намотано по 10 витков 0.5мм

Title **SWR Tandem Match**

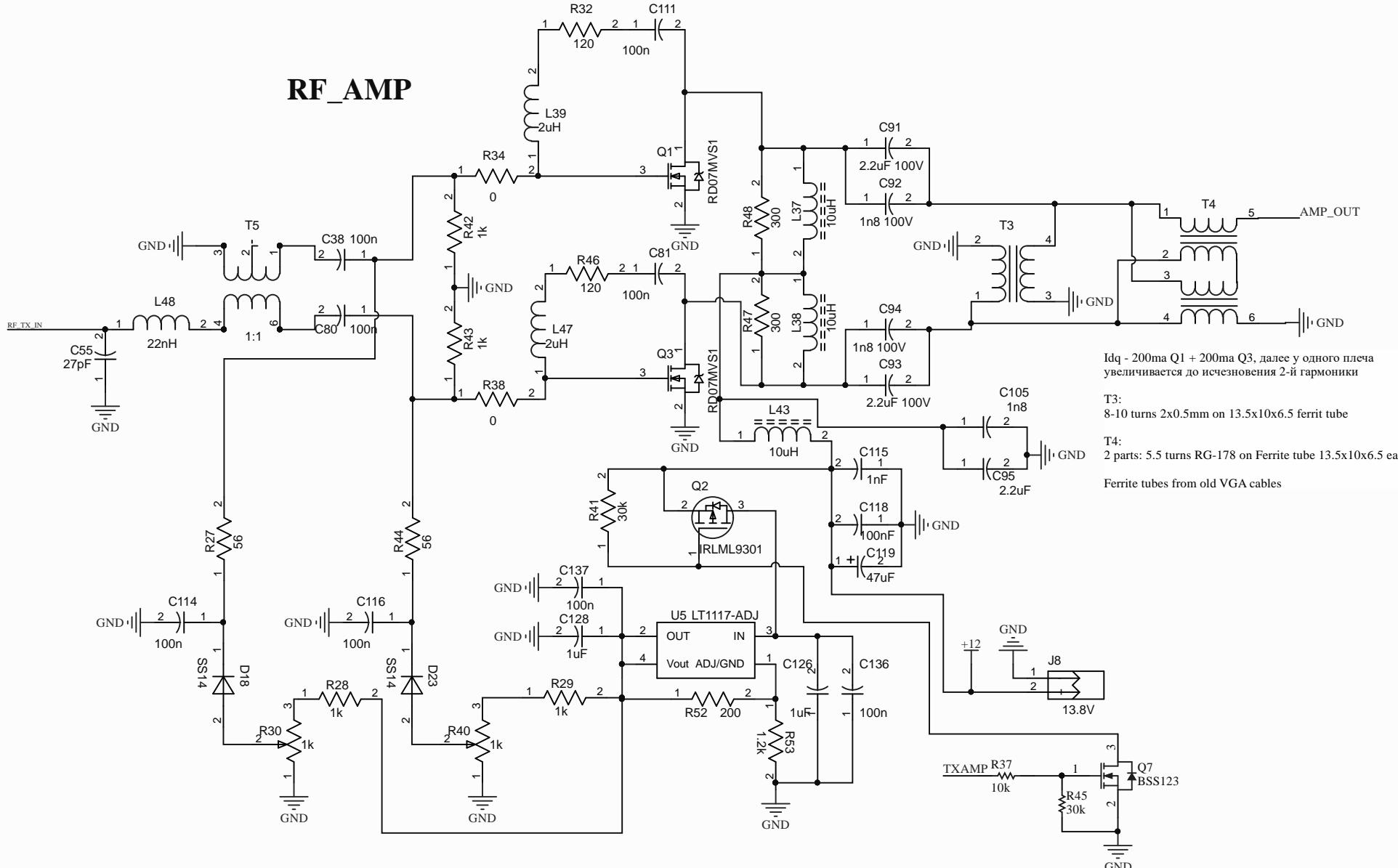
Size: A4 Number:18 Revision:2.0

Date: 24.12.2020 Time: 22:27:49 Sheet 18 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\SWR Tandem Match.SchDoc

Altium

A

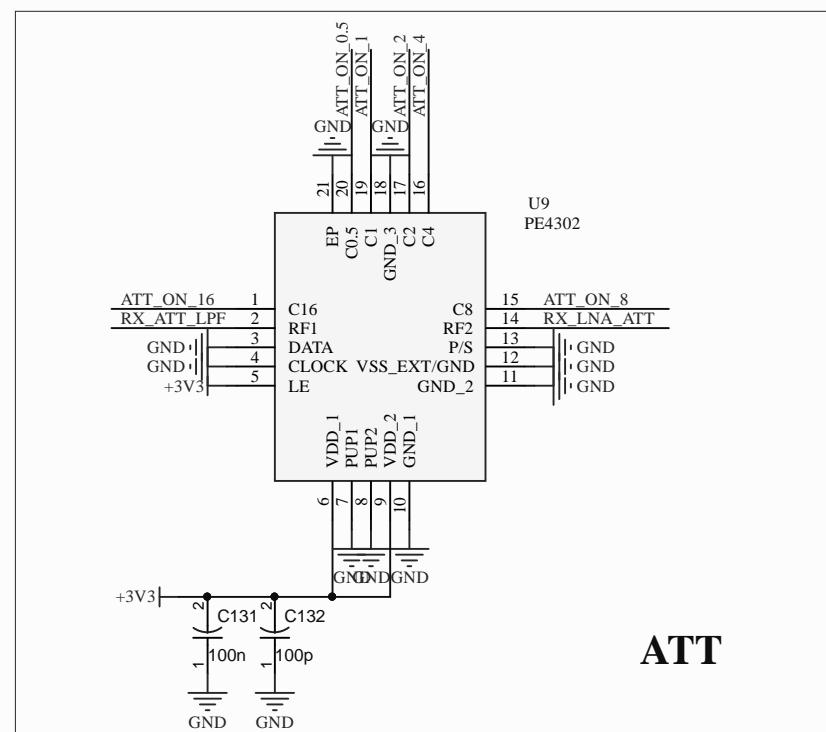
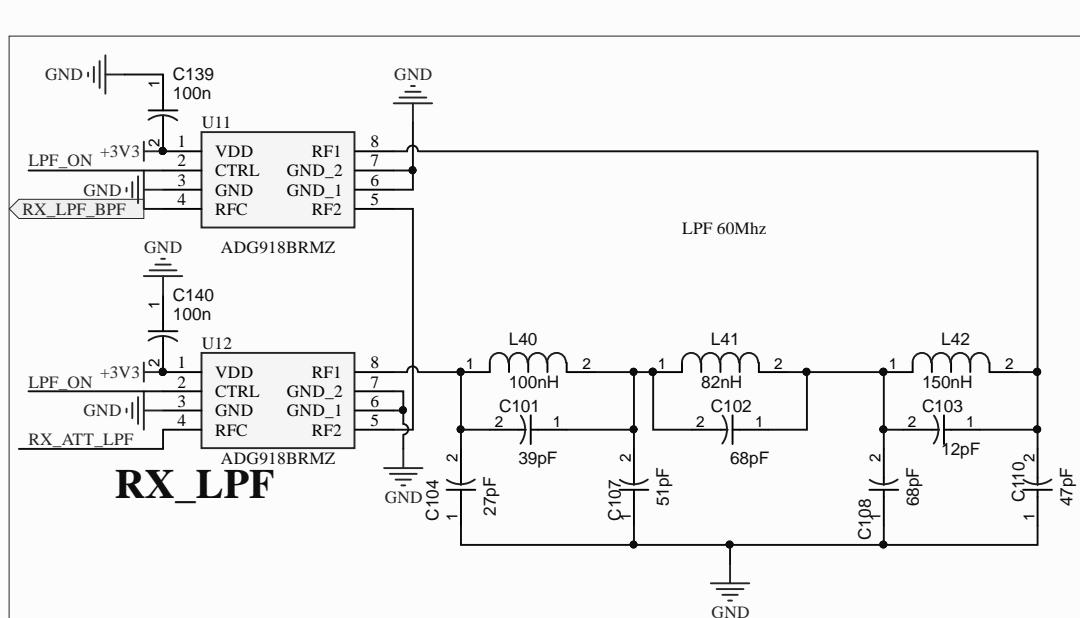
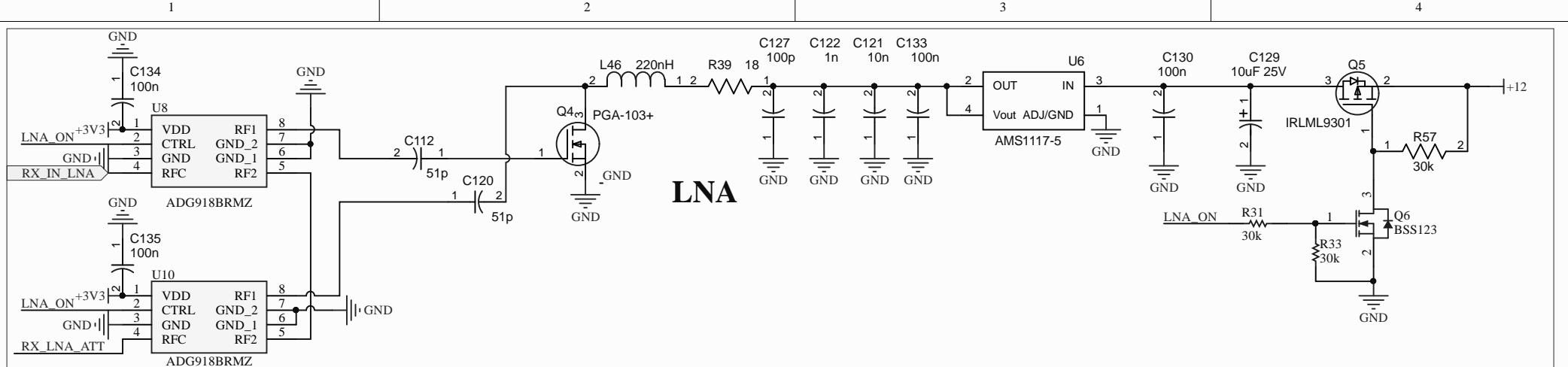
RF_AMPTitle **RF_AMP**

Size: A4 Number:17 Revision:2.0

Date: 24.12.2020 Time: 22:27:51 Sheet:17 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\RF-UNIT\RF_AMP.SchDoc

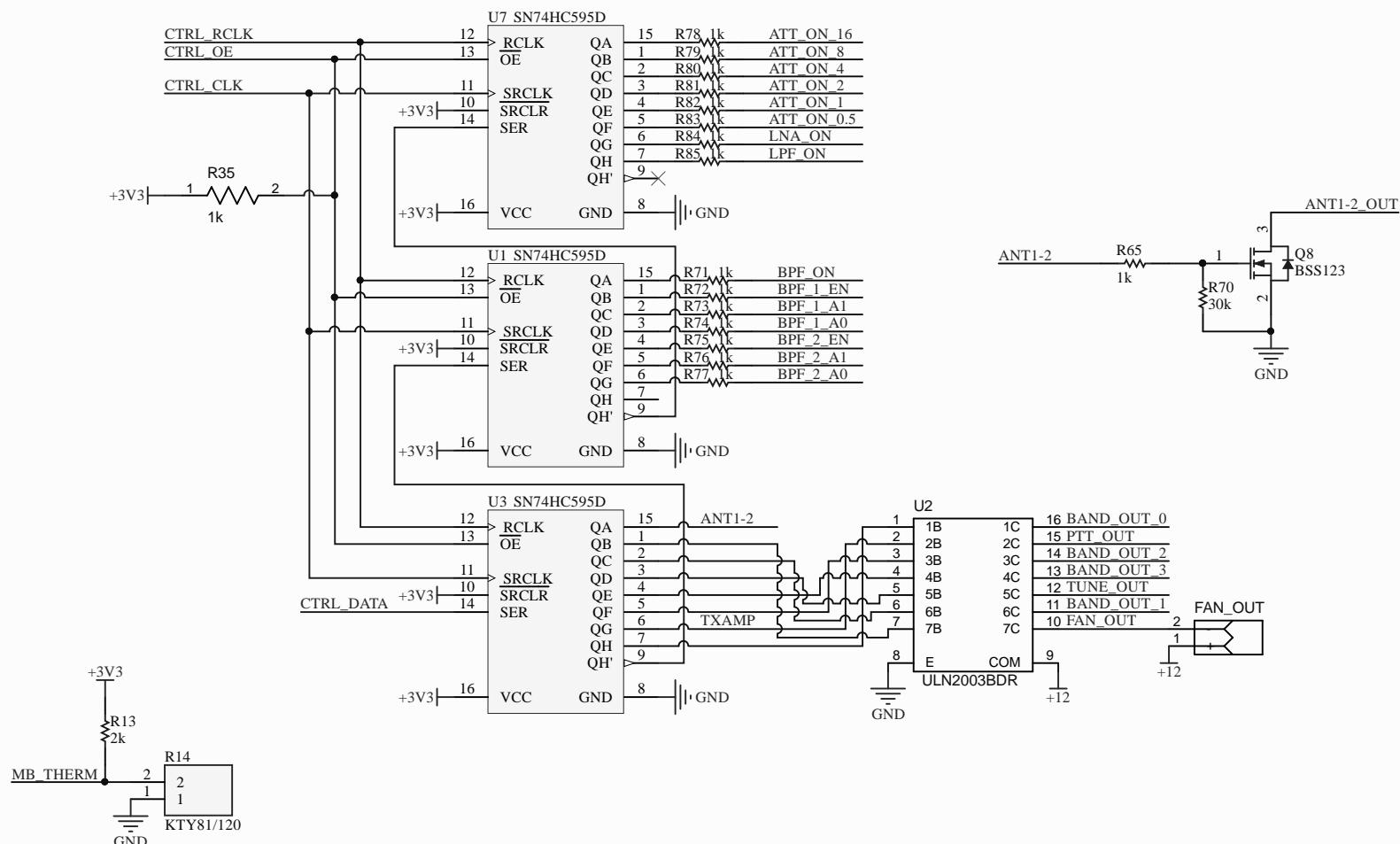
Altium



Title LNA			*
Size: A4	Number:16	Revision2.0	*
Date: 24.12.2020	Time: 22:27:53	Sheet16 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\LNA.SchDoc			

Altium

A

Title **CONTROL**

Size: A4 Number:15 Revision:2.0

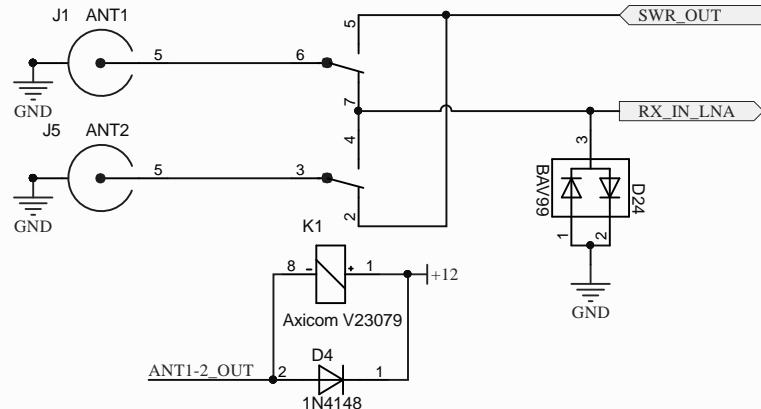
Date: 24.12.2020 Time: 22:27:55 Sheet 15 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONTROL.SchDoc

Altium

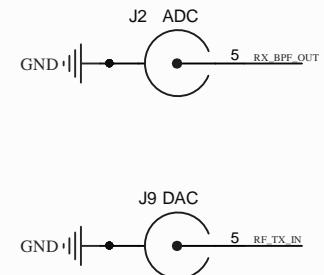
A

TXRX_COMUTATOR



B

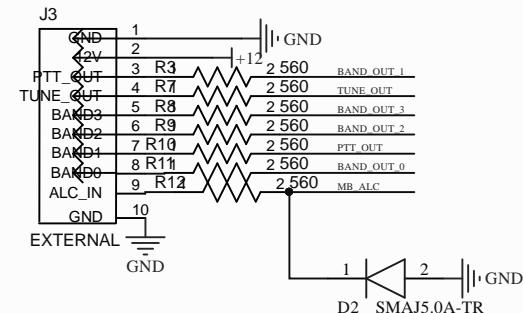
CONN



c

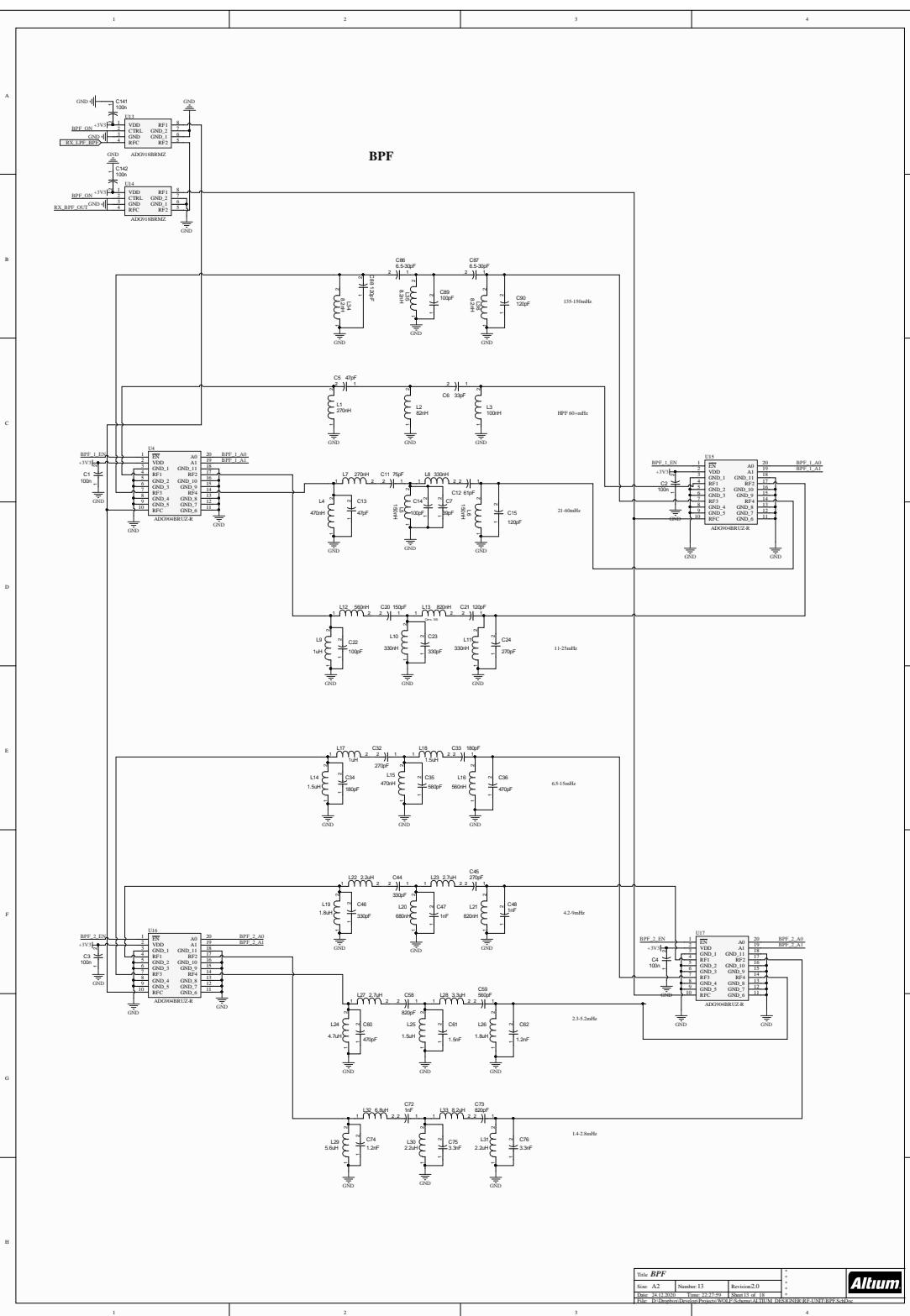
J4				
	23V	1	+3V ₃	
	GND	2		
RCLK		3	CTRL_RCLK	
CLK		4	CTRL_CLK	
OE		5	CTRL_OE	
DATA		6	CTRL_DATA	
SWR_F		7	SWR_DET_FORW	
SWR_B		8	SWR_DET_BACKW	
THERM		9	MB_THERM	
AIC		10	MB_AIC	

MOTHERBOARD

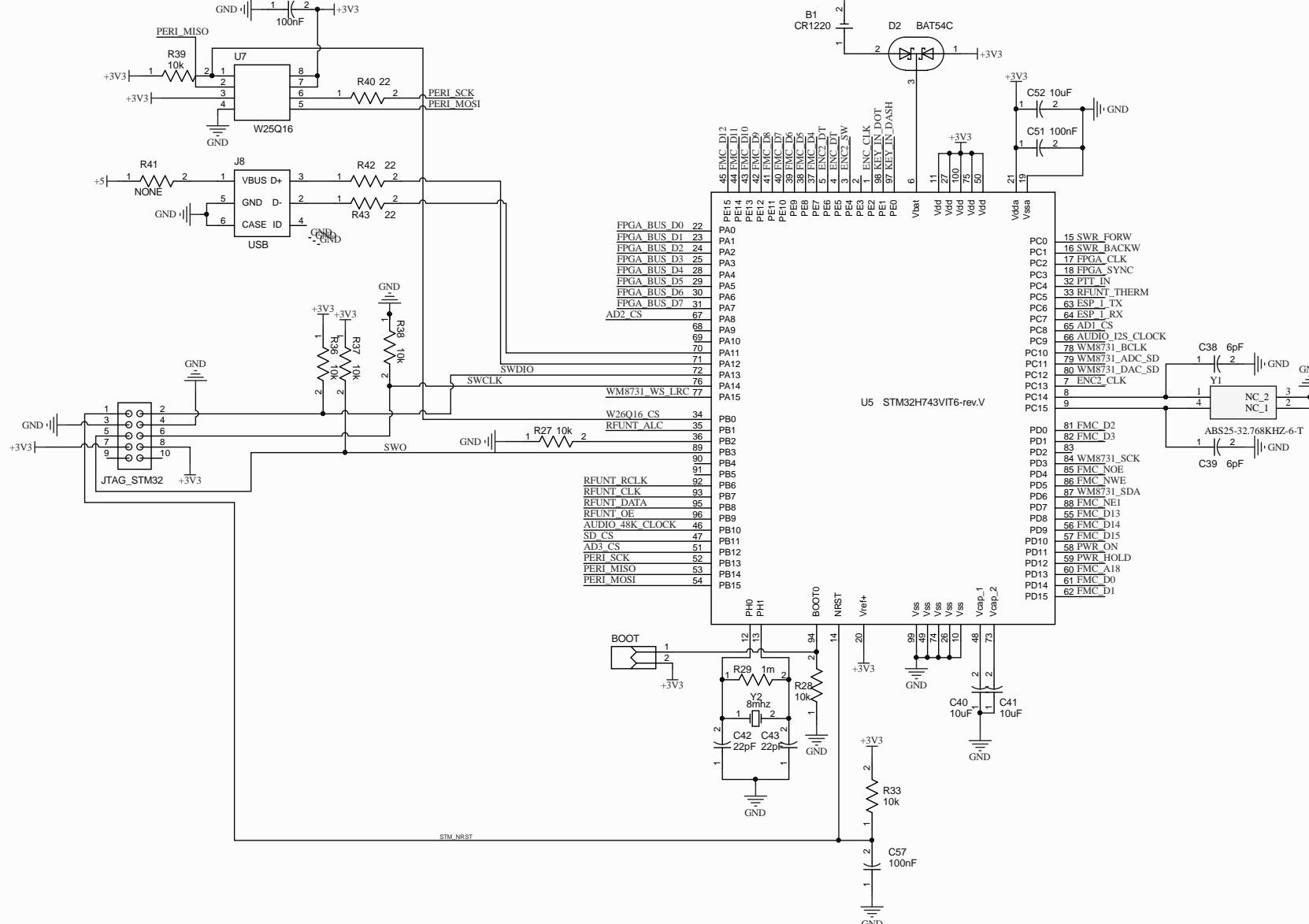


B

Title <i>CONN</i>	*		
Size: A4	Number:14		*
Revision2.0			*
Date: 24.12.2020	Time: 22:27:57		Sheet14 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM DESIGNER\RF-UNIT\CONN.SchDoc			



STM32

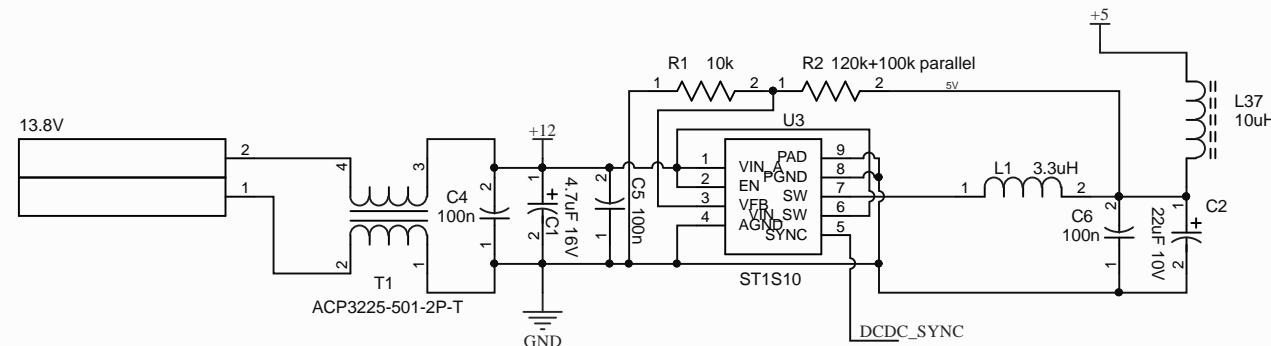


Title STM32

Size: A3	Number:11	Revision2.0
Date: 24.12.2020	Time: 22:28:02	Sheet 11 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM DES		

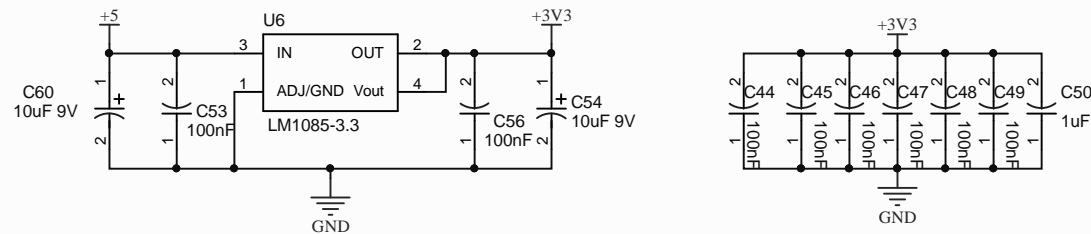
Altium

A



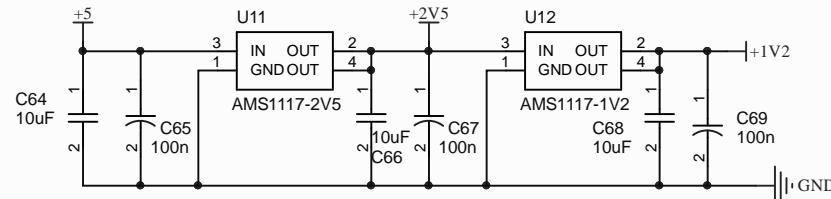
A

B



B

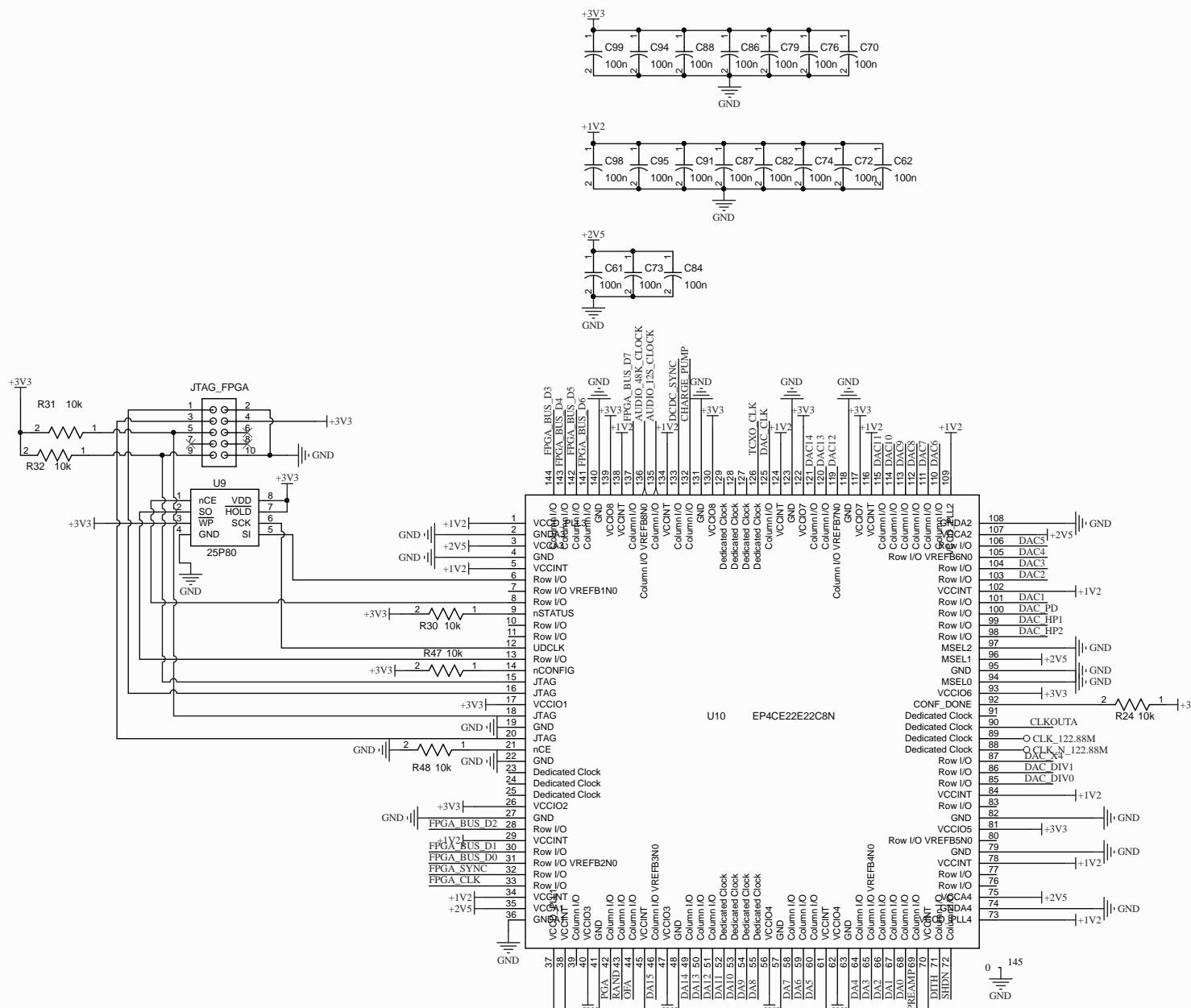
C



C

D

Title POWER			*
Size: A4	Number:10	Revision2.0	*
Date: 24.12.2020	Time: 22:28:04	Sheet10 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\POWER.SchDoc			*

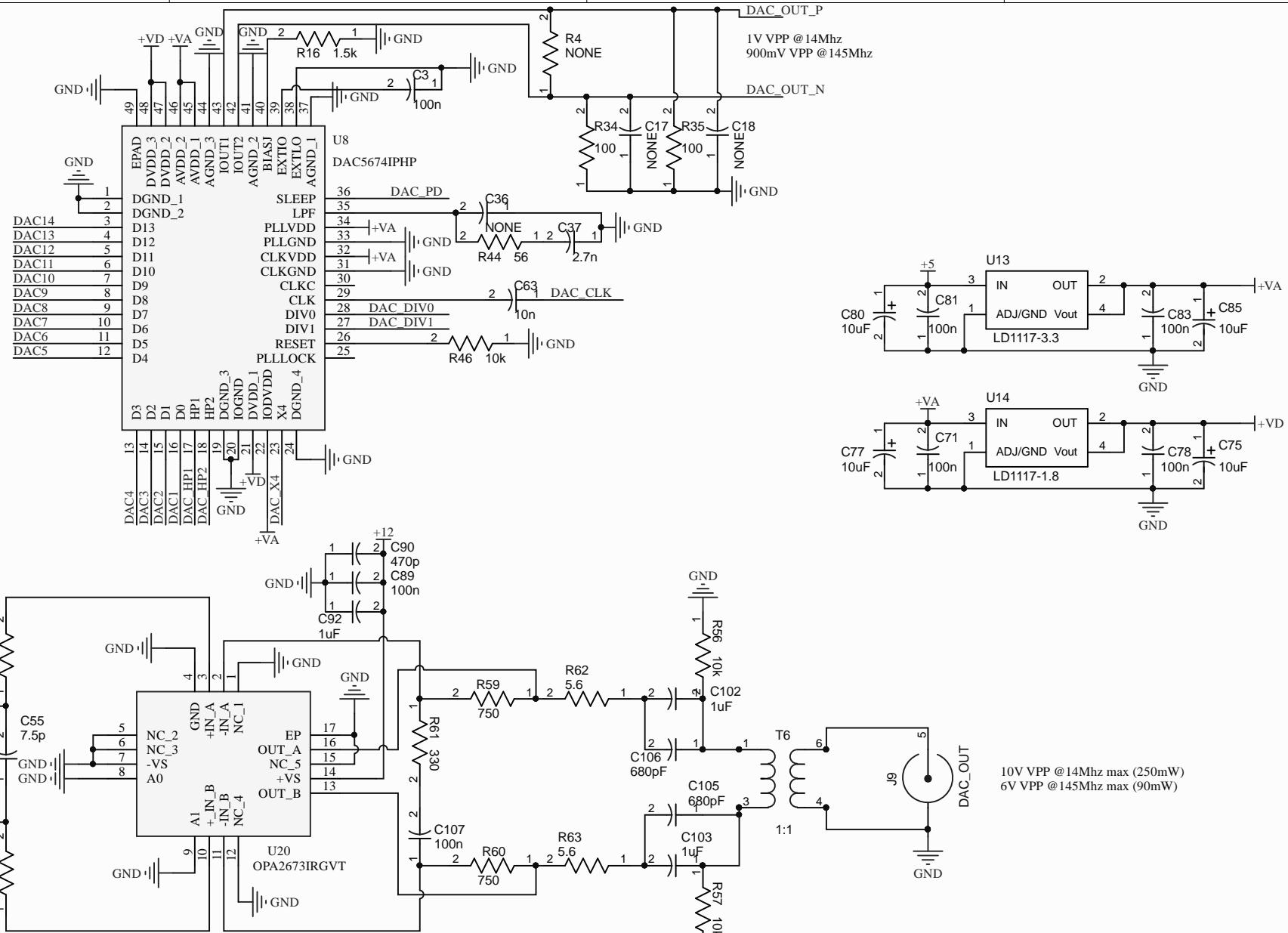


Title *FPGA*

Size: A3

Number:9 Revision:2.0
20 Time: 22:28:06 Sheet9 of 18

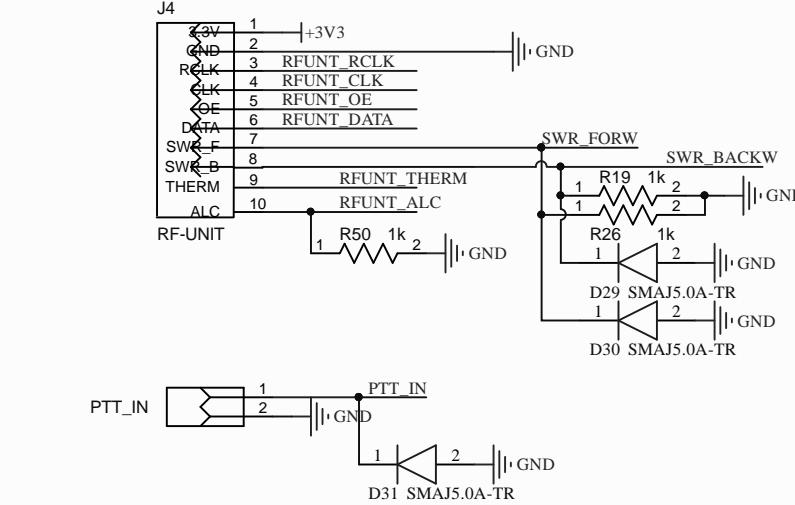
Altium



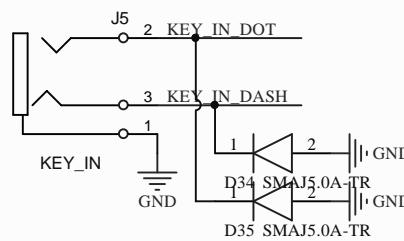
10V VPP @ 14Mhz max (250mW)
6V VPP @ 145Mhz max (90mW)

Title **DAC** *
 Size: A4 Number:8 Revision 2.0 *
 Date: 24.12.2020 Time: 22:28:09 Sheet 8 of 18 *
 File: D:\Dropbox\Developer\Projects\WOLF\Scheme\ALTIUM DESIGNER\MOTHERBOARD\DAC.SchDoc

A



B



C

J6

FMC_NE1	1	CS 3v3
FMC_A18	2	RS 3v3
FMC_NWE	3	WR GND
FMC_NOE	4	RD GND
STM_NRST	5	RSTMISO
FMC_D0	6	DB0MOSI
FMC_D1	7	DB1WRX
FMC_D2	8	DB2WTX
FMC_D3	9	DB3CLK
FMC_D4	10	DB\$DCS
FMC_D5	11	DB\$AD1C
FMC_D6	12	DB\$AD2C
FMC_D7	13	DB7E2A
FMC_D8	14	DB8E2B
FMC_D9	15	DB9E2S
FMC_D10	16	DB10E1A
FMC_D11	17	DB11E1B
FMC_D12	18	DB\$AD3C
FMC_D13	19	DB12E1D
FMC_D14	20	DB135v
FMC_D15	21	DB14GND
PWR_ON	22	DB15GND
PWR_HOLD	23	PONCAS
	24	PHOCAS
		3v3

FPC-45P-0.5mm

D

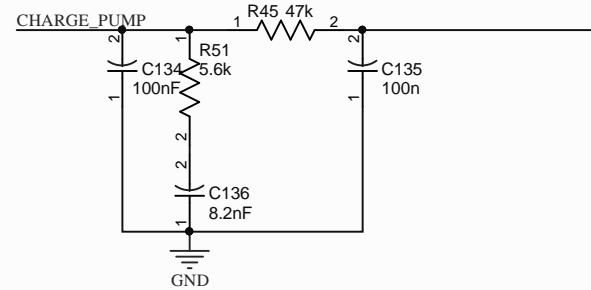
D

Title **CONNECTIONS**

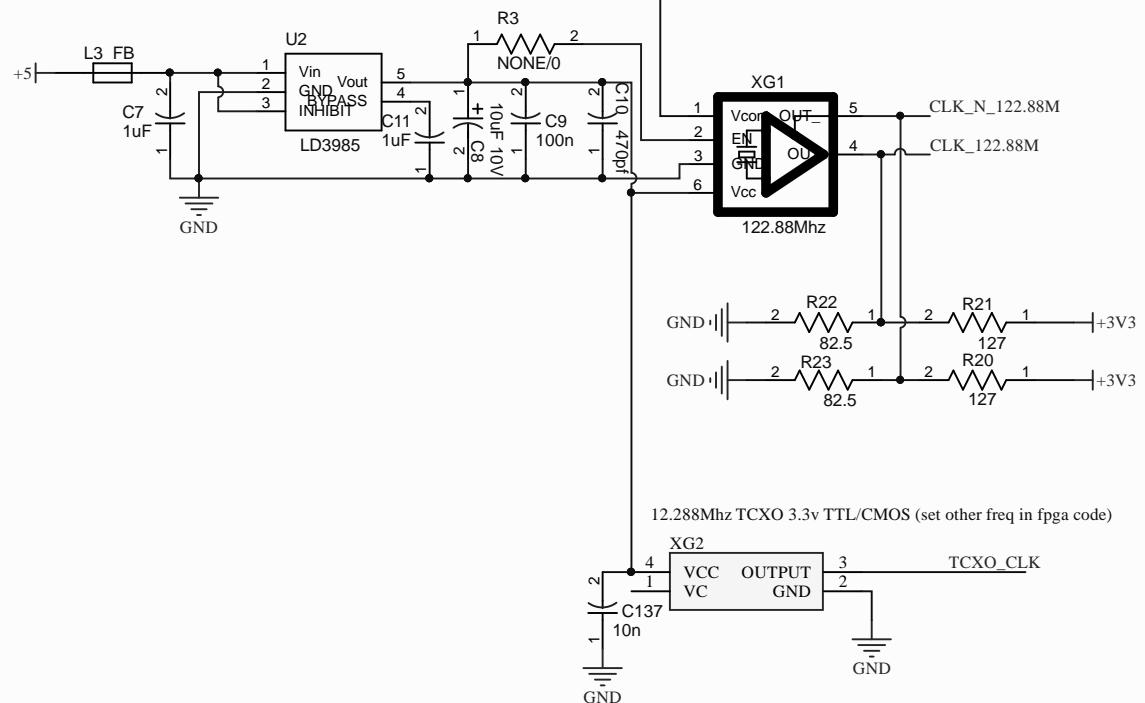
Size: A4	Number:7	Revision2.0
Date: 24.12.2020	Time: 22:28:11	Sheet7 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\CONNECTIONS.SchDoc		

Altium

A



B



C

D

Title **CLOCK_GENERATOR**

Size: A4 Number:6 Revision:2.0

Date: 24.12.2020 Time: 22:28:13 Sheet: 6 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\CLOCK_GENERATOR.SchDoc

Altium

A

B

C

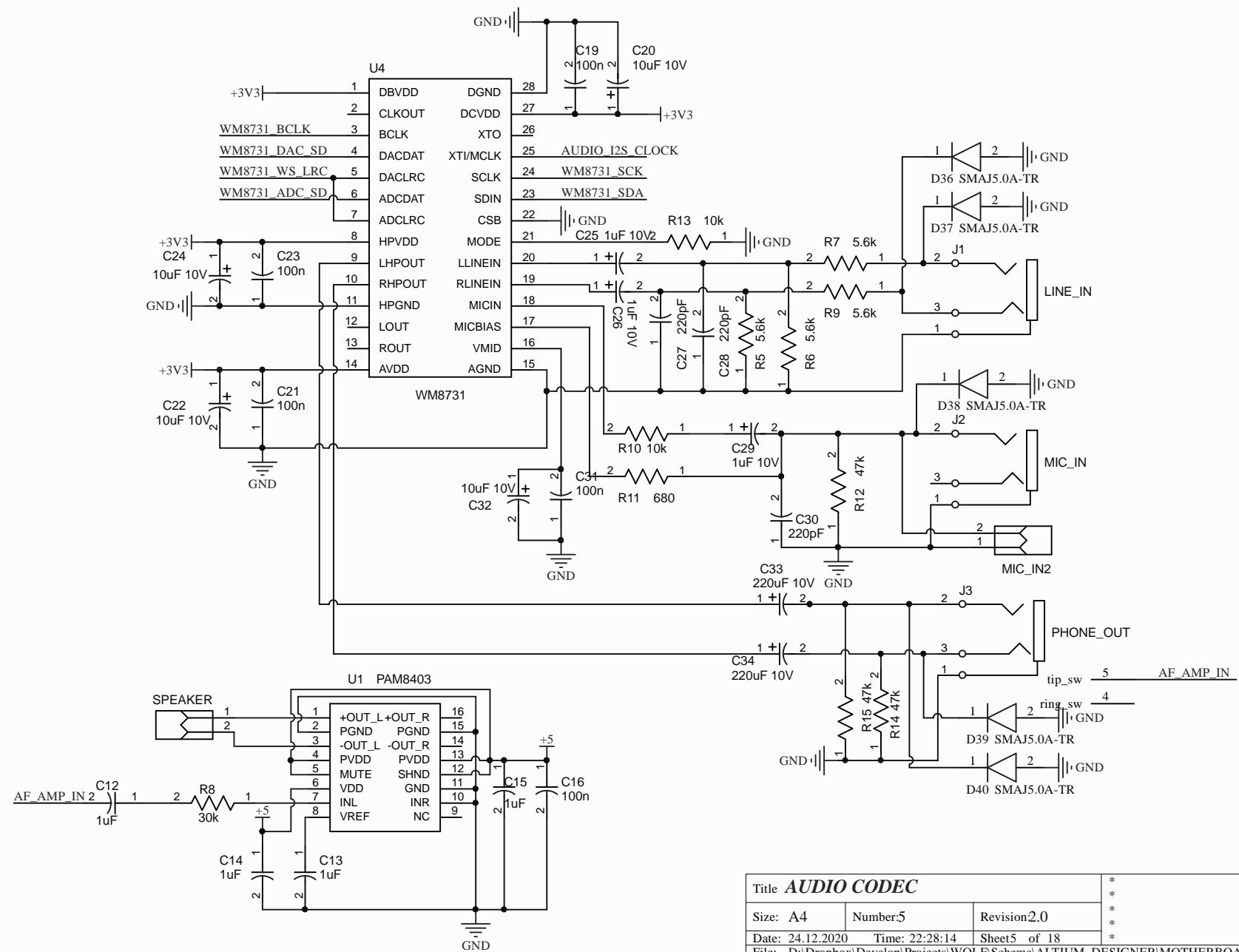
D

A

B

C

D

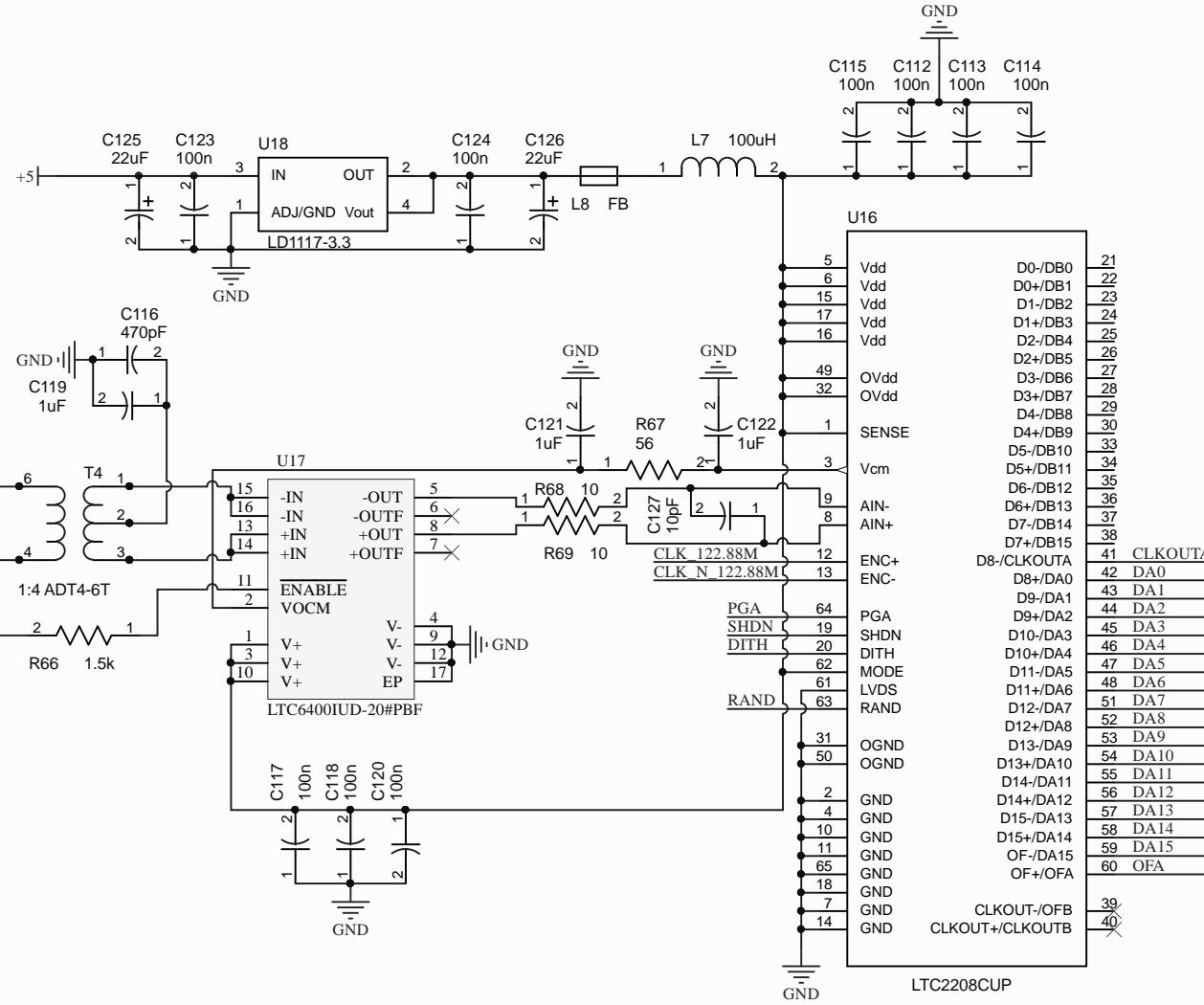
Title **AUDIO CODEC**

Size: A4 Number:5 Revision:2.0

Date: 24.12.2020 Time: 22:28:14 Sheet 5 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\AUDIO_CODEC.SchDoc

Altium

Title **ADC**

Size: A4	Number: 3	Revision 2.0
Date: 24.12.2020	Time: 22:28:16	Sheet 3 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\ADC.SchDoc		*

Altium

