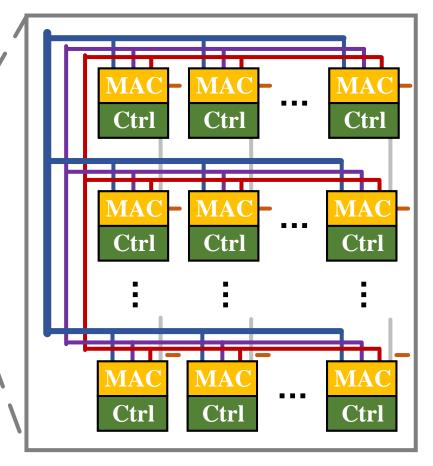
BB RAM Activation Weight RAM **RAM** Weight Dec. InOV Off-Weight DFN Chip DRAM PE0 PE3 PE4 PE5 PE6 Psum **ACCU**

Top Level Control

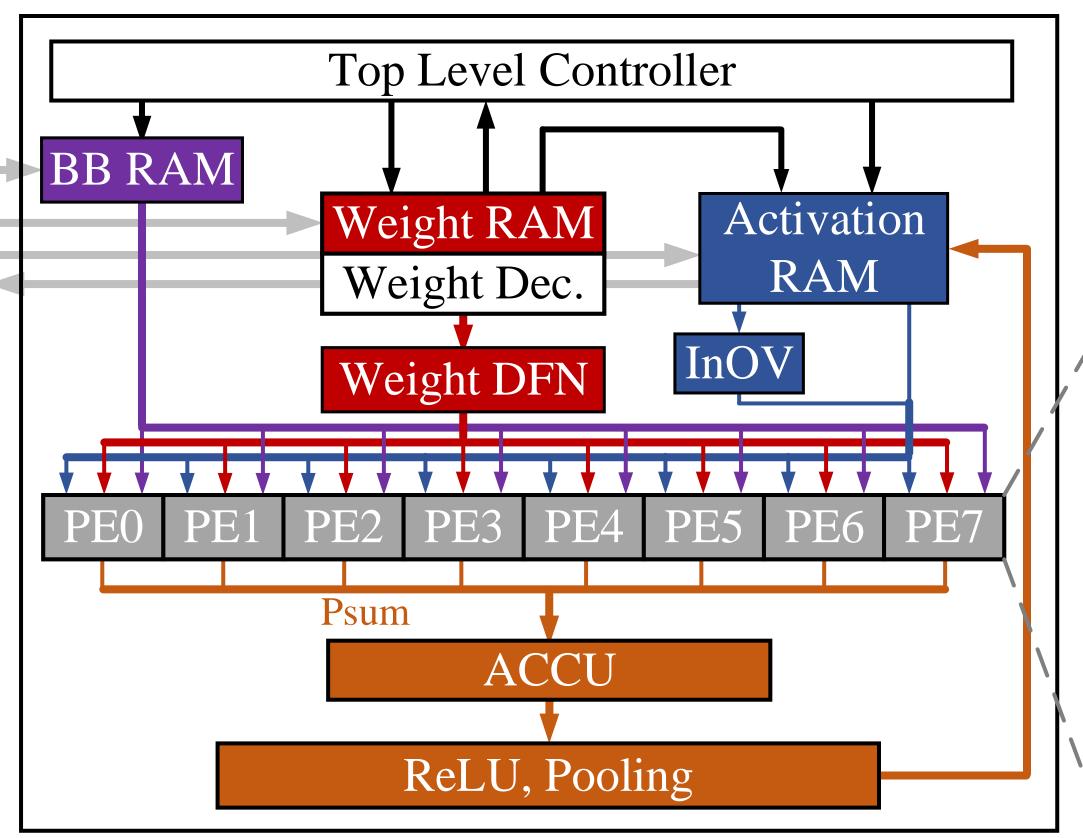
ReLU, Pooling

Processing Element



PE7

Off-Chip DRAM



Processing Element

