

















| Interface | | Pin |
|---------------------------|--|----------|
| EMAC | EMAC_MDC_out EMAC_MDL_in EMAC_MDO_out EMAC_CRS_out EMAC_COL_out | Any GPIO |
| I2C | I2CEXTO_SCL_in I2CEXTO_SDA_in I2CEXT1_SCL_in I2CEXT1_SDA_in I2CEXT0_SCL_out I2CEXT0_SDA_out I2CEXT1_SCL_out I2CEXT1_SCL_out | Any GPIO |
| General Purpose SPI | HSPIQ_in/_out HSPIQ_in/_out HSPIC_IN/_out HSPIC_CSO_in/_out HSPI_CSO_out HSPI_CS2_out VSPIQ_in/_out VSPIC_in/_out VSPIC_In/_out VSPIC_SO_in/_out VSPIC_SS_out VSPIC_SS_out | Any GPIO |

| | | Voltage of Intern | al LDO(VDD_SDIO) | | |
|--------------|-----------|---------------------|--|--|---|
| Pin | Default | 3.3V | 1.8V | | |
| GPI012/MTDI | Pull-Down | 0 | 1 | Dankska | ! D! |
| | | Bootin | g Mode | | pping Pins |
| Pin | Default | SPI Flash Boot | Download Boot | iii Sett | tings |
| GPI00 | Pull-Up | 1 | 0 | | |
| GPI02 | Pull-Down | Don't-care | 0 | | |
| | | Debugging Log on U | OTXD During Booting | | |
| Pin | Default | UOTXD Toggling | UOTXD Silent | | |
| GPI015/MTD0 | Pull-Up | 1 | 0 | | |
| | | Timing of | SDIO Slave | | |
| Pin | Default | Falling-edge:Output | Falling-edge:Input Rising-edge:Output | Rising—edge:Input Falling—edge:Output | Rising-edge:Input Rising-edge:Output |
| GPI015/MTD0 | Pull-Up | 0 | 0 | 1 | 1 |
| GPI05\PHY_PW | RPull-Up | 0 | 1 | 0 | 1 |

| Fiducials | | | | |
|-----------|----------|----------|--|--|
| FID1 | FID2 | FID3 | | |
| Fiducial | Fiducial | Fiducial | | |

| Internal | Bootstra | pping | Resistors |
|----------|-----------|--------|-----------|
| MTDI/ | GPI012: | Pul | l-Down |
| | GPI00: | Pul | l — U p |
| | GPI02: | Pul | l-Down |
| | GPI04: | Pul | l-Down |
| MTDO/0 | PI015: | Pul | l-Up |
| | GPI05\PHY | PWRPul | l-Up |

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| Sheet: / | |
| File: ESP32-GATEWAY_Rev_G.sch | |
| Title: ESP32-GATEWAY | |
| Size: A2 Date: 2019-12-17 | Rev: G |
| KiCad E.D.A. kicad 5.1.0-rc2-unknown-036be7d80ubuntu16.04.1 | ld: 1/1 |