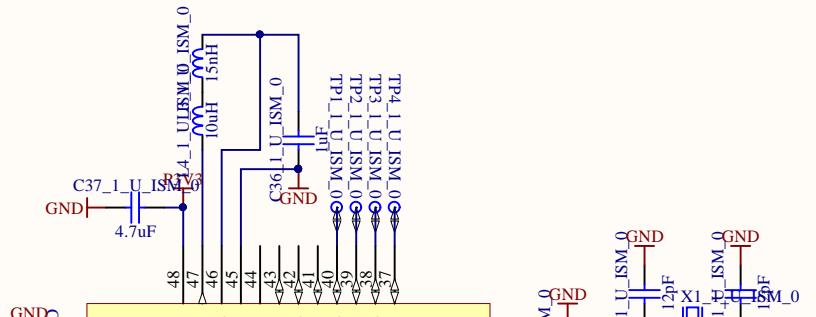
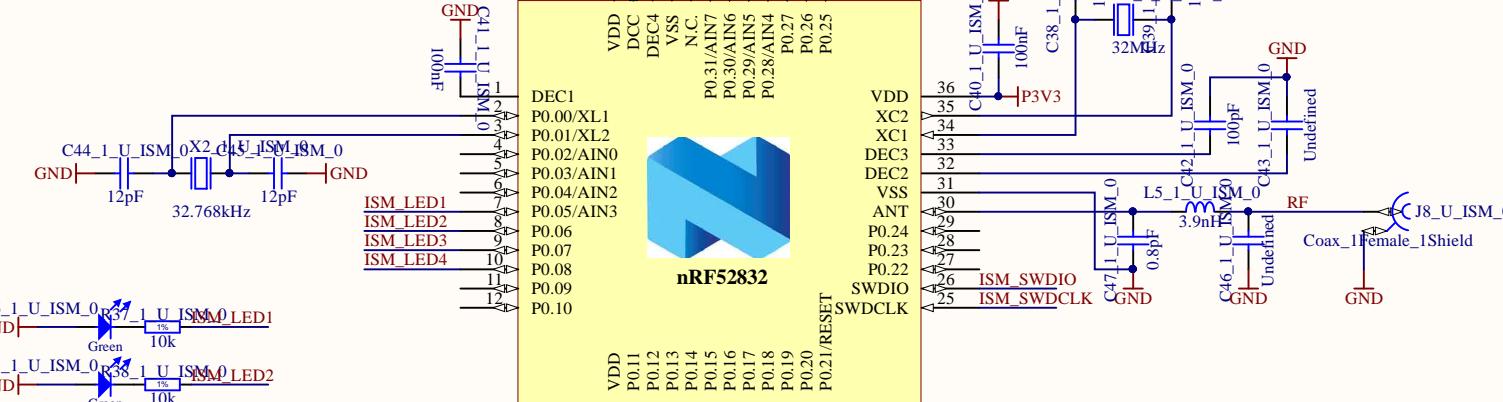


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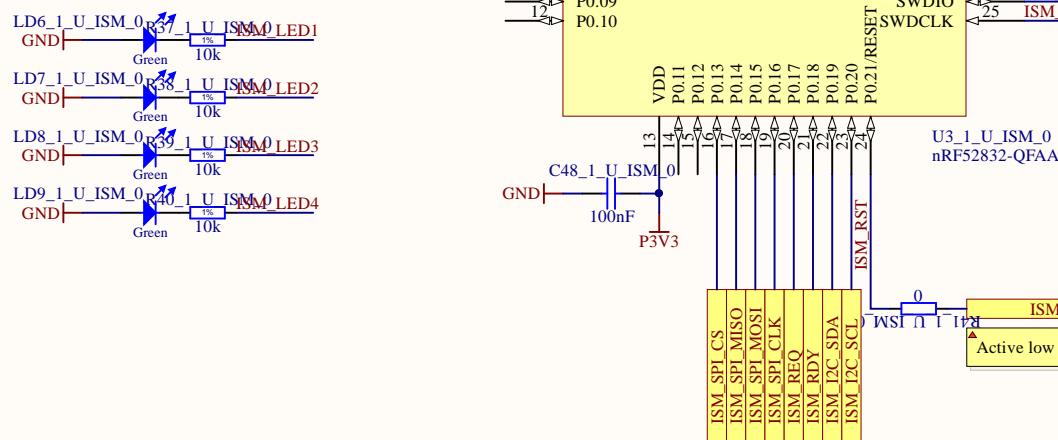
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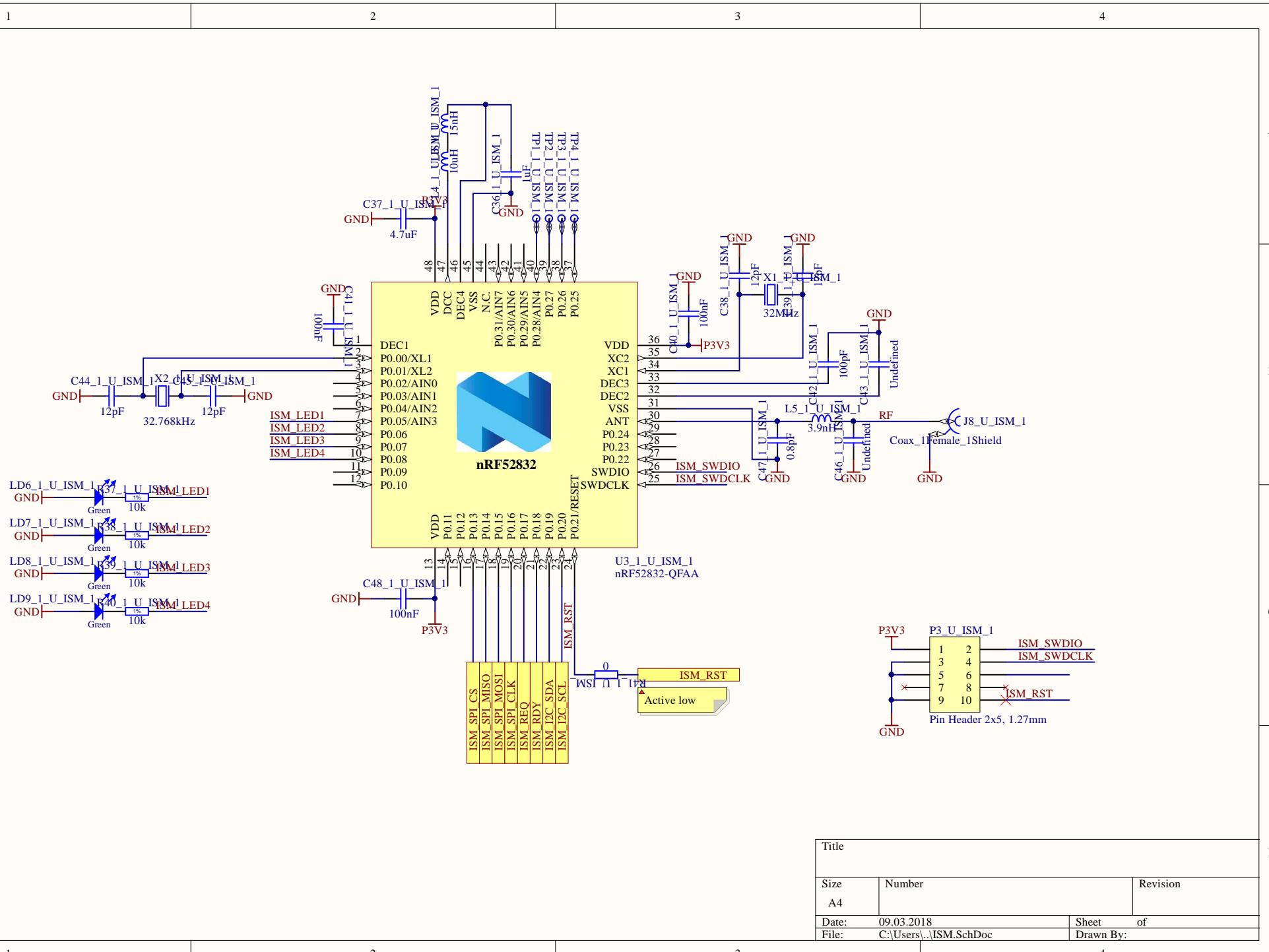
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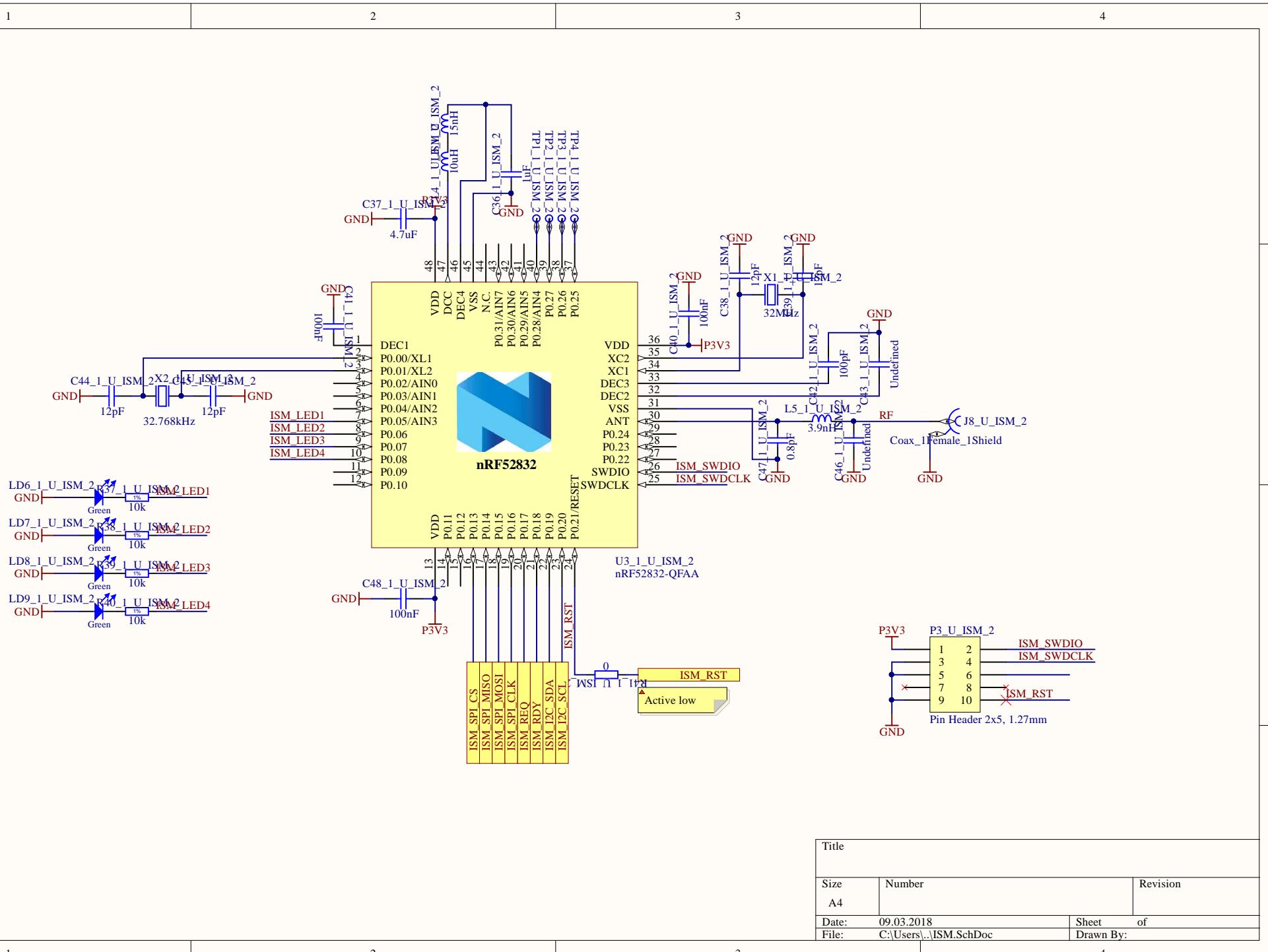
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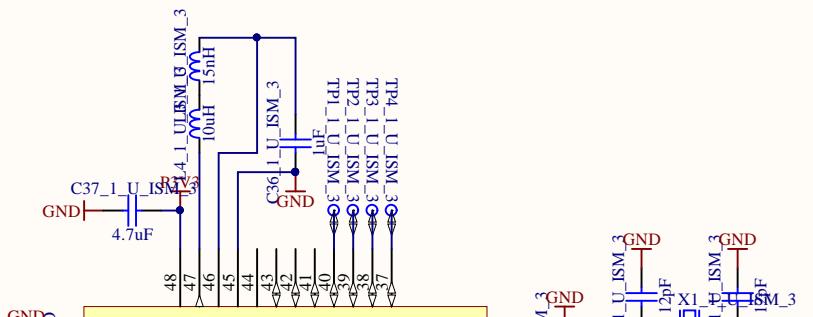
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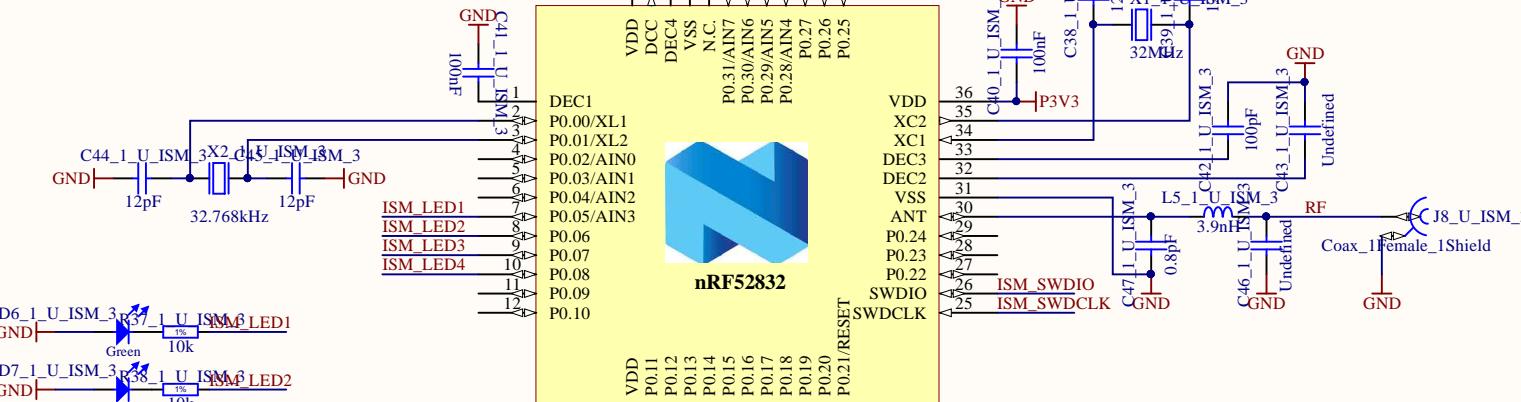




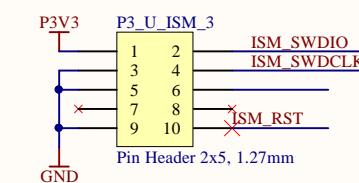
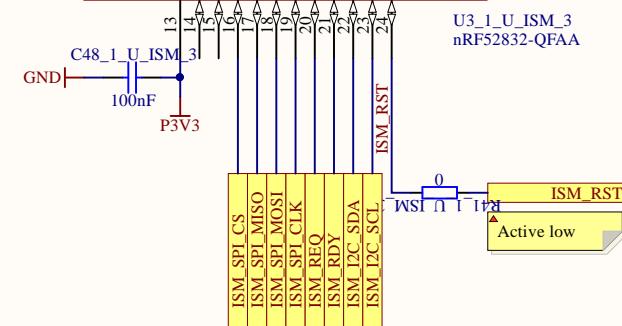
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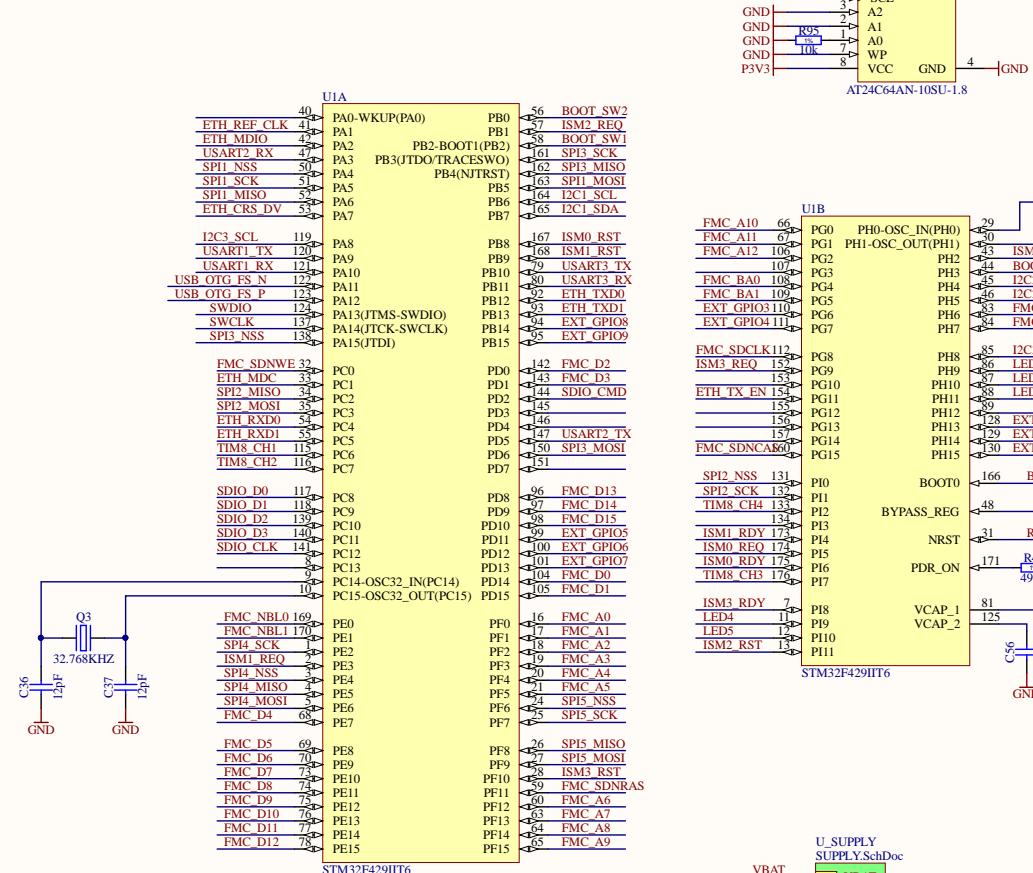
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SPI3_MOSI	ISM_SPL_MOSI
SPI3_SCK	ISM_SPL_CLK
ISMO_REQ	ISM_REQ
ISMO_RDY	ISM_RDY
ISMO_RST	ISM_RST
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I2C1_SDA	ISM_I2C_SDA
I2C1_SCL	ISM_I2C_SCL

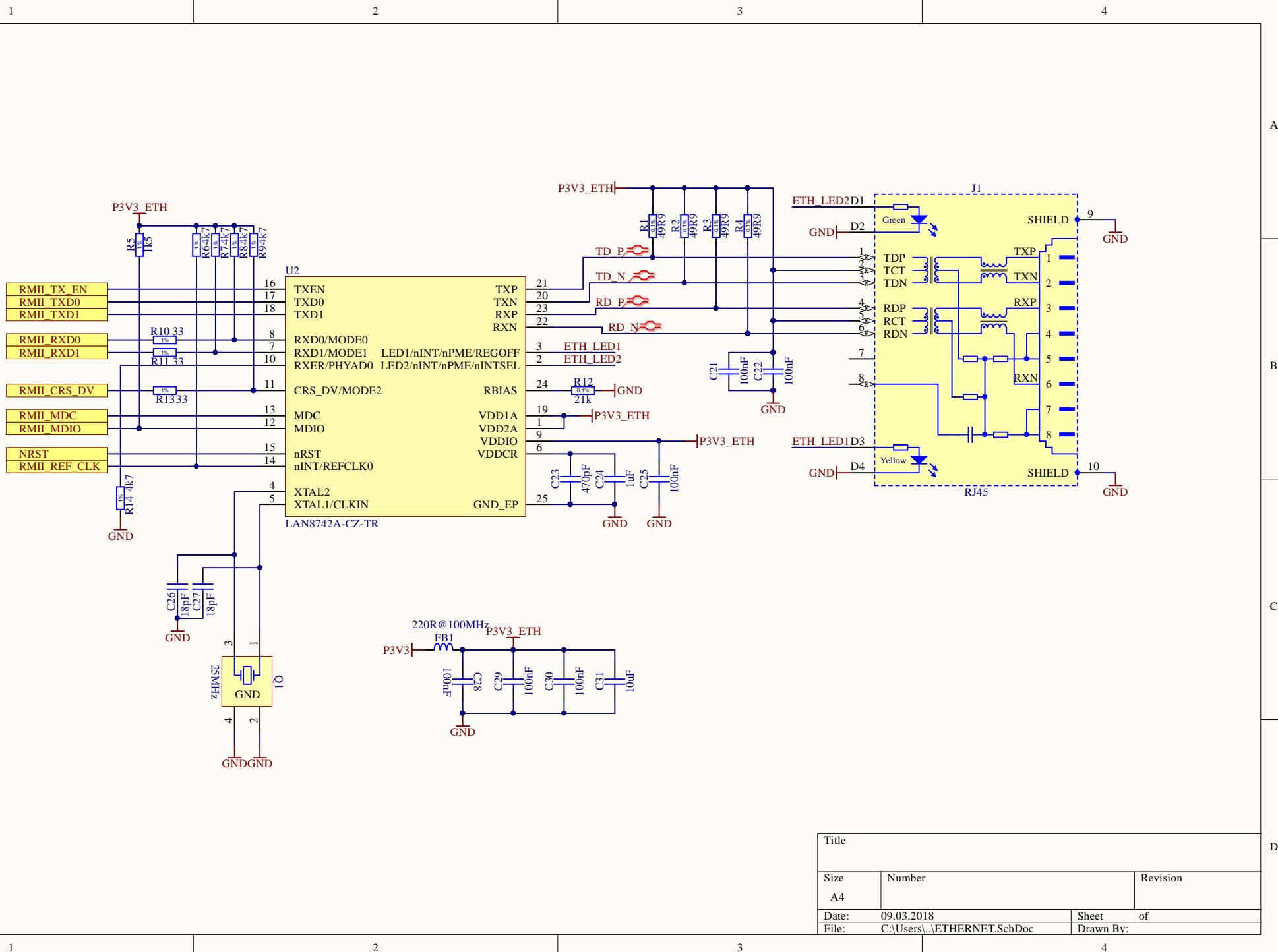
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SPI2_SCK	ISM_SPI_CLK
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ISMI_RDY	ISM_RDY
ISMI_RST	ISM_RST
SPI2_NSS	ISM_RST
I2C1_SDA	ISM_I2C_SDA
I2C1_SCL	ISM_I2C_SCL

U_ISM_2	ISM.SchDoc
SPI1_MISO	ISM_SPI_MISO
SPI1_MOSI	ISM_SPI_MOSI
SPI1_SCK	ISM_SPI_CLK
ISM2_REQ	ISM_REQ
ISM2_RDY	ISM_RDY
ISM2_RST	ISM_RST
SPI1_NSS	ISM_RST
I2C2_SDA	ISM_I2C_SDA
I2C2_SCL	ISM_I2C_SCL

U_ISM_3	ISM.SchDoc
SPI4_MISO	ISM_SPI_MISO
SPI4_MOSI	ISM_SPI_MOSI
SPI4_SCK	ISM_SPI_CLK
ISM3_REQ	ISM_REQ
ISM3_RDY	ISM_RDY
ISM3_RST	ISM_RST
SPI4_NSS	ISM_RST
I2C1_SDA	ISM_I2C_SDA
I2C1_SCL	ISM_I2C_SCL

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TIM8_CH2	NMOS1
TIM8_CH3	PMOS2
TIM8_CH4	NMOS2





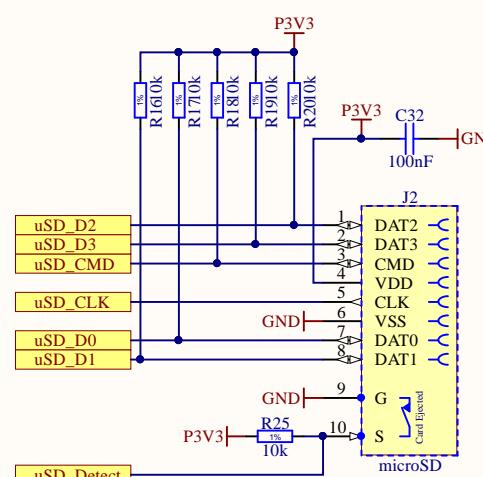
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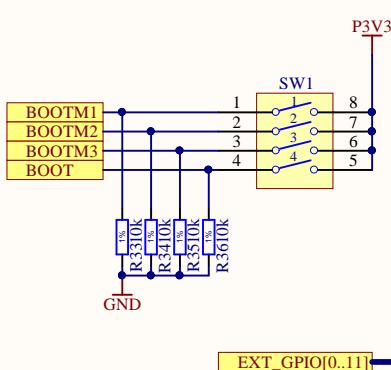
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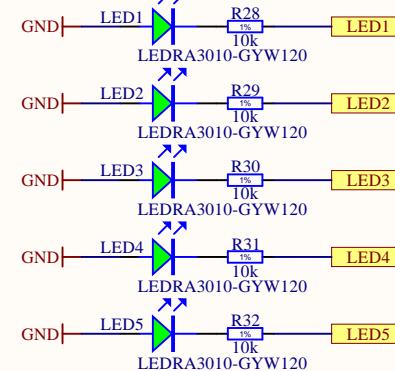
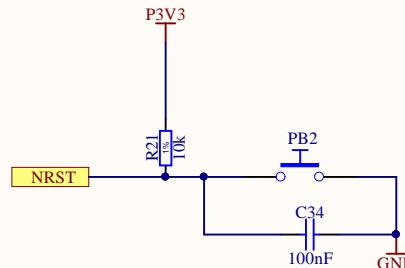
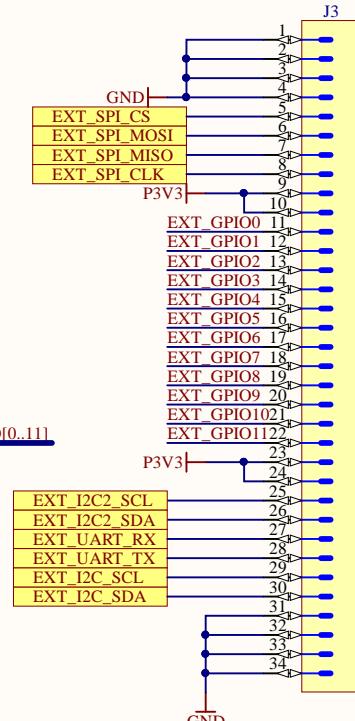
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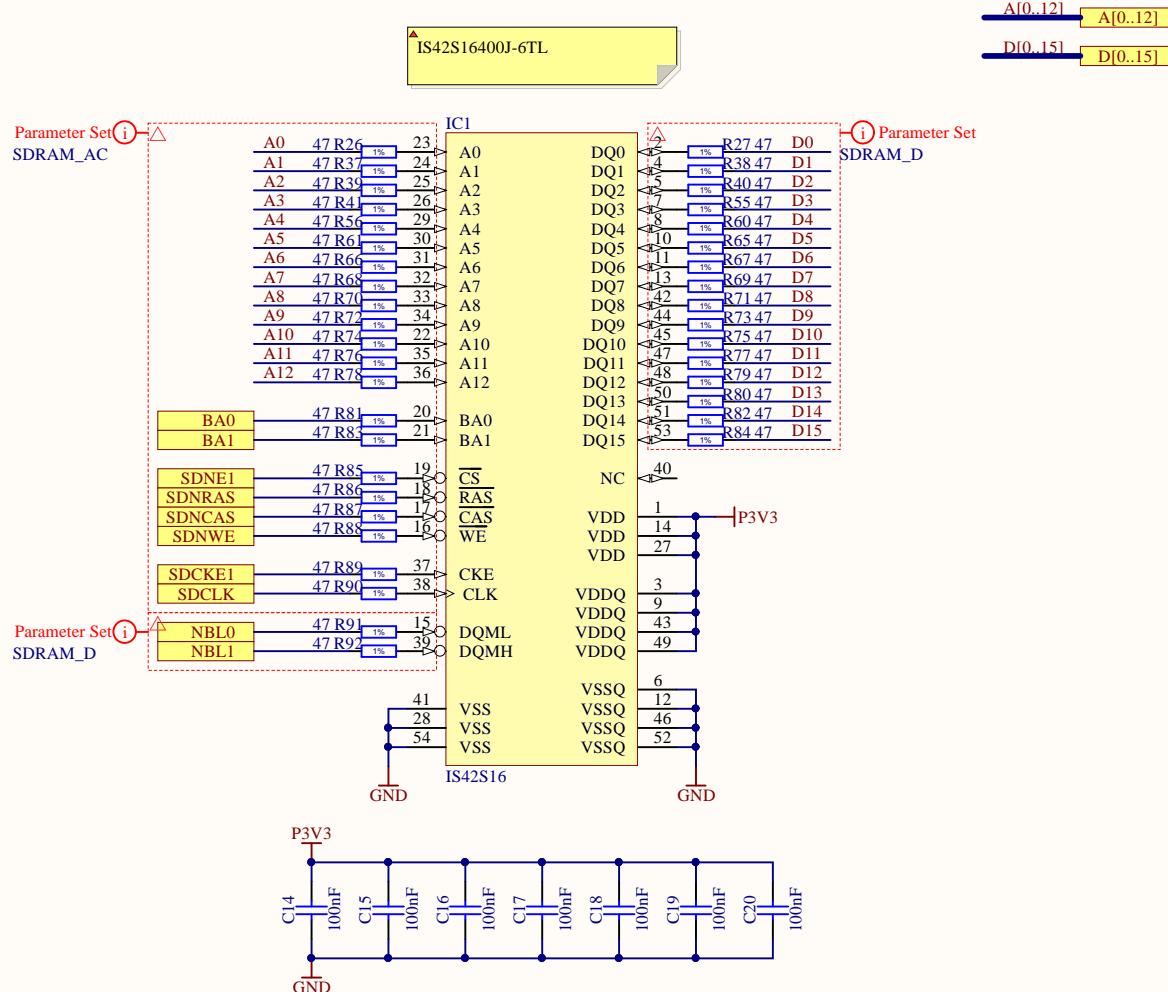
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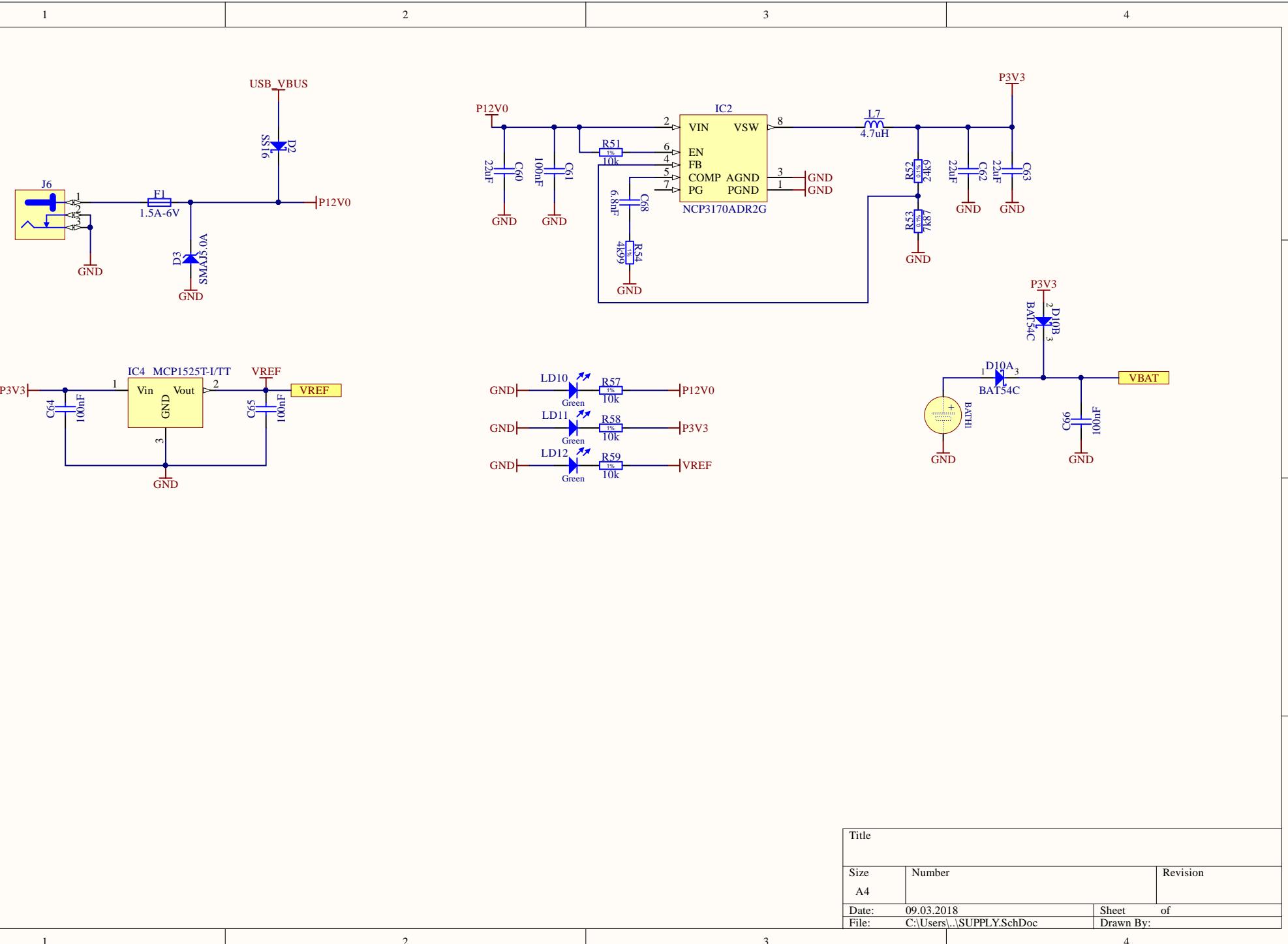
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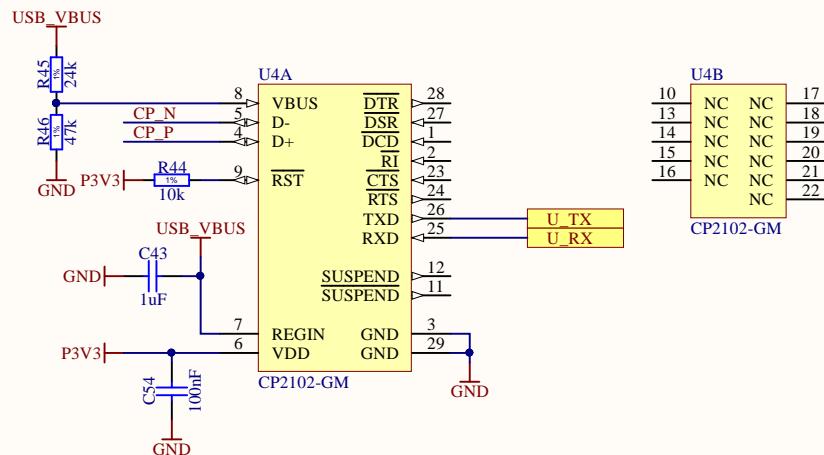
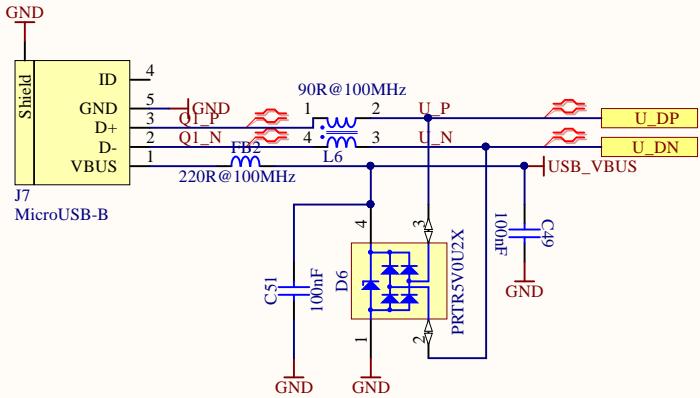
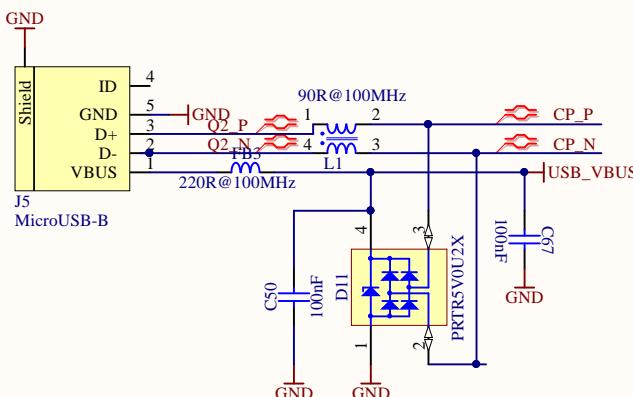
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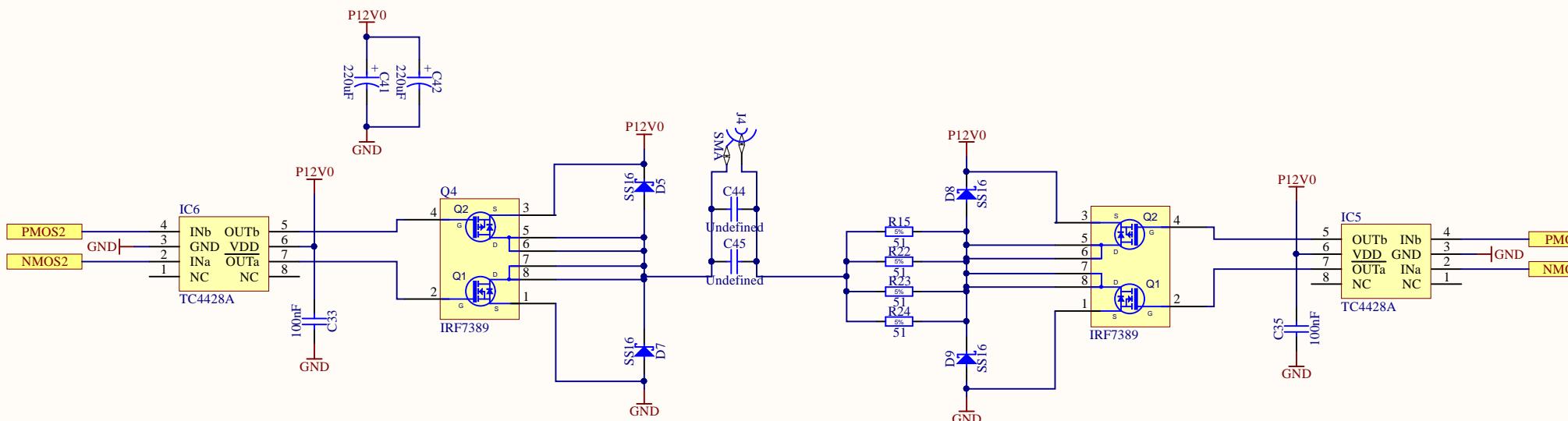
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OPTOGENETIC CONTROLLER v3.0
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