

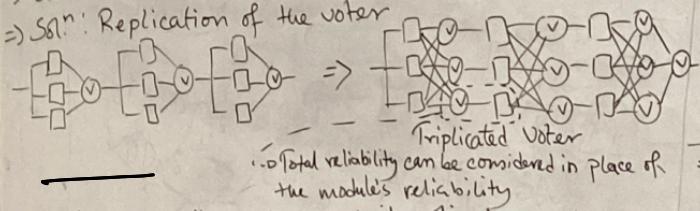
TMR System: $R_{sys} = (3R_m^2 - 2R_m^3)R_v$
 $= 3R_m^{-2\alpha t} - 2R_m^{-3\alpha t}$, assuming $R_v = 1$

$$MTTF_{TMR} = \int_0^\infty R_{sys}(t) dt = \left(\frac{5}{6}\right)^{\frac{1}{\alpha}}$$

K-out-of-n System: $R_{sys} = \left[\sum_{i=k}^n \binom{n}{i} R_m^i (1-R_m)^{n-i} \right] R_v$

\Rightarrow Bottleneck in the design of TMR: Voting ckt [Critical System Core]

\Rightarrow Soln: Replication of the voter



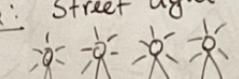
\Rightarrow Another issue with voting: Majority voting is a pessimistic approach. For example, in case of a binary-valued TMR module, if one module fails at stuck-at-1 state and another module fails at stuck-at-0 state, then the only module can provide correct output from the TMR system [Compensating failures].

\Rightarrow So far discussed: K-out-of-n: G_i System
 → Needs at least K modules to be functional
 → Similar to (n-k+1)-out-of-n: F System
 → Needs at least (n-k+1) failed modules for sys failure

\Rightarrow Consecutive k-out-of-n: G_i System

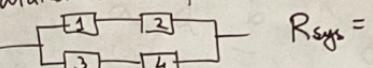
→ Consists of ordered modules, and good condⁿ of at least consecutive k modules keeps the sys functioning

Ex: Street light: (?)

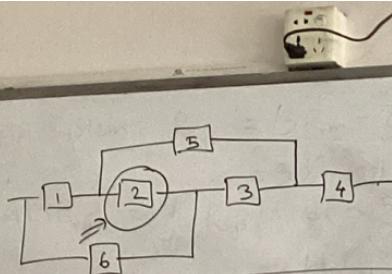


Reliability Block Diagram (RBD)

\Rightarrow Parallel connⁿ of Series System:



\Rightarrow Series connⁿ of parallel System:



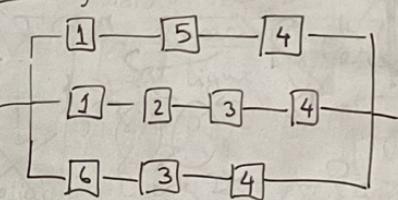
\Rightarrow Each module name labels one edge; module failure gives disconnected edge.
 [An edge labelled with "∞" is never disconnected]

\Rightarrow Analysis using success paths:

$$R_{sys} \leq 1 - \prod_i (1 - R_i)$$
 (1 - R_i is success path)

\Rightarrow Gives an upper bound on reliability, as it considers paths to be independent

Ex: for the prev. block diagram:



$$\text{So, } R_{sys} \leq 1 - (1 - R_1 R_5 R_4)$$

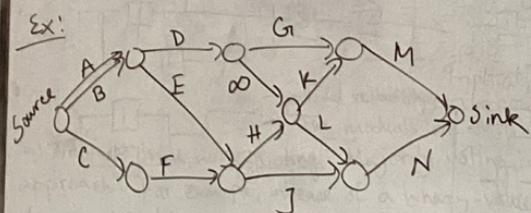
$$\times (1 - R_1 R_2 R_3 R_4)$$

$$\times (1 - R_6 R_3 R_4)$$

Reliability Graph:

\Rightarrow A schematic representation of system components, their interactions, and their roles in proper system opera?

\Rightarrow Use generalized Series parallel connⁿ's to visualize success paths, which are directed from a source to sink



Fault tree

- => Top-down approach of failure analysis.
- => Start from the top-level failure and then determine all possible ways to that top-level failure

Process:

Identify top-level failure => Identify 1st level contributors to the top-level failure



Identify 2nd level contributors ← Use logical gates to connect 1st level to top



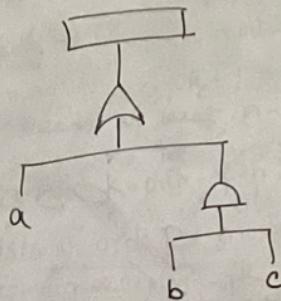
Link 2nd level to 1st level => Repeat until done

Cut set:

=> A set of initiators : Failures of all these initiators induce the top-level failure

=> Minimal Cut Set : A Cut Set containing no Cut Set on its subset.

Ex:



Minimal Cut Set: ?