

EECS 213

COMPUTER ARCHITECTURE

Instructor: Nader Bagherzadeh

Text: Computer Architecture: A Quantitative Approach,
By: *Hennessy and Patterson, 5th Edition*

References: Additional material available on the course website

Outline: Main Topics Covered:
Memory Systems
Microarchitecture
High Performance Computing (HPC)
Graphics Processing Unit (GPU)
System-on-Chip (SoC) and Network-on-Chip (NoC)

Prerequisite: EECS 112 and EECS 112L

Grade:

HWs/Assignments/Quizzes	30%
Midterm	35%
Final	35%

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