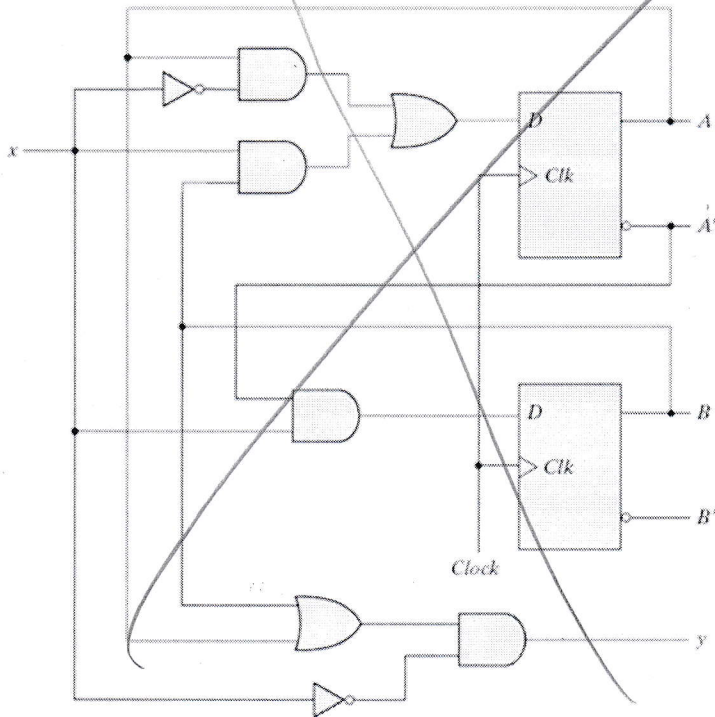


Oct. 15, 2019

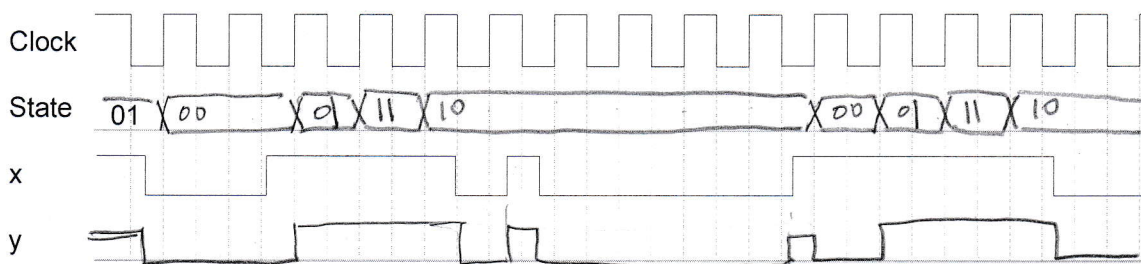
이름:

학번:

4



3 c.



4.

module 2to4\_decoder (a,b,d);  
output [3:0] d;  
input a, b;  
wire a, b;  
d(3) = a & b,  
d(2) = a & ~b,  
d(0) = ~a & ~b,  
d(1) = ~a & b;  
end module;

$\{ \begin{matrix} () \rightarrow a [ ] \\ \rightarrow 5 \\ \text{or } \frac{0}{12} \text{ on assign} \end{matrix} \}$

2222

assign d[3] = ... ,  
d[2]  
d[0]  
d[1] = ... ;

불러옴