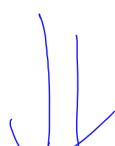
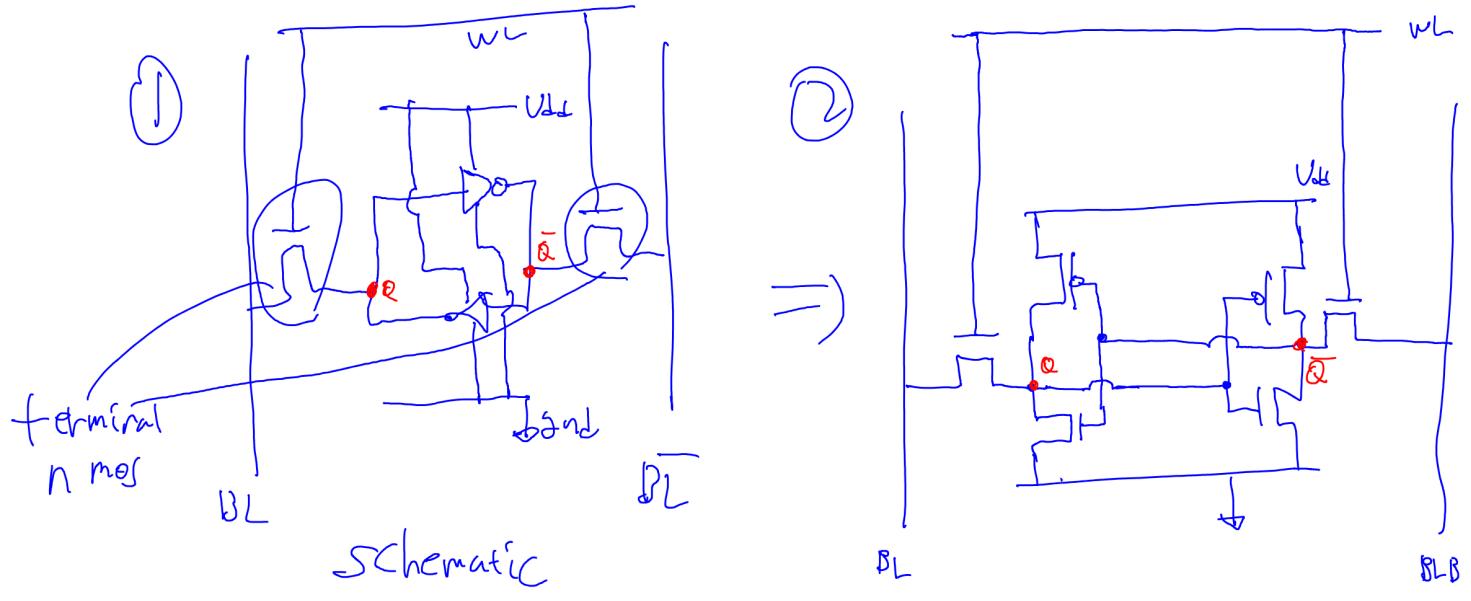
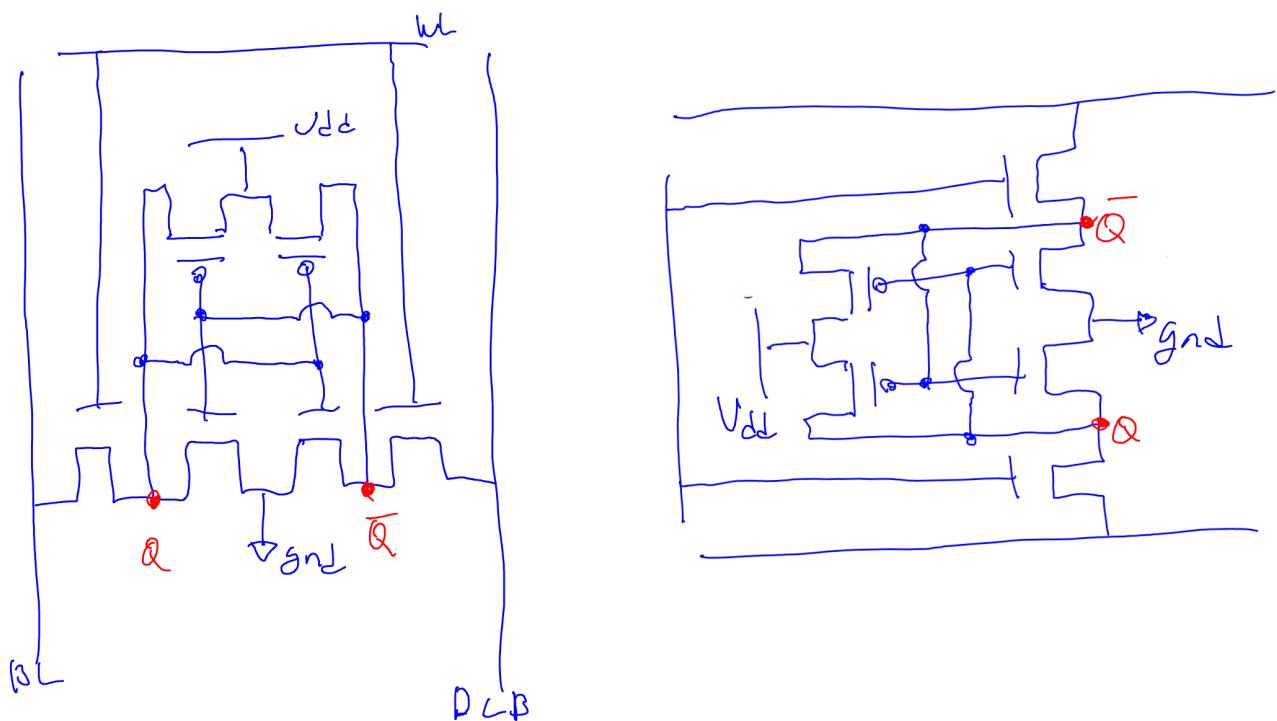


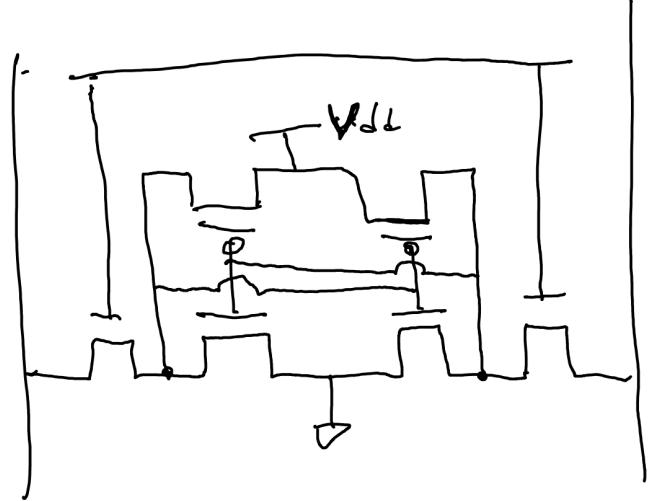
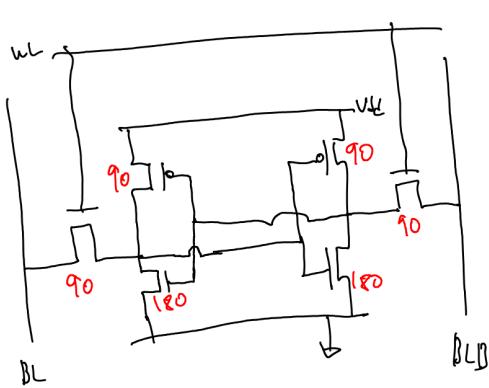
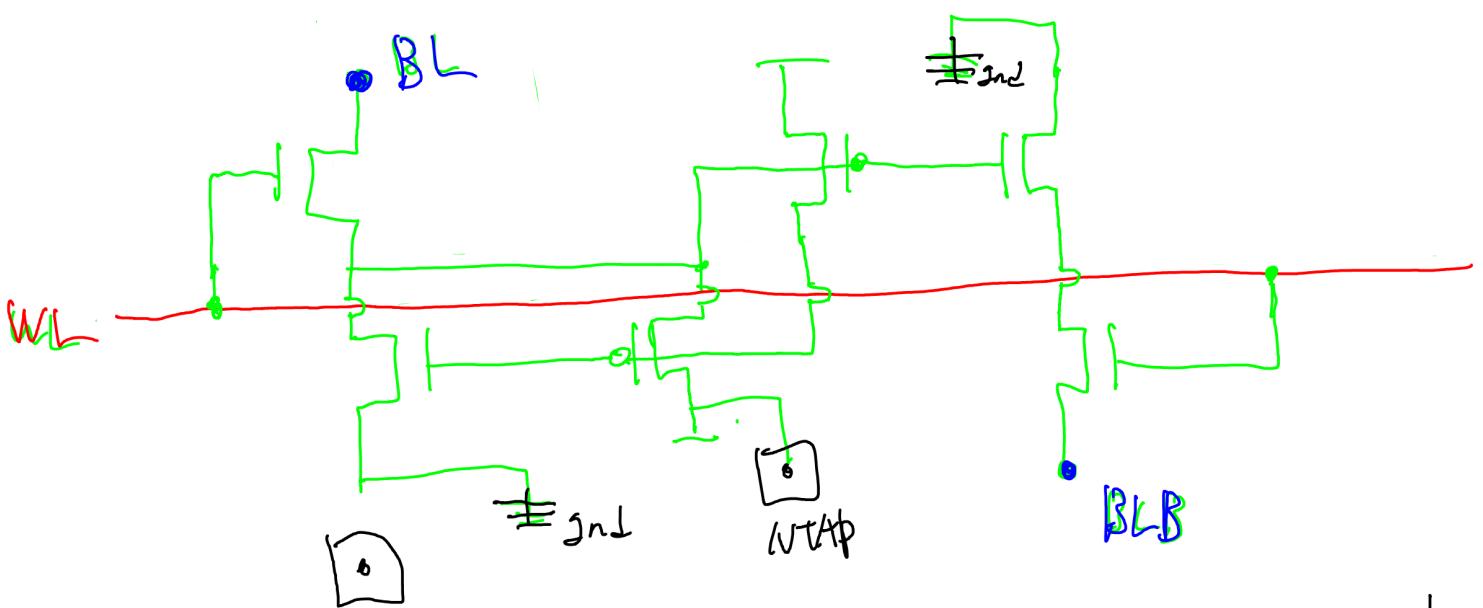
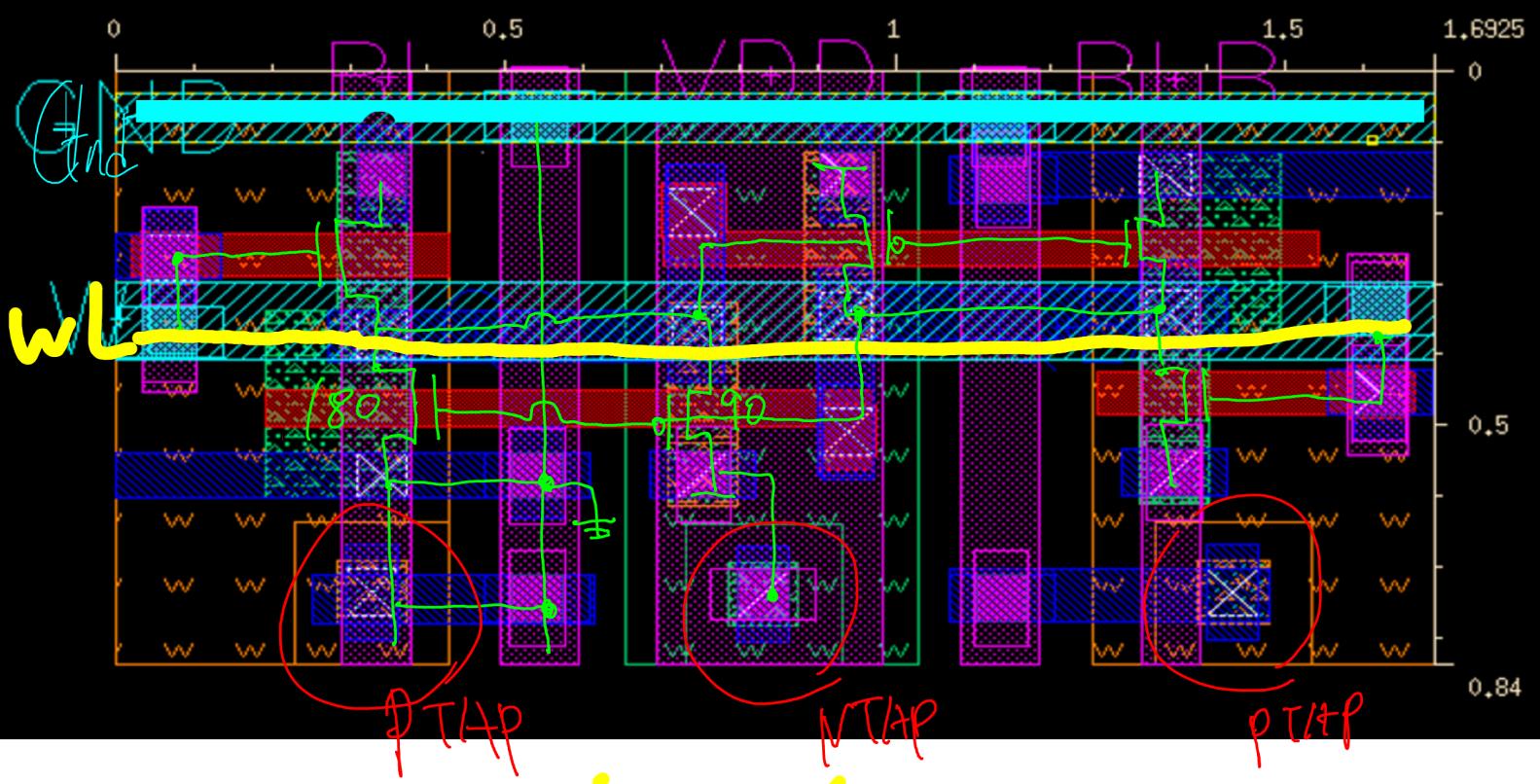
SRAM structure





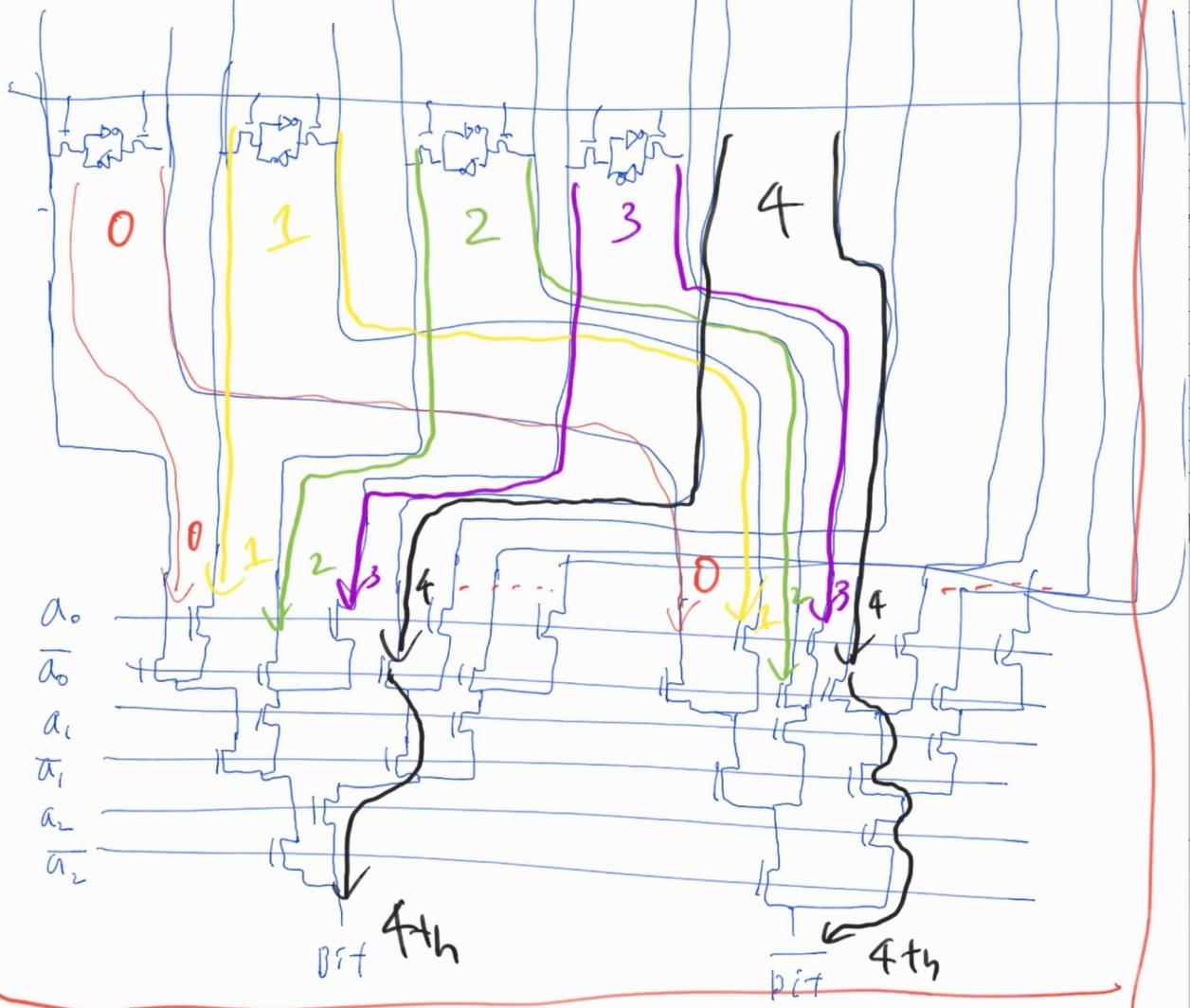
Stick diagram of  $\Sigma(1)$  cell





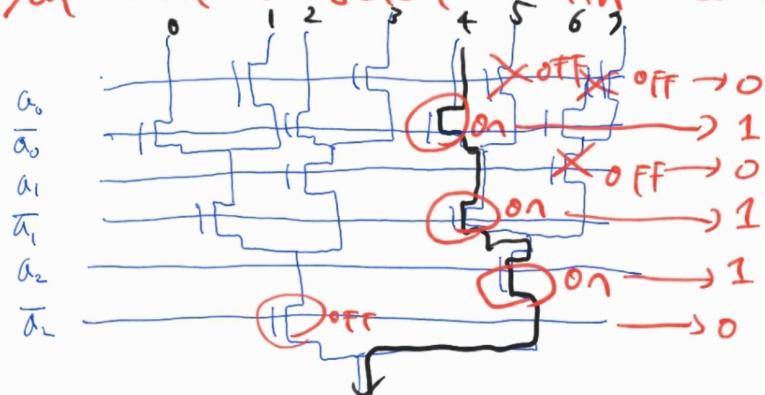
저장

# Tree decoder Mux 2진수!



$\therefore$  if you want to select 4th column

4th



$$\therefore a_0, a_1, a_2 = 001$$

$$\bar{a}_0, \bar{a}_1, \bar{a}_2 = 110$$

$$\therefore 4_{(10)} = 100_{(2)} = a_2 a_1 a_0$$

~~설계~~

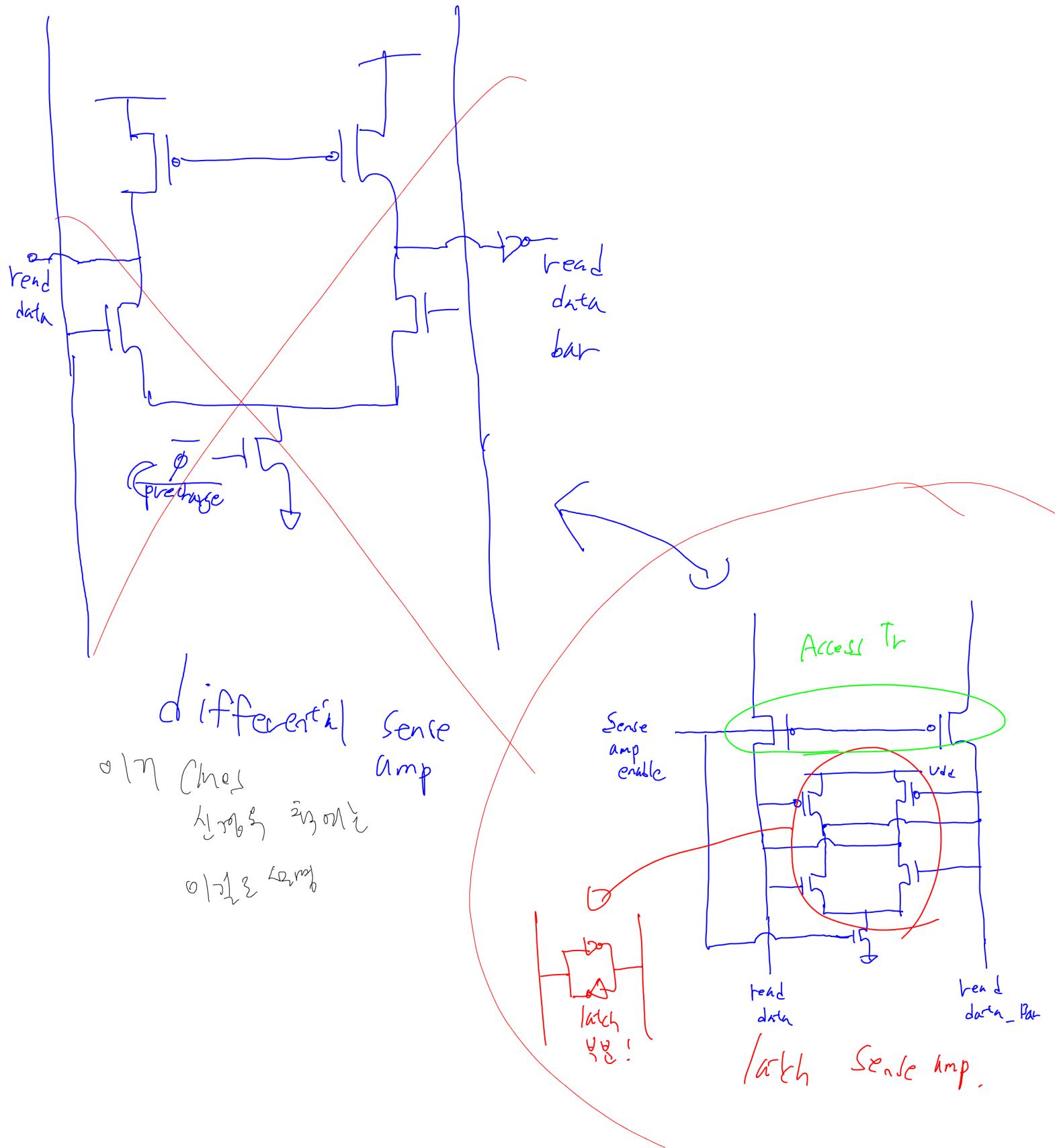
SRAM  $\times 1$  column

alug

differential Sense Amp (비교 증폭기)

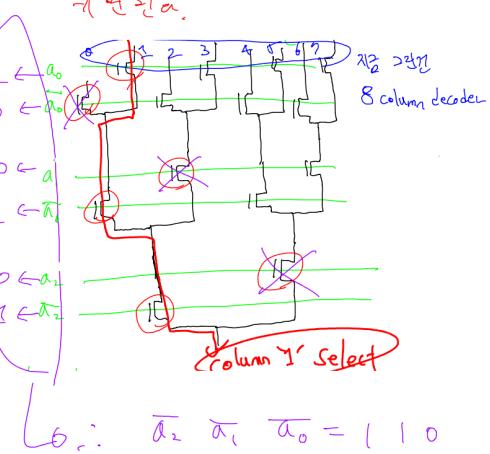
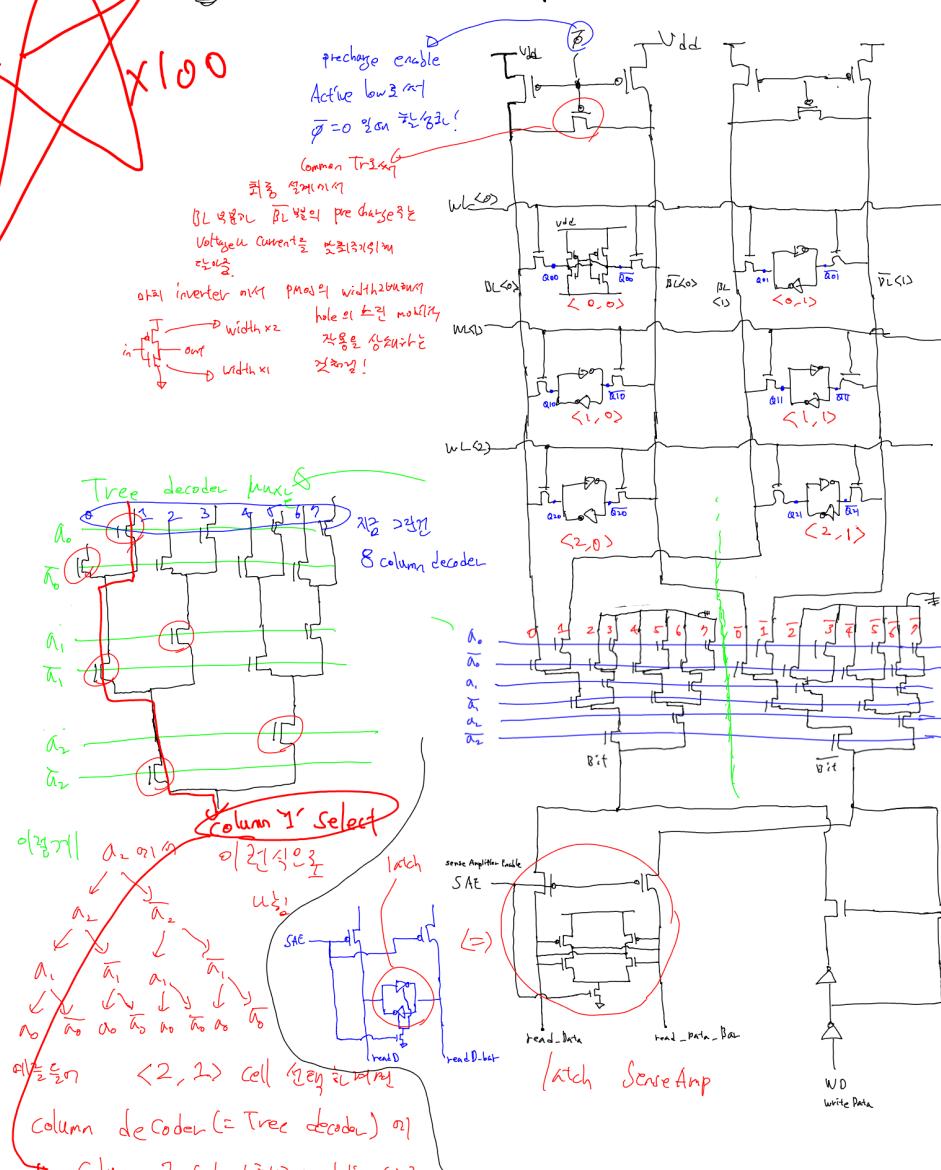
기본구조

latch Sense Amp 구조!



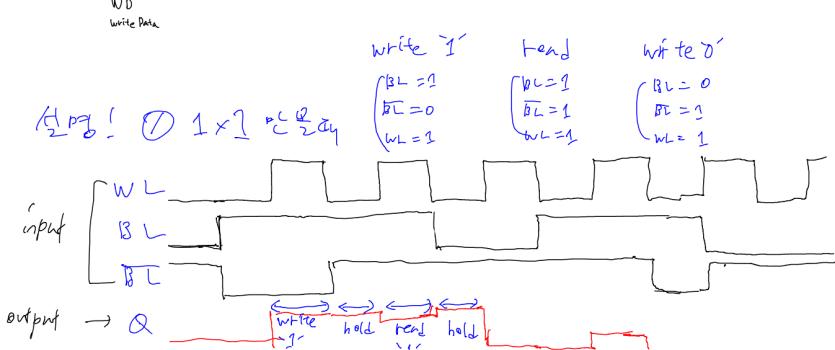
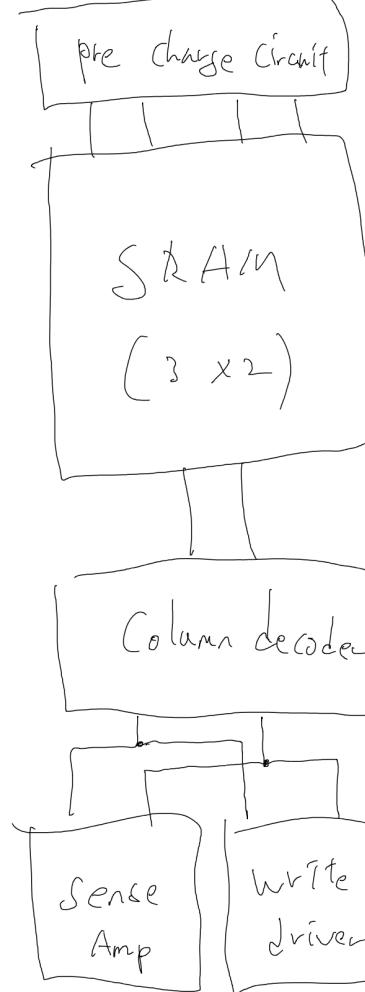
# 3x2 SRAM Design

$\times 100$



∴ When select Column '1'  
we have to assign  $(0 \ 0 \ 1)$  to  $(a_2, a_1, a_0)$   
and when select row '2'  
we have to turn on word line <2>

"read"



② peripheral circuit

Cij Write