

# Lecture 5: Procedure Calls

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- Today's topics:
  - Procedure calls and register saving conventions

# Registers

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- The 32 MIPS registers are partitioned as follows:
  - Register 0 : \$zero      always stores the constant 0
  - Regs 2-3 : \$v0, \$v1      return values of a procedure
  - Regs 4-7 : \$a0-\$a3      input arguments to a procedure
  - Regs 8-15 : \$t0-\$t7      temporaries
  - Regs 16-23: \$s0-\$s7      variables
  - Regs 24-25: \$t8-\$t9      more temporaries
  - Reg 28 : \$gp      global pointer
  - Reg 29 : \$sp      stack pointer
  - Reg 30 : \$fp      frame pointer
  - Reg 31 : \$ra      return address

# Procedures

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- Each procedure (function, subroutine) maintains a scratchpad of register values.

- When another procedure is called (the callee), the new procedure takes over the scratchpad

- Values in the register file may have to be saved so we can safely return them to the caller

1. Parameters (arguments) are placed where the callee can see them
2. Control is transferred to the callee
3. Acquire storage resources for the callee
4. Execute the procedure
5. Place result value where **the caller** can access it
6. Return control to caller

# Jump-and-Link

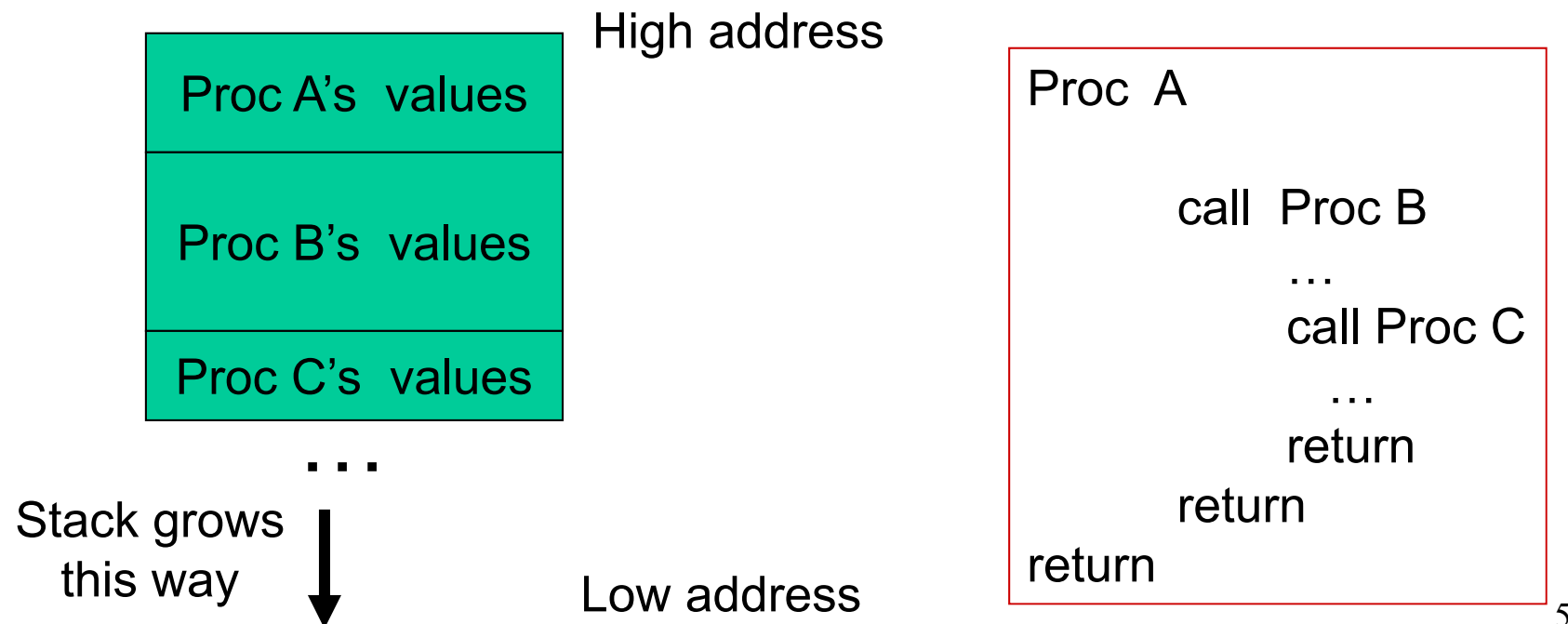
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- A special register (storage not part of the register file) maintains the address of the instruction currently being executed – this is the program counter (PC)
- The procedure (function) call is executed by invoking the **jump-and-link (jal)** instruction – the current PC (actually, PC+4) is saved in the register **\$ra** and we jump to the procedure's address (the PC is accordingly set to this address)  
`jal   NewProcedureAddress`
- Since jal may over-write a relevant value in \$ra, it must be saved somewhere (in memory?) before invoking the jal instruction
- How do we return the control back to the caller after completing the callee procedure?

# The Stack

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The register scratchpad for a procedure seems volatile – it seems to disappear every time we switch procedures – a procedure's values are therefore backed up in memory on a stack



# Storage Management on a Call/Return

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- A new procedure must create space for all its variables on the stack
- Before/after executing the jal, the caller/callee must save relevant values in \$s0-\$s7, \$a0-\$a3, \$ra, temps into the stack space
- Arguments are copied into \$a0-\$a3; the jal is executed
- After the callee creates stack space, it updates the value of \$sp
- Once the callee finishes, it copies the return value into \$v0, frees up stack space, and \$sp is incremented
- On return, the caller/callee brings in stack values, ra, temps into registers
- The responsibility for copies between stack and registers may fall upon either the caller or the callee

## Example 1 (pg. 98)

```
int leaf_example (int g, int h, int i, int j)
{
    int f ;
    f = (g + h) - (i + j);
    return f;
}
```

Notes:

Assume g,h,i, and j are in \$a0~3.

In this example, the callee took care of saving the registers it needs.

The caller took care of saving its \$ra and \$a0-\$a3.

```
leaf_example:
    addi    $sp, $sp, -12
    sw      $t1, 8($sp)
    sw      $t0, 4($sp)
    sw      $s0, 0($sp)
    add     $t0, $a0, $a1
    add     $t1, $a2, $a3
    sub     $s0, $t0, $t1
    add     $v0, $s0, $zero
    lw      $s0, 0($sp)
    lw      $t0, 4($sp)
    lw      $t1, 8($sp)
    addi    $sp, $sp, 12
    jr      $ra
```

Could have avoided using the stack altogether.

## Example 2 (pg. 101)

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```
int fact (int n)
{
    if (n < 1) return (1);
    else return (n * fact(n-1));
}
```

### Notes:

The caller saves \$a0 and \$ra in its stack space.

Temp register \$t0 is never saved.

```
fact:
    slti    $t0, $a0, 1
    beq     $t0, $zero, L1
    addi    $v0, $zero, 1
    jr      $ra
L1:
    addi    $sp, $sp, -8
    sw      $ra, 4($sp)
    sw      $a0, 0($sp)
    addi    $a0, $a0, -1
    jal fact
    lw      $a0, 0($sp)
    lw      $ra, 4($sp)
    addi    $sp, $sp, 8
    mul     $v0, $a0, $v0
    jr      $ra
```



## Example 2 (pg. 101)

$n = 3$

```
int fact (int n)
{
    if (n < 1) return (1);
    else return (n * fact(n-1));
}
```

Red annotations: 3 (under `fact`), 3 (under `n` in `else return`), 3 (under `n` in `n * fact(n-1)`), 2 (under `fact(n-1)`), 2 (under `n-1`).

```
int fact (int n)
{
    if (n < 1) return (1);
    else return (n * fact(n-1));
}
```

Red annotations: 2 (under `fact`), 2 (under `n` in `else return`), 2 (under `n` in `n * fact(n-1)`), 2 (under `fact(n-1)`), 1 (under `n-1`), 1 (under `n-1`).

```
int fact (int n)
{
    if (n < 1) return (1);
    else return (n * fact(n-1));
}
```

Red annotations: 1 (under `fact`), 1 (under `n` in `else return`), 1 (under `n` in `n * fact(n-1)`), 0 (under `fact(n-1)`), 1 (under `n-1`).

```
int fact (int n)
{
    if (n < 1) return (1);
    else return (n * fact(n-1));
}
```

Red annotations: 0 (under `fact`), 0 (under `n` in `else return`), 1 (under `n` in `n * fact(n-1)`), 1 (under `fact(n-1)`).

## Example 2 (pg. 101)

```
fact:
    slti    $t0, $a0, 1
    beq     $t0, $zero, L1
    addi    $v0, $zero, 1
    jr      $ra
L1:
    addi    $sp, $sp, -8
    sw      $ra, 4($sp)
    sw      $a0, 0($sp)
    addi    $a0, $a0, -1
    jal     fact
    lw      $a0, 0($sp)
    lw      $ra, 4($sp)
    addi    $sp, $sp, 8
    mul     $v0, $a0, $v0
    jr      $ra
```

Annotations for first assembly block:

- Red numbers: 3 (above `$a0`), 0 (above `$zero`), 3 (above `$v0`), 2 (above `fact`).
- Blue text: "ra for caller" (next to `$ra`), "ra for caller" (next to `$ra`), "ra for caller" (next to `$ra`).

```
fact:
    slti    $t0, $a0, 1
    beq     $t0, $zero, L1
    addi    $v0, $zero, 1
    jr      $ra
L1:
    addi    $sp, $sp, -8
    sw      $ra, 4($sp)
    sw      $a0, 0($sp)
    addi    $a0, $a0, -1
    jal     fact
    lw      $a0, 0($sp)
    lw      $ra, 4($sp)
    addi    $sp, $sp, 8
    mul     $v0, $a0, $v0
    jr      $ra
```

Annotations for second assembly block:

- Red numbers: 2 (above `$a0`), 0 (above `$zero`), 2 (above `$v0`), 1 (above `fact`).
- Blue text: "ra for 'jal fact'" (next to `$ra`), "ra for 'jal fact'" (next to `$ra`), "ra for 'jal fact'" (next to `$ra`).

```
fact:
    slti    $t0, $a0, 1
    beq     $t0, $zero, L1
    addi    $v0, $zero, 1
    jr      $ra
L1:
    addi    $sp, $sp, -8
    sw      $ra, 4($sp)
    sw      $a0, 0($sp)
    addi    $a0, $a0, -1
    jal     fact
    lw      $a0, 0($sp)
    lw      $ra, 4($sp)
    addi    $sp, $sp, 8
    mul     $v0, $a0, $v0
    jr      $ra
```

Annotations for third assembly block:

- Red numbers: 1 (above `$a0`), 0 (above `$zero`), 1 (above `$v0`), 0 (above `fact`).
- Blue text: "ra for 'jal fact'" (next to `$ra`), "ra for 'jal fact'" (next to `$ra`), "ra for 'jal fact'" (next to `$ra`).

## Example 2 (pg. 101)

---

fact:

```
    slti    $t0, $a0, 1
    beq     $t0, $zero, L1
    addi    $v0, $zero, 1
    jr      $ra
```

L1:

```
    addi    $sp, $sp, -8
    sw      $ra, 4($sp)
    sw      $a0, 0($sp)
    addi    $a0, $a0, -1
    jal fact
    lw      $a0, 0($sp)
    lw      $ra, 4($sp)
    addi    $sp, $sp, 8
    mul     $v0, $a0, $v0
    jr      $ra
```

# Saving Conventions

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- **Caller saves:** Temp registers \$t0-\$t9 (the callee won't bother saving these, so save them if you care), \$ra (it's about to get over-written), \$a0-\$a3 (so you can put in new arguments)
- **Callee saves:** \$s0-\$s7 (these typically contain “valuable” data)