# GigaDevice Semiconductor Inc.

**GD32470Z-EVAL** 

User Guide V2.0



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# 1. Summary

GD32470Z-EVAL uses GD32F470ZKT6 as the main controller. It uses Mini USB interface or DC-005 connector to supply 5V power. SWD, Reset, Boot, User button key, LED, CAN, I2C, I2S, USART, RTC, LCD, SPI, ADC, DAC, EXMC, CTC, SDIO, ENET, USBFS, USBHS, GD-Link and Extension Pins are also included. For more details please refer to GD32470Z-EVAL-V1.0 schematic.

# 2. Function pin assignment

Table 2-1 Function pin assignment

Function	Pin	Description
	PD4	LED1
LED	PD5	LED2
	PG3	LED3
RESET		K1-Reset
	PA0	K2-Wakeup
KEY	PC13	K3-Tamper
	PB14	K4-User key
LICADTO	PA9	USART0_TX
USART0	PA 10	USART0_RX
ADC	PF6	ADC012_IN4
DAC	PA4	DAC_OUT0
100	PB6	12C0_SCL
12C	PB7	12C0_SDA
	PG10	SPI5_IO2
	PG11	SPI5_IO3
SPI	PG13	SPI5_SCK
3PI	PG14	SPI5_MOSI
	PG12	SPI5_MISO
	PG9	SPI5_CS
	PC6	12S1_MCK
100	PC7	12S1_CK
128	PB9	12S1_WS
	PC1	12S1_SD
CAN	PD0	CAN0_RX
CAN	PD1	CAN0_TX
SDRAM	PC0	EXMC_SDNWE





	-	GD32470Z-EVA
	PC2	EXMC_SDNE0
	PC3	EXMC_SDCKE0
	PD0	EXMC_D2
	PD1	EXMC_D3
	PD8	EXMC_D13
	PD9	EXMC_D14
	PD10	EXMC_D15
	PD14	EXMC_D0
	PD15	EXMC_D1
	PE0	EXMC_NBL0
	PE1	EXMC_NBL1
	PE7	EXMC_D4
	PE8	EXMC_D5
	PE9	EXMC_D6
	PE10	EXMC_D7
	PE11	EXMC_D8
	PE12	EXMC_D9
	PE13	EXMC_D10
	PE14	EXMC_D11
	PE15	EXMC_D12
	PF0	EXMC_A0
	PF1	EXMC_A1
	PF2	EXMC_A2
	PF3	EXMC_A3
	PF4	EXMC_A4
	PF5	EXMC_A5
	PF11	EXMC_NRAS
	PF12	EXMC_A6
	PF13	EXMC_A7
	PF14	EXMC_A8
	PF15	EXMC_A9
	PG0	EXMC_A10
	PG1	EXMC_A11
	PG2	EXMC_A12
	PG4	EXMC_A14
	PG5	EXMC_A15
	PG8	EXMC_SDCLK
	PG15	EXMC_NCAS
	PD2	SDIO_CMD
SDIO	PC12	SDIO_CK
	PC8	SDIO_D0





		GD324102-LVA
	PC9	SDIO_D1
	PC10	SDIO_D2
	PC11	SDIO_D3
	PC6	TLI_HSYNC
	PA4	TLI_VSYNC
	PG7	TLI_PIXCLK
	PF10	TLI_DE
	PG6	TLI_R7
	PA8	TLI_R6
	PA 12	TLI_R5
	PA 11	TLI_R4
	PB0	TLI_R3
	PD3	TLI_G7
LCD	PC7	TLI_G6
	PB11	TLI_G5
	PB10	TLI_G4
	PG10	TLI_G3
	PA6	TLI_G2
	PB9	 TLI_B7
	PB8	 TLI_B6
	PA3	 TLI_B5
	PG12	TLI_B4
	PG11	TLI_B3
	PA1	ETH_RMII_REF_CLK
	PA2	ETH_MDIO
	PA7	ETH_RMII_CRS_DV
	PB11	ETH_RMII_TX_EN
Ethernet	PB12	ETH_RMII_TXD0
	PB13	ETH_RMII_TXD1
	PC1	ETH_MDC
	PC4	ETH_RMII_RXD0
	PC5	ETH_RMII_RXD1
	PA9	USB VBUS
	PA 11	USB_DM
USB_FS	PA 12	USB_DP
	PD13	USB_VBUS_CTRL
	PC3	USB_HS_ULPI_NXT
	PC2	USB_HS_ULPI_DIR
USB_HS	PC0	USB_HS_ULPI_STP
_	PA5	USB_HS_ULPI_CK
	PB5	USB HS ULPI D7
	1 50	999_110_0EF1_DI



PB13	USB_HS_ULPI_D6
PB12	USB_HS_ULPI_D5
PB11	USB_HS_ULPI_D4
PB10	USB_HS_ULPI_D3
PB1	USB_HS_ULPI_D2
PB0	USB_HS_ULPI_D1
PA3	USB_HS_ULPI_D0

# 3. Getting started

The EVAL board uses Mini USB connecter or DC-005 connector to get power DC +5V, which is the hardware system normal work voltage. A J-Link tool or GD-Link on board is necessary in order to download and debug programs. Select the correct boot mode and then power on, the LED5 will turn on, which indicates that the power supply is OK.

There are Keil version and IAR version of all projects. Keil version of the projects are created based on Keil MDK-ARM 4.74 uVision4. IAR version of the projects are created based on IAR Embedded Workbench for ARM 7.40.2. In Firmware folder, Addon and Software Pack are used to add the devices, peripherals and others to IDE. During use, the following points should be noted:

- 1. If you use Keil uVision4 to open the project, install the GD32F4xx\_Addon.3.0.0.exe which is in \Library\Firmware to load the associated files.
- 2. If you use Keil uVision5 to open the project, there are two ways to solve the "Device Missing (s)" problem. One is to install GigaDevice.GD32F4xx\_DFP.3.0.0.pack which is in \Library\Firmware. In Project menu, select the Manage sub menu, click on the "Version Migrate 5 Format..." menu, the Keil uVision4 project will be converted to Keil uVision5 project. Then add "C:\Keil\_v5\ARM\Pack\ARM\CMSIS\4.2.0\CMSIS\Include" to C/C++ in Option for Target. The other is to install Addon directly. Select the installation directory of Keil uVision5 software, such as C:\Keil\_v5, in Destination Folder of Folder Selection. Select the corresponding device in Device of Option for Target and add "C:\Keil\_v5\ARM\Pack\ARM\CMSIS\4.2.0\CMSIS\Include" to C/C++ in Option for Target.

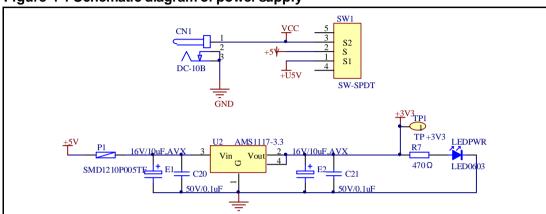
  3. If you use IAR to open the project, install IAR\_GD32F4xx\_ADDON.3.0.0.exe which is in \Library\Firmware to load the associated files.



# 4. Hardware layout overview

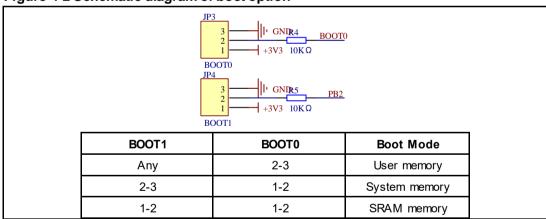
# 4.1. Power supply

Figure 4-1 Schematic diagram of power supply



# 4.2. Boot option

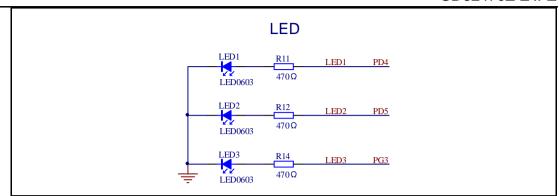
Figure 4-2 Schematic diagram of boot option



### 4.3. LED

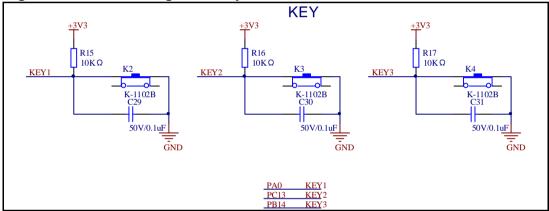
Figure 4-3 Schematic diagram of LED function





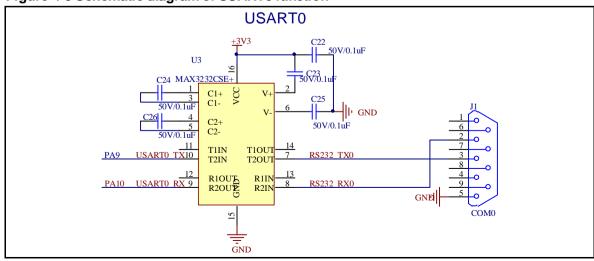
### 4.4. **KEY**





# 4.5. USART

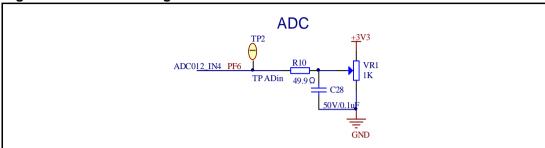
Figure 4-5 Schematic diagram of USART0 function





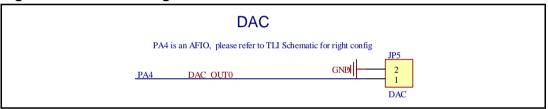
## 4.6. ADC

Figure 4-6 Schematic diagram of ADC function



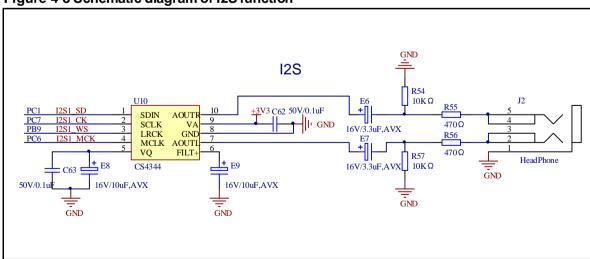
# 4.7. DAC

Figure 4-7 Schematic diagram of DAC function



#### 4.8. I2S

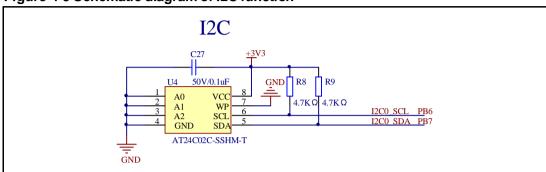
Figure 4-8 Schematic diagram of I2S function





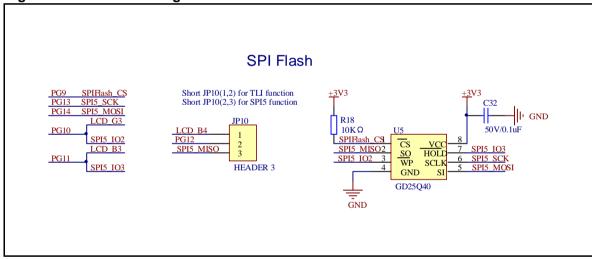
# 4.9. I2C

Figure 4-9 Schematic diagram of I2C function



# 4.10. SPI

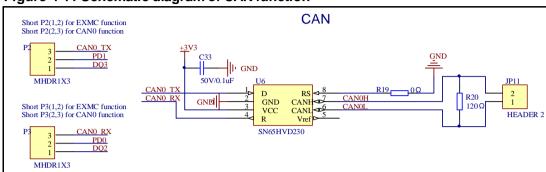
Figure 4-10 Schematic diagram of SPI function





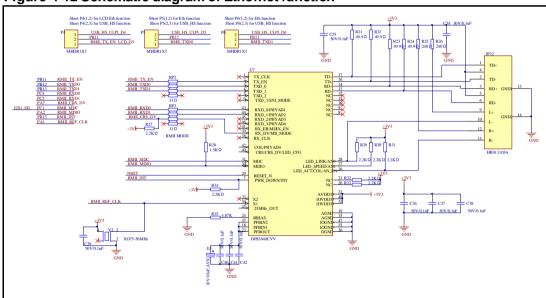
# 4.11. CAN

Figure 4-11 Schematic diagram of CAN function



# 4.12. **ENET**

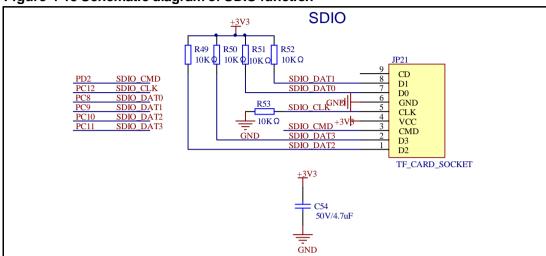
Figure 4-12 Schematic diagram of Ethernet function





# 4.13. SDIO

Figure 4-13 Schematic diagram of SDIO function





#### 4.14. SDRAM

**SDRAM** PF0 A0 119 A1 A2 PF1 VDD I GND VSS PF2 DO15 DO0 DQ0 VDDQ PF3 DQ15 A3 l GND PF4 A4 VSSC DQ1 DQ14 PF5 DQ1 DQ14 A5 50 PF12 PF13 DQ2 VSSQ A6 DO13 49 6 7 GND VDDQ +3V3 A7 DQ3 DQ4 DQ12 DQ11 DQ3 DQ4 VDDQ PF14 8 PF15 PG0 A9 A10 DO11 || GND DO5 10 DO10 45 DQ5 DQ6 VSSQ DQ7 PG1 A11 DO10 44 DQ9 DQ9 PG2 A12 43 GND +3V3 VDD DQ7 13 42 DO8 14 SDR\_NBL0 15 VDD l GND 40 LDQM WE DO0 PD14 NC SDR\_NBL1 SDR\_CLK 16 17 SDR NWE 39 DQ1 DQ2 PD15 PD0 38 SDR\_NCAS CAS RAS CS CLK SDR\_NRAS 18 SDR\_CKE0 PD1 DQ3 CKE SDR NE0 36 A12 PE7 PE8 SDR\_BA0 35 20 A11 BA0 DO5 A11 34 33 SDR\_BA1 A9 PE9 DQ6 A10 A8 PE10 PE11 A10/AP A0 DO7 A8 DQ8 A7 A1 24 31 A6 PE12 DQ9 A1 Α6 30 A5 A2 PE13 DO10 A5 26 29 A4 PE14 PE15 A3 VDD DQ11 l GND +3V DO12 VSS DQ13 PD8 MT48LZ16M16A2P-6AIT PD9 DO14 PD10 DQ15 <u>+3</u>V3 SDR\_NBL0 PE0 SDR NBL1 PE1 PC3 PG4 SDR CKE0 C58 C61 SDR BA0 50V/0.1uF 50V/0.1uF 50V/0.1uF 50V/0.1uF 50V/0.1uF 50V/0.1uF 50V/0.1u SDR BA1 PG8 SDR CLK SDR NCAS PF11 SDR NRAS **GND** SDR NE0 PC2 PC0 SDR NWE

Figure 4-14 Schematic diagram of SDRAM function

#### 4.15. LCD

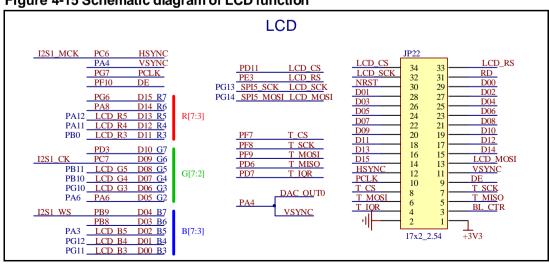
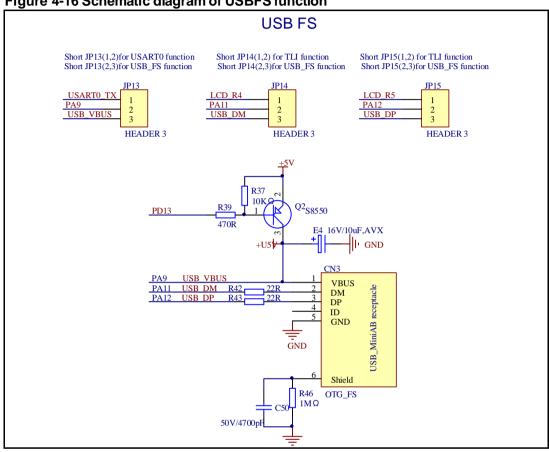


Figure 4-15 Schematic diagram of LCD function



#### 4.16. **USBFS**

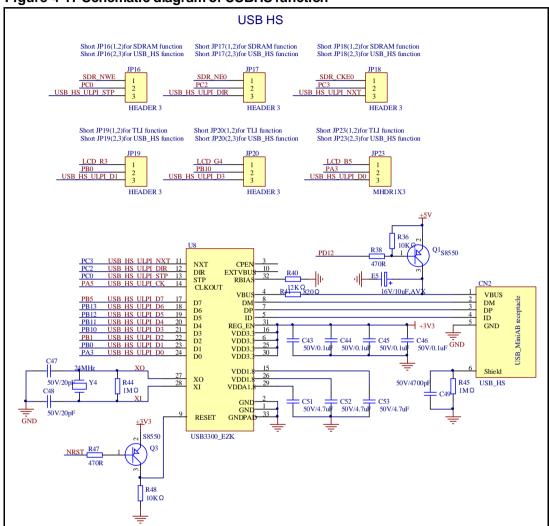






#### 4.17. USBHS

Figure 4-17 Schematic diagram of USBHS function



#### 4.18. Extension

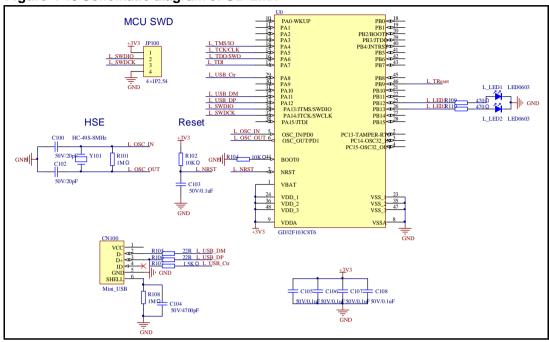
Figure 4-18 Schematic diagram of Extension Pin

Extension Pin				
PE2	PA3	PB12   PB13   PB14   3	PA14   PA15   PA15   PC10   3   4   PC10   15   6   PD2   PD3   PD4   PD5   PD5   PD7   PD6   PD7   PD7	



# 4.19. **GD-Link**

Figure 4-19 Schematic diagram of GD-Link





# 5. Routine use guide

### 5.1. **GPIO\_Running\_LED**

#### 5.1.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use GPIO control the LED
- Learn to use SysTick to generate 1ms delay

GD32470Z-EVAL-V1.0 board has three LEDs. The LED1, LED2 and LED3 are controlled by GPIO. This demo will show how to light the LEDs.

### 5.1.2. **DEMO** running result

Download the program <01\_GPIO\_Running\_LED> to the EVAL board, LED1, LED2 and LED3 will turn on in sequence with interval of 400ms, and repeat the process.

# 5.2. GPIO\_Key\_Polling\_mode

#### 5.2.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use GPIO control the LED and the Key
- Learn to use SysTick to generate 1ms delay

GD32470Z-EVAL-V1.0 board has four keys and three LEDs. The four keys are Reset key, Tamper key, Wakeup key and User key. The LED1, LED2 and LED3 are controlled by GPIO.

This demo will show how to use the Tamper key to control the LED1. When press down the Tamper Key, it will check the input value of the IO port. If the value is 0 and will wait for 100ms. Check the input value of the IO port again. If the value still is 0, it indicates that the button is pressed successfully and toggle LED1.

#### 5.2.2. DEMO running result

Download the program <02\_GPIO\_Key\_Polling\_mode> to the EVAL board, Press down the Tamper Key, LED1 will be turned on. Press down the Tamper Key again, LED1 will be turned off.



### 5.3. EXTI\_Key\_Interrupt\_mode

#### 5.3.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use GPIO control the LED and the KEY
- Learn to use EXTI to generate external interrupt

GD32470Z-EVAL-V1.0 board has four keys and three LEDs. The four keys are Reset key, Tamper key, Wakeup key and User key. The LED1, LED2 and LED3 are controlled by GPIO.

This demo will show how to use the EXTI interrupt line to control the LED1. When press down the Tamper Key, it will produce an interrupt. In the interrupt service function, the demo will toggle LED1.

#### 5.3.2. **DEMO** running result

Download the program <03\_EXTI\_Key\_Interrupt\_mode> to the EVAL board, LED1 is turned on and off for test. When press down the Tamper Key, LED1 will be turned on. Press down the Tamper Key again, LED1 will be turned off.

#### 5.4. USART Printf

#### 5.4.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use GPIO control the LED
- Learn to retarget the C library printf function to the USART

#### 5.4.2. DEMO running result

Download the program < 04\_USART\_Printf > to the EVAL board, connect serial cable to COM0 and jump JP13 to USART. This implementation outputs "USART printf example: please press the Tamper key" on the HyperTerminal using COM0. Press the Tamper key, the LED1 will be turned on and serial port will output "USART printf example".

The output information via the serial port is as following.

USART printf example: please press the Tamper key

USART printf example



### 5.5. USART\_Echo\_Interrupt\_mode

#### 5.5.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

■ Learn to use the USART transmit and receive interrupts to communicate with the serial terminal tool.

#### 5.5.2. DEMO running result

Download the program < 05\_USART\_Echo\_Interrupt\_mode > to the EVAL board, connect serial cable to COM0 and jump JP13 to USART. Firstly, all the LEDs are turned on and off for test. Then, the COM0 sends the tx\_buffer array (from 0x00 to 0xFF) to the serial terminal tool supporting hex format communication and waits for receiving data of same bytes from the serial terminal. The data MCU has received is stored in the rx\_buffer array. After that, compare tx\_buffer with rx\_buffer. If tx\_buffer is same with rx\_buffer, LED1, LED2, LED3 flash by turns. Otherwise, LED1, LED2, LED3 toggle together. The output information via the serial port is as following.

00 01 02 03 04 05 06 07 08 09 0A 0B OC OD OE OF 10 11 12 13 14 15 16 17 1C 1D 1E 1F 2O 21 22 23 24 25 26 27 28 29 2A 2B 2C 2D 2E 2F 30 31 32 33 34 3B 3C 3D 3E 3F 40 41 42 43 45 46 47 48 49 4A 4B 4C 4D 4E 4F 50 51 44 57 58 59 5A 5B 5C 5D 5E 5F 60 61 62 63 64 65 66 67 68 69 6A 6B 6C 6D 6E 6F 70 71 72 73 74 75 76 77 78 79 7A 7B 7C 7D 7E 7F 8O 81 82 83 84 85 86 87 88 89 8A 8B 98 99 9A 9B 9C 9D 9E 9F AO A1 A2 8C 8D 8E 8F 9O 91 92 93 94 95 96 97 A3 A4 A9 AA AB AC AD AE AF BO B1 B2 B3 B4 B5 B6 B7 B8 B9 BA BB BC BD BE BF CO C1 C4 C5 C6 C7 C8 C9 CA CB CC CD CE CF DO D1 D2 D3 D4 D5 D6 D7 D8 D9 DA DB DC DD DE DF EO E1 E2 Lo FC FD FE FF E3 E4 E5 E6 E7 E8 E9 EA EB EC ED EE EF F0 F1 F2 F3 F4 F5 F6 F7 F8 F9 FA FB

#### 5.6. USART\_DMA

#### 5.6.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

Learn to use the USART transmit and receive data using DMA.

#### 5.6.2. DEMO running result

Download the program < 06\_USART\_DMA > to the EVAL board, connect serial cable to COM0 and jump JP13 to USART. Firstly, all the LEDs are turned on and off for test. Then, the COM0 sends the tx\_buffer array (from 0x00 to 0xFF) to the serial terminal tool supporting hex format communication and waits for receiving data of same bytes as tx\_buffer from the serial terminal. The data MCU have received is stored in the rx\_buffer array. After that, compare tx\_buffer with rx\_buffer. If tx\_buffer is same with rx\_buffer, LED1, LED2, LED3 flash by turns. Otherwise, LED1, LED2, LED3 toggle together.



00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 10 11 12 13 14 15 16 17 18 19 1A 1B 1C 1D 1E 1F 20 21 22 23 24 25 26 27 28 29 2A 2B 2C 2D 2E 2F 30 31 32 33 34 35 36 37 38 39 3A 3B 3C 3D 3E 3F 40 41 42 43 44 45 46 47 48 49 4A 4B 4C 4D 4E 4F 50 51 52 53 54 55 56 57 58 59 5A 5B 5C 5D 5E 5F 60 61 62 63 64 65 66 67 68 69 6A 6B 6C 6D 6E 6F 70 71 72 73 74 75 76 77 78 79 7A 7B 7C 7D 7E 7F 80 81 82 83 84 85 86 87 88 89 8A 8B 8C 8D 8E 8F 90 91 92 93 94 95 96 97 98 99 9A 9B 9C 9D 9E 9F AO A1 A2 A3 A4 A5 A6 A7 A8 A9 AA AB AC AD AE AF BO B1 B2 B3 B4 B5 B6 B7 B8 B9 BA BB BC BD BE BF CO C1 C2 C3 C4 C5 C6 C7 C8 C9 CA CB CC CD CE CF DO D1 D2 D3 D4 D5 D6 D7 D8 D9 DA DB DC DD DE DF EO E1 E2 E3 E4 E5 E6 E7 E8 E9 EA EB EC ED EE EF FO F1 F2 F3 F4 F5 F6 F7 F8 F9 FA FB FC FD FE FF |

## 5.7. ADC\_Temperature\_Vrefint

#### 5.7.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use the ADC to convert analog signal to digital data
- Learn to get the value of inner channel 16(temperature sensor channel), channel 17 (VREFINT channel) and channel 18(VBAT/4 channel)

#### 5.7.2. DEMO running result

Jump the JP13 to USART with the jumper cap, and then download the program <07\_ADC\_Temperature\_Vrefint\_Vbat> to the board. Connect serial cable to COM0, open the HyperTerminal.

When the program is running, HyperTerminal display the value of temperature, internal voltage reference (VREFINT) and external battery voltage VBAT.

Notice: because there is an offset, when inner temperature sensor is used to detect accurate temperature, an external temperature sensor part should be used to calibrate the offset error.

the temperature data is 24 degrees Celsius the reference voltage data is 1.198V the battery voltage is 3.213V

the temperature data is 25 degrees Celsius the reference voltage data is 1.201V the battery voltage is 3.213V

the temperature data is 25 degrees Celsius the reference voltage data is 1,199V the battery voltage is 3,203V

the temperature data is 25 degrees Celsius the reference voltage data is 1.198V the battery voltage is 3.213V



### 5.8. ADC0\_ADC1\_Follow\_up\_mode

#### 5.8.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use the ADC to convert analog signal to digital data
- Learn to use ADC0 and ADC1 follow-up mode

#### 5.8.2. **DEMO** running result

Jump the JP13 to USART with the jumper cap, and then download the program <08\_ADC0\_ADC1\_Follow\_up\_mode> to the board. Connect serial cable to COM0, open the HyperTerminal. PC5 pin connect to the external voltage input. PC3 is the output voltage of the slide rheostat VR1 on board. Keep PC5 pin should not be reused by other peripherals. JP17 should not be connected.

TIMER1\_CH1 is the trigger source of ADC0 and ADC1. When the rising edge of TIMER1\_CH1 coming, ADC0 starts immediately and ADC1 starts after a delay of several ADC clock cycles. The values of ADC0 and ADC1 are transmitted to array adc\_value[0] and adc\_value [1] by DMA.

When the first rising edge of TIMER1\_CH1 coming, the value of the ADC0 conversion of PC3 pin is stored into the low half word of adc\_value [0], and after a delay of several ADC clock cycles the value of the ADC1 conversion of PC5 pin is stored into the high half word of adc\_value [0]. When the second rising edge of TIMER1\_CH1 coming, the value of the ADC0 conversion of PC5 pin is stored into the low half word of adc\_value [1], and after a delay of several ADC clock cycles the value of the ADC1 conversion of PC3 pin is stored into the high half word of adc\_value [1].

When the program is running, HyperTerminal display the regular value of ADC0 and ADC1 by adc\_value [0] and adc\_value [1].



```
the data adc_value[0] is OODEOFF3
the data adc_value[1] is OFFFOOA3

the data adc_value[0] is OOE3OFFE
the data adc_value[1] is OFFFOOA4

the data adc_value[0] is OOEAOFF9
the data adc_value[1] is OFF4OOB2

the data adc_value[0] is OODEOFFF
the data adc_value[1] is OFFEOOA9

the data adc_value[0] is OOEOOFF1
the data adc_value[1] is OFF5OOA6
```

## 5.9. ADC0\_ADC1\_Regular\_Parallel\_mode

#### 5.9.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use the ADC to convert analog signal to digital data
- Learn to use ADC0 and ADC1 regular parallel mode

#### 5.9.2. DEMO running result

Jump the JP13 to USART with the jumper cap, and then download the program <09\_ADC0\_ADC1\_Regular\_Parallel\_mode> to the board. Connect serial cable to COM0, open the HyperTerminal. PC5 pin connect to the external voltage input. PC3 is the output voltage of the slide rheostat VR1 on board. Keep PC5 pin should not be reused by other peripherals. JP17 should not be connected.

TIMER1\_CH1 is the trigger source of ADC0 and ADC1. When the rising edge of TIMER1\_CH1 coming, ADC0 and ADC1 convert the regular channel group parallelly. The values of ADC0 and ADC1 are transmitted to array adc\_value[0] and adc\_value [1] by DMA.

When the first rising edge of TIMER1\_CH1 coming, the value of the ADC0 conversion of PC3 pin is stored into the low half word of adc\_value [0], the value of the ADC1 conversion of PC5 pin is stored into the high half word of adc\_value [0]. When the second rising edge of TIMER1\_CH1 coming, the value of the ADC0 conversion of PC5 pin is stored into the low half word of adc\_value [1], the value of the ADC1 conversion of PC3 pin is stored into the high half word of adc\_value [1].

When the program is running, HyperTerminal displays the regular value of ADC0 and ADC1



stored in adc value [0] and adc value [1].

```
the data adc_value[0] is 06210000
the data adc_value[1] is 00000627

the data adc_value[0] is 06290B29
the data adc_value[1] is 0B40061F

the data adc_value[0] is 06250B49
the data adc_value[1] is 0B590629

the data adc_value[0] is 06280B3F
the data adc_value[1] is 0B320628

the data adc_value[1] is 0B320628

the data adc_value[0] is 06230B30
the data adc_value[1] is 0B430622
```

# 5.10. DAC\_Output\_Voltage\_Value

#### 5.10.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

■ Learn to use DAC to output voltage on DAC0 output

#### 5.10.2. DEMO running result

Download the program <10\_DAC\_Output\_Voltage\_Value> to the EVAL board and run, all the LEDs will turn on and turn off for test. The digital value is 0x7FF0, its converted analog voltage should be 1.65V (VREF/2), using the voltmeter to measure PA4 or DAC\_OUT0 on JP5, its value is 1.65V. And the signal can be observed through the oscilloscope.

### **5.11. I2C\_EEPROM**

#### 5.11.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use the master transmitting mode of I2C module
- Learn to use the master receiving mode of I2C module
- Learn to read and write the EEPROM with I2C interface



#### 5.11.2. DEMO running result

Download the program <11\_I2C\_EEPROM> to the EVAL board and run. Connect serial cable to COM0, jump JP13 to USART, then open the HyperTerminal to show the print message.

Firstly, the data of 256 bytes will be written to the EEPROM from the address 0x00 and printed by the serial port. Then, reading the EEPROM from address 0x00 for 256 bytes and the result will be printed. Finally, compare the data that were written to the EEPROM and the data that were read from the EEPROM. If they are the same, the serial port will output "I2C-AT24C02 test passed!" and the LEDs lights flashing, otherwise the serial port will output "Err: data read and write aren't matching." and all the LEDs light.

The output information via the serial port is as following.

```
I2C-24C02 configured....
The I2C is hardware interface
The speed is 400K
AT24C02 writing...
0x00 0x01 0x02 0x03 0x04 0x05 0x06 0x07 0x08 0x09 0x0A 0x0B 0x0C 0x0D 0x0E 0x0F
0x10 0x11 0x12 0x13 0x14 0x15 0x16 0x17 0x18 0x19 0x1A 0x1B 0x1C 0x1D 0x1E 0x1F
0x20 0x21 0x22 0x23 0x24 0x25 0x26 0x27 0x28 0x29 0x2A 0x2B 0x2C 0x2D 0x2E 0x2F
0x30 0x31 0x32 0x33 0x34 0x35 0x36 0x37 0x38 0x39 0x3A 0x3B 0x3C 0x3D 0x3E 0x3F
0x40 0x41 0x42 0x43 0x44 0x45 0x46 0x47 0x48 0x49 0x4A 0x4B 0x4C 0x4D 0x4E 0x4F
0x50 0x51 0x52 0x53 0x54 0x55 0x56 0x57 0x58 0x59 0x5A 0x5B 0x5C 0x5D 0x5E 0x5E
0x60 0x61 0x62 0x63 0x64 0x65 0x66 0x67 0x68 0x69 0x6A 0x6B 0x6C 0x6D 0x6E 0x6F
0x70 0x71 0x72 0x73 0x74 0x75 0x76 0x77 0x78 0x79 0x7A 0x7B 0x7C 0x7D 0x7E 0x7F
0x80 0x81 0x82 0x83 0x84 0x85 0x86 0x87 0x88 0x89 0x8A 0x8B 0x8C 0x8D 0x8E 0x8F
0x90 0x91 0x92 0x93 0x94 0x95 0x96 0x97 0x98 0x99 0x9A 0x9B 0x9C 0x9D 0x9E 0x9F
DXAD DXA1 DXA2 DXA3 DXA4 DXA5 DXA6 DXA7 DXA8 DXA9 DXAA DXAB DXAC DXAD DXAE DXAF
OxBO OxB1 OxB2 OxB3 OxB4 OxB5 OxB6 OxB7 OxB8 OxB9 OxBA OxBB OxBC OxBD OxBE OxBF
OxCO OxC1 OxC2 OxC3 OxC4 OxC5 OxC6 OxC7 OxC8 OxC9 OxCA OxCB OxCC OxCD OxCE OxCF
OxDO OxD1 OxD2 OxD3 OxD4 OxD5 OxD6 OxD7 OxD8 OxD9 OxDA OxDB OxDC OxDD OxDE OxDF
OXEO OXE1 OXE2 OXE3 OXE4 OXE5 OXE6 OXE7 OXE8 OXE9 OXEA OXEB OXEC OXED OXEE OXEF
OxFO OxF1 OxF2 OxF3 OxF4 OxF5 OxF6 OxF7 OxF8 OxF9 OxFA OxFB OxFC OxFD OxFE OxFE
AT24C02 reading...
0x00 0x01 0x02 0x03 0x04 0x05 0x06 0x07 0x08 0x09 0x0A 0x0B 0x0C 0x0D 0x0E 0x0F
0x10 0x11 0x12 0x13 0x14 0x15 0x16 0x17 0x18 0x19 0x1A 0x1B 0x1C 0x1D 0x1E 0x1F
0x20 0x21 0x22 0x23 0x24 0x25 0x26 0x27 0x28 0x29 0x2A 0x2B 0x2C 0x2D 0x2E 0x2F
0x30 0x31 0x32 0x33 0x34 0x35 0x36 0x37 0x38 0x39 0x3A 0x3B 0x3C 0x3D 0x3E 0x3F
0x40 0x41 0x42 0x43 0x44 0x45 0x46 0x47 0x48 0x49 0x4A 0x4B 0x4C 0x4D 0x4E 0x4F
0x50 0x51 0x52 0x53 0x54 0x55 0x56 0x57 0x58 0x59 0x5A 0x5B 0x5C 0x5D 0x5E 0x5E
0x60 0x61 0x62 0x63 0x64 0x65 0x66 0x67 0x68 0x69 0x6A 0x6B 0x6C 0x6D 0x6E 0x6F
0x70 0x71 0x72 0x73 0x74 0x75 0x76 0x77 0x78 0x79 0x7A 0x7B 0x7C 0x7D 0x7E 0x7E
0x80 0x81 0x82 0x83 0x84 0x85 0x86 0x87 0x88 0x89 0x8A 0x8B 0x8C 0x8D 0x8E 0x8F
0x90 0x91 0x92 0x93 0x94 0x95 0x96 0x97 0x98 0x99 0x9A 0x9B 0x9C 0x9D 0x9E 0x9F
OxAO OxA1 OxA2 OxA3 OxA4 OxA5 OxA6 OxA7 OxA8 OxA9 OxAA OxAB OxAC OxAD OxAE OxAF
OxBO OxB1 OxB2 OxB3 OxB4 OxB5 OxB6 OxB7 OxB8 OxB9 OxBA OxBB OxBC OxBD OxBE OxBF
OxCO OxC1 OxC2 OxC3 OxC4 OxC5 OxC6 OxC7 OxC8 OxC9 OxCA OxCB OxCC OxCD OxCE OxCF
OxDO OxD1 OxD2 OxD3 OxD4 OxD5 OxD6 OxD7 OxD8 OxD9 OxDA OxDB OxDC OxDD OxDE OxDF
OXEO OXE1 OXE2 OXE3 OXE4 OXE5 OXE6 OXE7 OXE8 OXE9 OXEA OXEB OXEC OXED OXEE OXEF
OxFO OxF1 OxF2 OxF3 OxF4 OxF5 OxF6 OxF7 OxF8 OxF9 OxFA OxFB OxFC OxFD OxFE OxFE
I2C-AT24C02 test passed!
```



### 5.12. SPI Quad Flash

#### 5.12.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

Learn to use the Quad-SPI mode of SPI unit to read and write NOR Flash with the SPI interface

GD32470Z-EVAL-V1.0 board integrates SPI5 module with Quad-SPI mode and the mode supports communication with external NOR Flash devices. The SPI NOR FLASH is a serial FLASH memory chip GD25Q40 which size is 40Mbit, and the chip supports standard SPI and quad SPI operation instructions.

#### 5.12.2. DEMO running result

The computer serial port line connected to the COM0 port of development board, set the baud rate of HyperTerminal software to 115200, 8 bits data bit, 1 bit stop bit. At the same time you should jump the JP13 to USART, and jump the JP10 to SPI.

Download the program <12\_SPI\_Quad\_Flash> to the EVAL board, the HyperTerminal software can observe the operation condition and will display the ID of the flash, 256 bytes data which are written to and read from flash. Compare the data that were written to the flash and the data that were read from the flash. If they are the same, the serial port will output "SPI-GD25Q40 Test Passed!", otherwise, the serial port will output "Err: Data Read and Write aren't Matching.". At last, turn on and off the leds one by one. The following is the experimental results.



```
GD32470Z-EVAL System is Starting up...
GD32470Z-EVAL SystemCoreClock:240000000Hz
GD32470Z-EVAL The CPU Unique Device ID: [33394150-B323830-3431309D]
GD32470Z-EVAL SPI Flash: GD25Q40 configured...
The Flash_ID:0xC84015
Write to tx_buffer:
0x00 0x01 0x02 0x03 0x04 0x05 0x06 0x07 0x08 0x09 0x0A 0x0B 0x0C 0x0D 0x0E 0x0F
0x10 0x11 0x12 0x13 0x14 0x15 0x16 0x17 0x18 0x19 0x1A 0x1B 0x1C 0x1D 0x1E 0x1F
0x20 0x21 0x22 0x23 0x24 0x25 0x26 0x27 0x28 0x29 0x2A 0x2B 0x2C 0x2D 0x2E 0x2F
0x30 0x31 0x32 0x33 0x34 0x35 0x36 0x37 0x38 0x39 0x3A 0x3B 0x3C 0x3D 0x3E 0x3F
0x40 0x41 0x42 0x43 0x44 0x45 0x46 0x47 0x48 0x49 0x4A 0x4B 0x4C 0x4D 0x4E 0x4F
0x50 0x51 0x52 0x53 0x54 0x55 0x56 0x57 0x58 0x59 0x5A 0x5B 0x5C 0x5D 0x5E 0x5E
0x60 0x61 0x62 0x63 0x64 0x65 0x66 0x67 0x68 0x69 0x6A 0x6B 0x6C 0x6D 0x6E 0x6F
0x70 0x71 0x72 0x73 0x74 0x75 0x76 0x77 0x78 0x79 0x7A 0x7B 0x7C 0x7D 0x7E 0x7E
0x80 0x81 0x82 0x83 0x84 0x85 0x86 0x87 0x88 0x89 0x8A 0x8B 0x8C 0x8D 0x8E 0x8F
0x90 0x91 0x92 0x93 0x94 0x95 0x96 0x97 0x98 0x99 0x94 0x9B 0x9C 0x9D 0x9E 0x9E
OXAO OXAI OXAZ OXA3 OXA4 OXA5 OXA6 OXA7 OXA8 OXA9 OXAA OXAB OXAC OXAD OXAE OXAF
OxBO OxB1 OxB2 OxB3 OxB4 OxB5 OxB6 OxB7 OxB8 OxB9 OxBA OxBB OxBC OxBD OxBE OxBE
OxCO OxC1 OxC2 OxC3 OxC4 OxC5 OxC6 OxC7 OxC8 OxC9 OxCA OxCB OxCC OxCD OxCE OxCE
OxDO OxD1 OxD2 OxD3 OxD4 OxD5 OxD6 OxD7 OxD8 OxD9 OxDA OxDB OxDC OxDD OxDE OxDE
OMEO OME1 OME2 OME3 OME4 OME5 OME6 OME7 OME8 OME9 OMEA OMEB OMEC OMED OMEE OMEF
OxFO OxF1 OxF2 OxF3 OxF4 OxF5 OxF6 OxF7 OxF8 OxF9 OxFA OxFB OxFC OxFD OxFE OxFE
Read from rx buffer:
0x00 0x01 0x02 0x03 0x04 0x05 0x06 0x07 0x08 0x09 0x0A 0x0B 0x0C 0x0D 0x0E 0x0F
0x10 0x11 0x12 0x13 0x14 0x15 0x16 0x17 0x18 0x19 0x1A 0x1B 0x1C 0x1D 0x1E 0x1F
0x20 0x21 0x22 0x23 0x24 0x25 0x26 0x27 0x28 0x29 0x2A 0x2B 0x2C 0x2D 0x2E 0x2F
0x30 0x31 0x32 0x33 0x34 0x35 0x36 0x37 0x38 0x39 0x3A 0x3B 0x3C 0x3D 0x3E 0x3E
0x40 0x41 0x42 0x43 0x44 0x45 0x46 0x47 0x48 0x49 0x44 0x48 0x4C 0x4D 0x4E 0x4E
0x50 0x51 0x52 0x53 0x54 0x55 0x56 0x57 0x58 0x59 0x5A 0x5B 0x5C 0x5D 0x5E 0x5E
0x60 0x61 0x62 0x63 0x64 0x65 0x66 0x67 0x68 0x69 0x64 0x6B 0x6C 0x6D 0x6E 0x6E
0x70 0x71 0x72 0x73 0x74 0x75 0x76 0x77 0x78 0x79 0x7A 0x7B 0x7C 0x7D 0x7E 0x7E
0x80 0x81 0x82 0x83 0x84 0x85 0x86 0x87 0x88 0x89 0x8A 0x8B 0x8C 0x8D 0x8E 0x8E
0x90 0x91 0x92 0x93 0x94 0x95 0x96 0x97 0x98 0x99 0x9A 0x9B 0x9C 0x9D 0x9E 0x9E
OxCO OxC1 OxC2 OxC3 OxC4 OxC5 OxC6 OxC7 OxC8 OxC9 OxCA OxCB OxCC OxCD OxCE OxCE
OxDO OxD1 OxD2 OxD3 OxD4 OxD5 OxD6 OxD7 OxD8 OxD9 OxDA OxDB OxDC OxDD OxDE OxDF
OMEO OME1 OME2 OME3 OME4 OME5 OME6 OME7 OME8 OME9 OMEA OMEB OMEC OMED OMEE OMEF
OxFO OxF1 OxF2 OxF3 OxF4 OxF5 OxF6 OxF7 OxF8 OxF9 OxFA OxFB OxFC OxFD OxFE OxFE
SPI-GD25Q40 Test Passed!
```

# 5.13. I2S\_Audio\_Player

#### 5.13.1. DEMO purpose

This Demo includes the following functions of GD32 MCU:

- Learn to use I2S module to output audio file
- Parsing audio files of wav format

GD32470Z-EVAL-V1.0 board integrates the I2S (Inter-IC Sound) module, and the module can communicate with external devices using the I2S audio protocol. This Demo mainly shows how to use the I2S interface of the board for audio output.



#### 5.13.2. **DEMO** running result

Download the program<13\_I2S\_Audio\_Player>to the EVAL board, insert the headphone into the audio port, and then listen to the audio file.

### 5.14. EXMC\_SDRAM

#### 5.14.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

Learn to use EXMC control the SDRAM

#### 5.14.2. **DEMO** running result

GD32470Z-EVAL-V1.0 board has EXMC module to control SDRAM. Before running the demo, P2,P3,JP16,JP17,JP18 must be fitted to SDRAM, JP13 must be fitted to USART. Download the program <14\_EXMC\_SDRAM> to the EVAL board. This demo shows the write and read operation process of SDRAM memory by EXMC module. If the test succeed, LED1 will be turned on. Otherwise, turn on the LED3. Information via a HyperTerminal output as following:



SDRAM initial: SDRAM write d	ata complete						
SDRAM read date Check the date SDRAM test sue The data is:	a! ccessed!						
0 11 10 121 30 31 40 41 50 61 70 71 80 81 60 61 70 71 80 61 70 81 60 61 70 71 80 61 70 71 80 61 70 71 80 61 70 71 80 61 70 71 80 61 70 71 80 81 90 91 80 61 70 71 80 81 90 91 80 61 70 71 80 81 90 81 90 81 90 81 90 81 90 81 90 81 90 81 90 81 90 81 90 81 90 81 90 81 90 81 90 81 90 81 90 81 90 81	2 3 12 13 22 23 32 33 42 43 52 53 62 63 63 62 63 62 63 62 63 62 63 62 63 62 63 62 63 62 63 62 63 62 63 62 63 62 63 62 63 62 63 62 63 62 63 62 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 63 63 62 63 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63 63 62 63	4 5 14 15 24 25 34 46 46 55 64 65 74 75 84 85 64 64 65 64 64 65 64 64 65 64 64 65 64 64 65 64 64 65 64 64 65 64 64 64 65 64 64 64 65 64 64 64 64 64 64 64 64 64 64 64 64 64	66 77 166 177 267 277 368 477 568 577 668 677 669 677 669 677 669 677 669 677 669 677 669 677 669 677 669 677 669 677 669 677 679 679 689 687 689 689 687 689 687 689 687 689 687 689 687 689 689 687 689 689 687 689 689 687 689 689 687 689 689 689 689 689 689 689 689 689 689	9 1 1 2 9 3 4 9 4 9 1 9 1 9 2 9 3 4 9 9 9 4 9 9 9 1 9 9 9 2 9 9 9 9 9 9 9 9 9 9 9 9	a bh  2a 3bb  2a 3bb  5a 6bb  6a 6bb  6a 1bb  6a 6bb  6a 6bb	c 1dd 3dd 4dd 4dd 4dd 4dd 4dd 4dd 4dd 4dd 4	fiffiffiffiffiffiffiffiffiffiffiffiffif

# 5.15. EXMC\_SDRAM\_DeepSleep

### 5.15.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use EXMC control the SDRAM
- Learn to use deepsleep mode

#### 5.15.2. **DEMO** running result

GD32470Z-EVAL-V1.0 board has EXMC module to control SDRAM. Before running the demo, P2,P3,JP16,JP17,JP18 must be fitted to SDRAM, JP13 must be fitted to USART. Download



the program <15\_EXMC\_SDRAM\_DeepSleep> to the EVAL board. This demo shows how to use SDRAM in the deepsleep mode. Firstly, MCU works in the normal mode, SDRAM autorefresh cycles are performed by MCU, we write the specified data to the SDRAM. Secondly, we make the MCU to deepsleep mode, at the time, SDRAM auto-refresh cycles are performed by itself and LED2 will light on. Thirdly, press the user key to wake up MCU, compare the data which read from SDRAM with the write data, if the test pass, LED1 will be turned on. Otherwise, turn on the LED3. Information via a HyperTerminal output as following:

```
SDRAM initialized!
SDRAM write data completed!
Enter deepsleep mode!
Press the user key to wakeup the MCU!
User key has been pressed!
SDRAM read data completed!
Check the data!
SDRAM test successed!
The data is:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     1f
2f
3f
4f
5f
6f
7f
8f
                                                                    122a245a2 abaa2a2 122345a2 abaa2a2 122a245a2 abaa2a2 abaa2a2 122345a2 abaa2a2 
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  11
21
31
41
51
61
71
81
91
                                                                                                                                                                                    a1
b1
c1
d1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   e1
f1
                                                                                                                                                                               11
21
31
41
51
61
71
81
61
61
61
61
61
61
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      cf
df
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ff
                                                                                                                                                                               11
21
31
41
51
61
71
81
81
61
61
71
81
81
81
81
81
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   f
1f
2f
3f
4f
5f
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   6f
7f
8f
9f
af
bf
cf
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   ef
ff
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   f
1f
2f
3f
4f
5f
6f
7f
8f
```

# 5.16. SDIO\_SDCardTest

#### 5.16.1. DEMO purpose

This demo includes the following functions of GD32 MCU:



- Learn to use SDIO to single block or multiple block write and read.
- Learn to use SDIO to erase, lock and unlock a SD card.

GD32470Z-EVAL-V1.0 board has a secure digital input/output interface (SDIO) which defines the SD/SD I/O /MMC CE-ATA card host interface. This demo will show how to use SDIO to operate on SD card.

#### 5.16.2. DEMO running result

Jump the JP13 to USART to show the print message through HyperTerminal, and download the program <16\_SDIO\_SDCardTest> to the EVAL board and run. Connect serial cable to COM0, open the HyperTerminal. Firstly, all the LEDs are turned on and off for test. Then initialize the card and print out the information of the card. After that, test the function of single block operation, lock and unlock operation, erase operation and multiple blocks operation. If any error occurs, print the error message and turn on LED1, LED3 and turn off LED2. Otherwise, turn on all the LEDs.

Uncomment the macro DATA\_PRINT to print out the data and display them through HyperTerminal. Set bus mode(1-bit or 4-bit) and data transfer mode(polling mode or DMA mode) by comment and uncomment the related statements.

Information via a serial port output as following.

```
Card init success!
Card information:
## Card version 3.0x ##
## SDHC card ##
## Device size is 7782400KB ##
## Block size is 512B ##
## Block count is 15564800 ##
## CardCommandClasses is: 5b5 ##
## Block operation supported ##
## Erase supported ##
## Lock unlock supported ##
## Application specific supported ##
## Switch function supported ##
 Card test:
 Block write success!
 Block read success!
 The card is locked!
 Erase failed!
 The card is unlocked!
 Erase success!
 Block read success!
 Multiple block write success!
 Multiple block read success!
```

## 5.17. CAN Network

#### 5.17.1. DEMO purpose

This demo includes the following functions of GD32 MCU:



#### Learn to use the CAN0 communication between two boards

GD32470Z-EVAL board integrates CAN (controller area network) bus controller. It is a common industrial control bus. The CAN bus controller follows the 2.0A and 2.0B bus protocols. This routine demonstrates the communication between two boards through CANO.

#### 5.17.2. DEMO running result

This example is tested with two GD32F470Z-EVAL boards. Jump the JP13 to USART and P2, P3 to CAN0 with the jumper cap. Connect L pin to L pin and H pin to H pin of JP11 on the boards for sending and receiving frames. Download the program <17\_CAN\_Network> to the two EVAL boards, and connect serial cable to COM0. Firstly, the COM0 sends "please press the Tamper key to transmit data!" to the HyperTerminal. The frames are sent and the transmit data are printed by pressing Tamper Key push button. When the frames are received, the receive data will be printed and the LED2 will toggle one time.

The output information via the serial port is as following.

```
please press the Tamper key to transmit data!
CANO transmit data: ab,cd
CANO receive data: ab,cd
```

#### 5.18. RCU Clock Out

#### 5.18.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use GPIO control the LED.
- Learn to use the clock output function of RCU.
- Learn to communicate with PC by USART.

#### 5.18.2. DEMO running result

Jump the JP13 to USART with the jumper cap, and download the program <18\_RCU\_Clock\_Out> to the EVAL board and run. Connect serial cable to COM0, open the HyperTerminal. When the program is running, HyperTerminal will display the initial information. Then user can choose the type of the output clock by pressing the TAMPER button. After pressing, the corresponding LED will be turned on and HyperTerminal will display which mode be selected. The frequency of the output clock can be observed through the oscilloscope by PA8 and PC9 pin.



Information via a serial port output as following:

```
/======== Gigadevice Clock output Demo ========/
press tamper key to select clock output source
CK_OUTO: IRC16M, CK_OUT1: system clock/5
CK_OUTO: LXTAL, CK_OUT1: PLLI25R/5
```

#### 5.19. CTC Calibration

### 5.19.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use external low speed crystal oscillator (LXTAL) to implement the CTC calibration function
- Learn to use clock trim controller (CTC) to trim internal 48MHz RC oscillator (IRC48M) clock

The CTC unit trim the frequency of the IRC48M based on an external accurate reference signal source. It can automatically adjust the trim value to provide a precise IRC48M clock.

#### 5.19.2. DEMO running result

Download the program <19\_CTC\_Calibration > to the GD32470Z-EVAL-V1.0 board and run. The LED1 will turn on if the internal 48MHz RC oscillator (IRC48M) clock trim is OK.

# 5.20. PMU\_sleep\_wakeup

#### 5.20.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

■ Learn to use the USART receive interrupt to wake up the PMU from sleep mode

#### 5.20.2. DEMO running result

Download the program < 20\_PMU\_Sleep\_Wakeup > to the EVAL board, jump the JP13 to USART with the jumper cap and connect serial cable to COM0. After power-on, all the LEDs are off. The mcu will enter sleep mode and the software stop running. When the USART0 receives a byte of data from the HyperTerminal, the mcu will wake up from a



receive interrupt. And all the LEDs will flash together.

### 5.21. FMC DualBoot

### 5.21.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use FMC module to implement boot from bank0 and bank1 function
- Learn to set vector table offset

### 5.21.2. DEMO running result

Jumper JP13 to usart0, open program <21\_FMC\_DualBoot>, and then select "START\_FROM\_BANK0" or "START\_FROM\_BANK1" in the target selection box, compile them and download them to the EVAL board and run. Connect the COM0 port of the EVAL board to the computer and open the HyperTerminal. When the program is running, the serial port software displays the bank area where the currently running program is located. Each time the tamper key is pressed, the BB bit in the FMC option byte will be flipped and a system reset will be generated. When the program boot from bank0, LED1 will be lighten and the serial port will output relevant information. When the program boot from bank1, LED2 will be lighten and the serial port will output relevant information.

This application starts from bankO, and turn on the LED1

Set BB bit and then restart from bankI

This application starts from bankI, and turn on the LED2

# 5.22. RTC\_Calendar

### 5.22.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

■ Learn to use RTC module to implement calendar function



Learn to use USART module to implement time display

### 5.22.2. **DEMO** running result

Jump the JP13 to USART with the jumper cap, and download the program <22\_RTC\_Calendar> to the EVAL board and run. Connect serial cable to COM0, open the HyperTerminal. After start-up, the program will boots to reset the current time and displayed on the HyperTerminal.

### 5.23. TIMER\_Breath\_LED

### 5.23.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use Timer output PWM wave
- Learn to update channel value

### 5.23.2. **DEMO** running result

Use the DuPont line to connect the TIMER1\_CH2 (PB10) and LED1 (PD4), and then download the program <23\_TIMER\_Breath\_LED> to the board and run. When the program is running, you can see LED1 lighting from dark to bright gradually and then gradually darken, just like breathing as rhythm.



### 5.24. TLI IPA

### 5.24.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use TLI to control LCD for displaying different images
- Learn to use IPA to process image data

#### 5.24.2. DEMO running result

Jump the JP4, JP10, JP14, JP15, JP19, JP20, JP23 to LCD, and download the program <24\_TLI\_IPA> to the EVAL board and run. After downloading program to board, a running cheetah on the background of GD logo is appeared on the LCD, which outputs as following. DC-5V power supply is recommended due to the large current consumption caused by LCD screen.



## 5.25. TRNG Get Random

#### 5.25.1. **DEMO** purpose

This demo includes the following functions of GD32 MCU:

- Learn to use TRNG generate the random number.
- Learn to communicate with PC by USART.

### 5.25.2. DEMO running result

Jump the JP13 to USART with the jumper cap, and download the program <25\_TRNG\_Get\_Random> to the EVAL board and run. Connect serial cable to COM0,



open the serial terminal tool supporting hex format communication. When the program is running, the serial terminal tool will display the initial information. User can use the serial terminal tool to input the minimum and maximum values (for example, the minimum value is 0x00, the maximum value is 0x09), then application will generate random number in the input range and display it by the serial terminal tool.

Information via a serial port output as following:

```
/=========Gigadevice TRNG test========/
TRNG init ok
Please input min num (hex format):
Please input max num (hex format):
Input min num is 0
Input max num is 9
Generate random num1 is 9
Generate random num2 is 8
Please input min num (hex format):
```

### 5.26. **ENET**

#### 5.26.1. FreeRTOS\_tcpudp

### **DEMO** purpose

This demo includes the following functions of GD32 MCU:

- Learn to use Lwip stack.
- Learn to use FreeRTOS operation system.
- Learn to use netconn and socket API to handle with a task.
- Learn how to realize a tcp server.
- Learn how to realize a tcp client.
- Learn how to realize a udp server/client.
- Learn how to use DHCP to allocate ip address automatically.

This demo is based on the GD32470Z-EVAL-V1.0 board, it shows how to configure the enet peripherals to send and receive frames in normal mode and use lwip tcp / ip stack to realize ping, telnet and server/client functions.

JP4, JP5, JP6 must be fitted. JP13 jump to Usart.

It is configured in RMII mode, and 25MHz oscillator is used, the system clock is configured to 240MHz.

This demo realizes three applications:

- Telnet application, the eval board acts as tcp server. Users can link the client with the eval board server, using 8000 port. Users can see the reply from the server, and can send the name(should input enter key) to server.
- tcp client application, the eval board acts as tcp client. Users can link the eval board client with the server, using 10260 port. Users can send information from server to client, then the client will send back the information.



udp application. Users can link the eval board with another station, using 1025 port. Users can send information from station to board, then the board will send back the information.

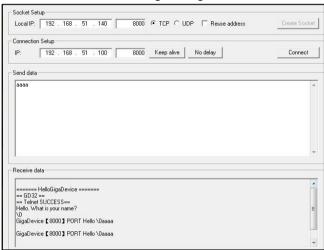
If users need dhcp function, it can be configured from the private defines in main.h. This function is closed by default.

Note: Users should configure ip address, mask and gw of GD32470Z-EVAL-V1.0 board or served according to the actual net situation from the private defines in main.h.

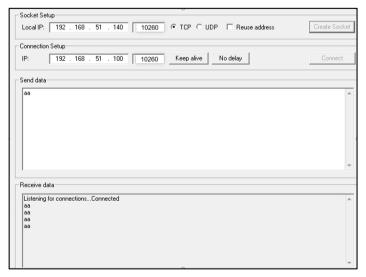
### **DEMO** running result

Download the program <FreeRTOS\_tcpudp> to the EVAL board, LED3 will light every 500ms.

Using Network assistant software, configure the pc side to tcp client, using 8000 port, and when send something through the assistant, users can see the reply from the server:



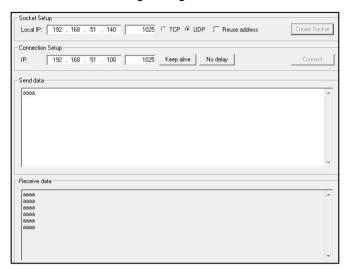
Using Network assistant software, configure the pc side to tcp server, using 10260 port, and when send something through the assistant, users can see the echo reply from the client:



Using Network assistant software, configure to use udp protocol, using 1025 port, and



when send something through the assistant, users can see the echo reply from the board:



Open the DHCP function in main.h, using a router to connect the board with the pc, users can see the automatic allocated ip address of the board from the HyperTerminal.

### 5.26.2. Raw\_tcpudp

### **DEMO** purpose

This demo includes the following functions of GD32 MCU:

- Learn to use Lwip stack.
- Learn to use raw API to handle with a task.
- Learn how to realize a tcp server.
- Learn how to realize a tcp client.
- Learn how to realize a udp server/client.
- Learn how to use DHCP to allocate ip address automatically.
- Learn to handle with received packet in polling mode and in interrupt mode.

This demo is based on the GD32470Z-EVAL-V1.0 board, it shows how to configure the enet peripherals to send and receive frames in normal mode and use lwip tcp / ip stack to realize ping, telnet and server/client functions.

JP4, JP5, JP6 must be fitted. JP13 jump to Usart.

It is configured in RMII mode, and 25MHz oscillator is used, the system clock is configured to 240MHz.

This demo realizes three applications:

- Telnet application, the eval board acts as tcp server. Users can link the client with the eval board server, using 8000 port. Users can see the reply from the server, and can send the name(should input enter key) to server.
- tcp client application, the eval board acts as tcp client. Users can link the eval board client with the server, using 10260 port. Users can send information from server to client, then the client will send back the information. If the server is not online at



first, or is break during process, when the server is ready again, users can press tamper key to reconnect with server, and communicate.

udp application. Users can link the eval board with another station, using 1025 port. Users can send information from station to board, then the board will send back the information.

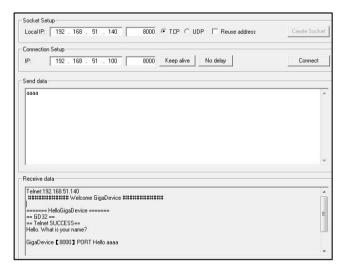
By default, the packet reception is polled in while(1). If users want to receive packet in interrupt service, uncomment the macro defined USE\_ENET\_INTERRUPT in main.h. If users need dhcp function, it can be configured from the private defines in main.h. This function is closed in default.

Note: Users should configure ip address, mask and gw of GD32470Z-EVAL-V1.0 board, or server according to the actual net situation from the private defines in main.h.

### **DEMO** running result

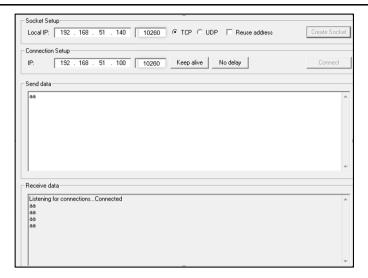
Download the program <Raw tcpudp> to the EVAL board.

Using Network assistant software, configure the pc side to tcp client, using 8000 port, and when send something through the assistant, users can see the reply from the server:

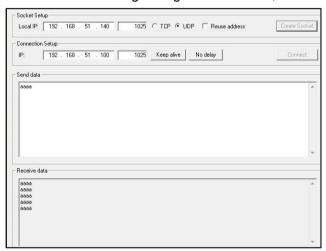


Using Network assistant software, configure the pc side to tcp server, using 10260 port, press the Tamper key, and when send something through the assistant, users can see the echo reply from the client:





Using Network assistant software, configure to use udp protocol, using 1025 port, and when send something through the assistant, users can see the echo reply from the board:



Open the DHCP function in main.h, using a router to connect the board with the pc, users can see the automatic allocated ip address of the board from the HyperTerminal.

### 5.26.3. Raw\_webserver

### **DEMO** purpose

This demo includes the following functions of GD32 MCU:

- Learn to use Lwip stack.
- Learn to use raw API to handle with a task.
- Learn how to realize a web server.
- Learn how to use a web server to control LEDs.
- Learn how to use a web server to monitor the board VREFINT voltage.
- Learn how to use DHCP to allocate ip address automatically.
- Learn to handle with received packet in polling mode and in interrupt mode.



This demo is based on the GD32470Z-EVAL-V1.0 board, it shows how to configure the enet peripherals to send and receive frames in normal mode and use lwip tcp / ip stack to realize webserver application.

JP4, JP5, JP6 must be fitted. JP13 jump to Usart.

It is configured in RMII mode, and 25MHz oscillator is used, the system clock is configured to 240MHz.

This demo realizes webserver application:

Users can visit the eval board through Internet Explorer, the eval board acts as a webserver, and the url is the local ip address of the eval board. There are two experiments realized, one is the LEDs control, the other one is the ADC monitoring VREFINT voltage in real-time.

If users need dhcp function, it can be configured from the private defines in main.h. This function is closed by default. Users can use a router to connect the eval board, and use the COM port to print the automatic allocated ip address, then connect your mobile phone to the wifi which the router send. Users can visit the eval board and control it on your mobile phone.

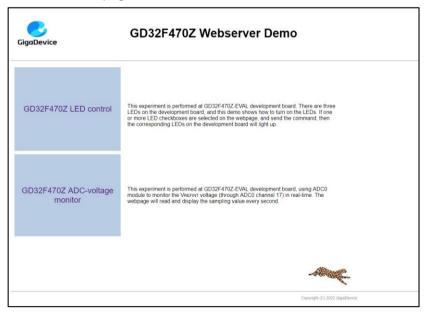
By default, the packet reception is polled in while(1). If users want to receive packet in interrupt service, uncomment the macro define USE ENET INTERRUPT in main.h.

Note: Users should configure ip address, mask and gw of GD32470Z-EVAL-V1.0 board according to the actual net situation from the private defines in main.h.

### **DEMO** running result

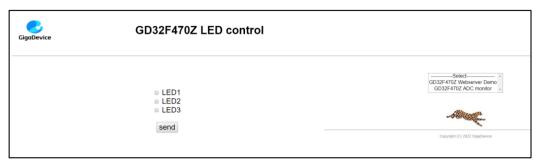
Download the program <Raw\_webserver> to the EVAL board, using Internet Explorer software, enter in the ip address of the board, click on the LED control linker, choose the LED checkboxes users want to light, and "send", the corresponding LEDs will light. Click on the ADC monitor linker, the real-time VREFINT voltage is showed on the webpage, and the data refreshes every second automatically.

The web home page shows as below:

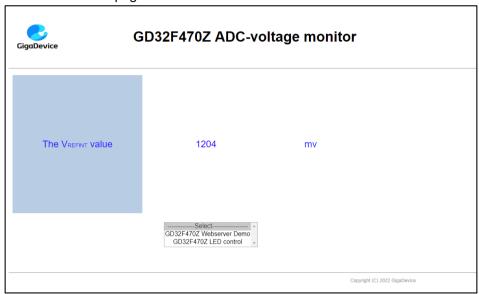




The LED control page shows as below:



The ADC monitor page shows as below:



Open the DHCP function in main.h, using a router to connect the board, and use the HyperTerminal to print the automatic allocated ip address, then connect your mobile phone to the wifi which the router send. Users can visit the eval board and control it on your mobile phone.

### 5.27. USB\_Device

### 5.27.1. HID\_Keyboard

### **DEMO Purpose**

This demo includes the following functions of GD32 MCU:

- Learn how to use the USBFS/USBHS peripheral mode
- Learn how to implement USB HID(human interface) device

GD32470Z-EVAL-V1.0 board has four keys one USB\_FS interface and one USB\_HS interface. The four keys are Reset key, Wakeup key, User key and Tamper key. In this demo, the GD32470Z-EVAL board is enumerated as an USB Keyboard, which uses the



native PC Host HID driver, as shown below. The USB Keyboard uses three keys(wakeup key, tamper key and user key) to output three characters ('b', 'a' and 'c'). In addition, the demo also supports remote wakeup which is the ability of a USB device to bring a suspended bus back to the active condition, and the wakeup key is used as the remote wakeup source.



### **DEMO Running Result**

According to the VBUSIG bit in USBFS\_GCCFG register, user can decide whether or not to jump JP13 to USB\_FS. Then connect the EVAL board to the PC through USB cable to the USB\_FS connector. After doing this, download the program <27\_USB\_Device\HID\_Keyboard> to the EVAL board and run. If you press the Wakeup key, will output 'b'. If you press the User key, will output 'c'. If you press the Tamper key, will output 'a'.

If you want to test USB remote wakeup function, you can do as follows:

- Manually switch PC to standby mode
- Wait for PC to fully enter the standby mode
- Push the Wakeup key
- If PC is ON, remote wakeup is OK, else failed.

#### 5.27.2. MSC Udisk

#### **DEMO Purpose**

This demo includes the following functions of GD32 MCU:

- Learn how to use the USBFS/USBHS peripheral mode
- Learn how to implement USB MSC(mass storage) device

This demo mainly implements a U disk. U disk is currently very widely used removable MSC devices. MSC, the Mass Storage device Class, is a transport protocol between a computer and mobile devices, which allow a universal serial bus (USB) equipment to access a host computing device, file transfer between them, mainly including mobile hard disk, mobile U disk drive, etc... The MSC device must have a storage medium, and this Demo uses the MCU's internal SRAM as the storage medium. For more details of the



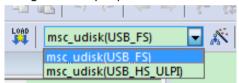
MSC protocol please refer to the MSC protocol standard.

MSC device will use a variety of transport protocols and command formats for communication, so it need to choose the appropriate protocol and command format in the realization of the application. This Demo selects the BOT (bulk only transport) protocol and the required SCSI (small computer interface) command, and is compatible with a wide variety of Window operating systems. Specific BOT protocol and SCSI command specification please refer to the standard of their agreement.

In the workspace toolbar, user should select the project configuration,

#### In the keil4:

- GD32470Z\_EVAL\_USBFS: to configure the project for GD32F470Z devices using USBFS peripheral
- GD32470Z\_EVAL\_USBHS (ULPI): to configure the project for GD32F470Z devices using USBHS peripheral



#### In the IAR:

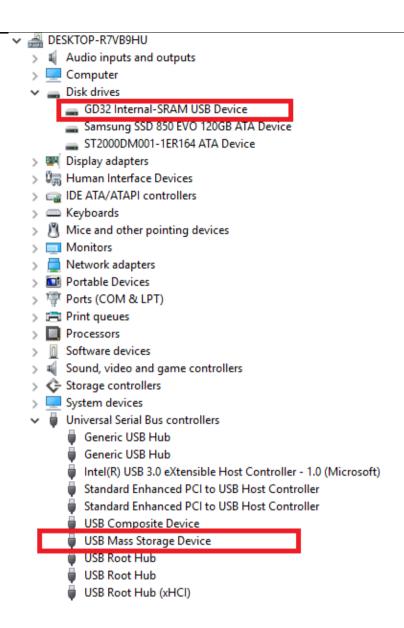
- USBFS: to configure the project for GD32F470Z devices using USBFS peripheral
- USBHS: to configure the project for GD32F470Z devices using USBHS peripheral



# **DEMO Running Result**

According to the VBUSIG bit in USBFS\_GCCFG register, user can decide whether or not to jump JP13 to USB\_FS if user use USBFS core. Then connect the EVAL board to the PC through USB cable to the USB\_FS connector if user use USBFS core or the USB\_HS\_ULPI connector if use USBHS core. After doing this, download the program <27\_USB\_Device\MSC\_Udisk> to the EVAL board and run. When the EVAL board connect to the PC, you will find a USB large capacity storage device is in the universal serial bus controller, and there is 1 more disk drives in the equipment manager of PC, as shown below:





Then, after opening the resource manager, you will see more of the 1 disk, as shown in the following diagram:



At this point, the write/read/formatting operation can be performed as the other mobile devices.



#### 5.28. **USB Host**

#### 5.28.1. **HID Host**

#### **DEMO Purpose**

This demo includes the following functions of GD32 MCU:

- Learn to use the USBFS/USBHS as a HID host
- Learn the operation between the HID host and the mouse device
- Learn the operation between the HID host and the keyboard device

GD32470Z-EVAL-V1.0 evaluation board integrates the USBFS module and USBHS module, and the module can be used as a USB device, a USB host or an OTG device. This demo mainly shows how to use the USBFS/USBHS as a USB HID host to communicate with external USB HID device.

#### **DEMO Running Result**

Jump the JP13, JP14 and the JP15 to USB FS. Then download the program <28 USB Host\HID Host> to the EVAL board and run.

If a mouse has been attached, the user will see the information of mouse enumeration. First pressing the user key will see the inserted device is mouse, and then moving the mouse will show the movement of mouse in the HyperTerminal.

```
۸
##### USB Host library started #####
> Device Attached.
 Reset the USB device.
 Low speed device detected.
> VID: 046Dh
 PID: C077h
> HID device connected.
> Manufacture string is : Logitech
  Product string is: USB Optical Mouse
 Serial Number string is : N/A
> Enumeration completed.
  To start the HID class operations:
> Press User Key...
> Wait for user input!
 User has input!
> HID Demo Device : Mouse.
MoveRight 32 units---*---MoveUp 7f units---*---No button is pressed.
MoveRight 0 units---*--MoveUp 14 units---*--No button is pressed.

MoveRight 1 units---*---MoveUp 1 units---*--No button is pressed.
MoveRight 3 units---*---MoveDown 0 units---*---No button is pressed.
MoveRight 1 units---*---MoveDown 0 units---*---No button is pressed.
MoveRight 2 units---*---MoveDown 0 units---*---No button is pressed.
MoveRight 5 units---*---MoveDown 0 units---*--No button is pressed.
MoveRight 6 units---*--MoveDown 1 units---*--No button is pressed.
MoveRight 9 units---*---MoveDown 2 units---*---No button is pressed.
MoveRight 9 units---*---MoveDown 1 units---*--No button is pressed.
MoveRight 8 units---*--MoveDown 1 units---*--No button is pressed.
MoveRight 6 units---*---MoveDown 1 units-
                                            --*---No button is pressed.
```

If a keyboard has been attached, the user will see the information of keyboard



enumeration. First pressing the user key will see the inserted device is keyboard, and then pressing the keyboard, the input will be printed in the HyperTerminal.

```
##### USB Host library started #####
 Device Attached.
 Reset the USB device.
 Low speed device detected.
 VID: 413Ch
> PID: 2003h
> HID device connected.
 Manufacture string is : Dell
 Product string is : Dell USB Keyboard
 Serial Number string is : N/A
 Enumeration completed.
 To start the HID class operations:
> Press User Key...
 Wait for user input!
> User has input!
> HID Demo Device : Keyboard.
The pressed button is
The pressed button is h
The pressed button is e
The pressed button is 1
The pressed button is 1
The pressed button is o
```

#### 5.28.2. MSC\_Host

#### **DEMO Purpose**

This demo includes the following functions of GD32 MCU:

- Learn to use the USBFS/USBHS as a MSC host
- Learn the operation between the MSC host and the Udisk

GD32470Z-EVAL-V1.0 evaluation board integrates the USBFS module and the USBHS module, and the module can be used as a USB device, a USB host or an OTG device. This demo mainly shows how to use the USBFS and USBHS as a USB MSC host to communicate with external Udisk.

#### **DEMO Running Result**

Jump the JP13, JP14 and the JP15 to USB\_FS or jump the JP16-20, JP23, P5 and P6 to USB\_HS, then insert the OTG cable to the USB port, download the program <28\_USB\_Host\MSC Host > to the EVAL board and run.

If an Udisk has been attached, the user will see the information of Udisk enumeration. First pressing the user key will see the Udisk information, next pressing the tamper key will see the root content of the Udisk, then press the wakeup key will write file to the Udisk, finally the user will see information that the MSC host demo is end.



# 6. Revision history

Table 6-1 Revision history

Revision No.	Description	Date
2.0	Initial Release	May.26, 2022



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