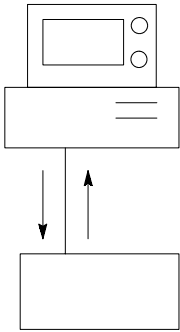

MEWTOCOL

1.1	MEWTOCOL - COM()	1 - 2
1.1.1	MEWTOCOL - COM		1-2
1.1.2			1-6
1.1.3	MEWTOCOL - COM		...	1-8
1.2	MEWTOCOL - DAT()	1 - 24
1.2.1	MEWTOCOL - DAT		1-24
1.2.2	MEWTOCOL - DAT		1-26
1.3	MEWTOCOL		1 - 30
1.3.1			1-30

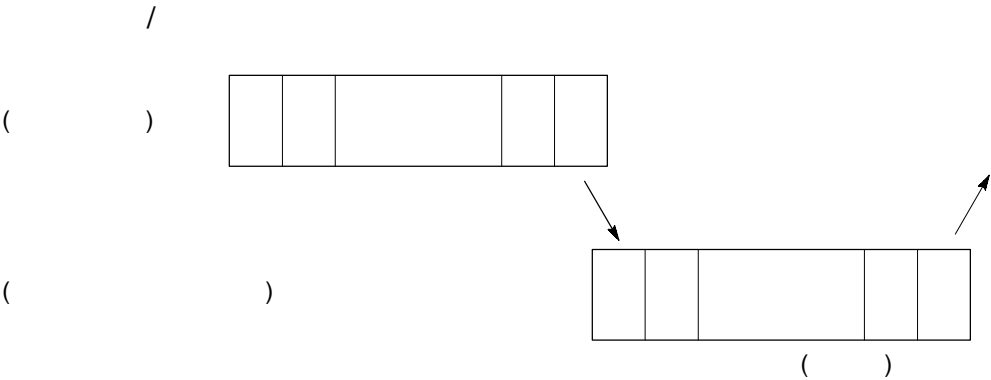
1.1 MEWTOCOL - COM()

1.1.1 MEWTOCOL - COM

/ () , ()
가 .



:





		ASCII	
	% <	25H 3CH	.
	#	23H	.
()	\$	24H	.
()	!	21H	.
	C _R	0DH	.
	&(+C _R)	26H	.



AD (H),(L)
 2 10 01 32(ASCII)
 UNIT No. ,
 (H) , (L) PC UNIT No.
 , "01"
 , FF(ASCII) ()
 ,



BCC (H),(L)
 2 16 00 FF(ASCII)
 , BCC ** , BCC 가
 BCC가



Err (H),(L)
 2 16 00 FF(ASCII)
 .

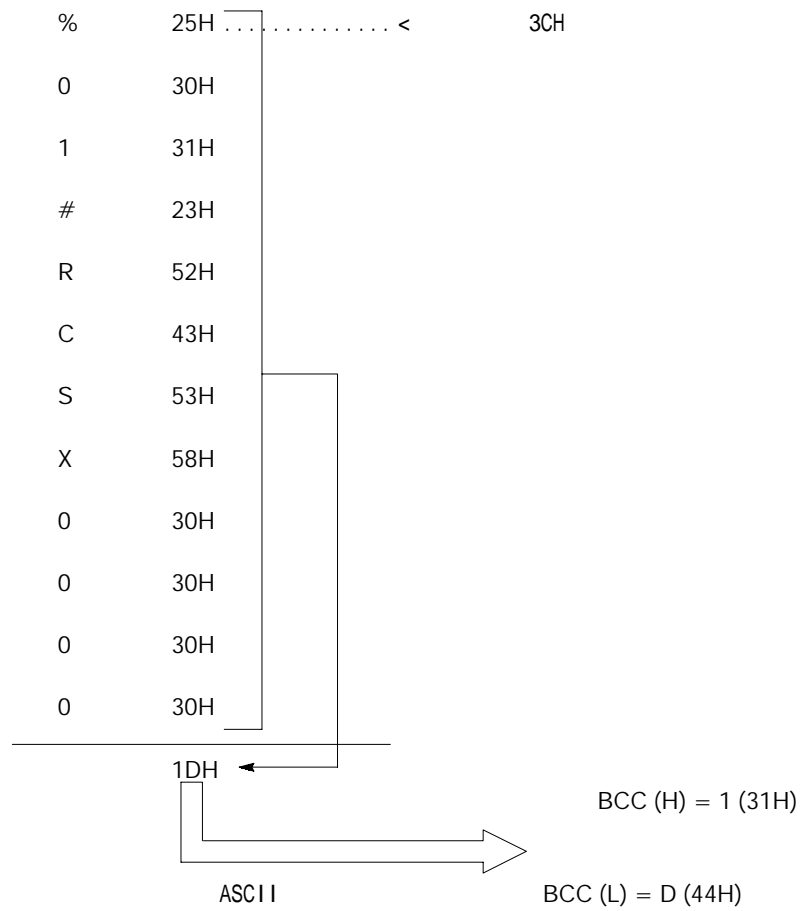
BCC()

· BCC ,

· BCC , (%) , 8
ASCII 2

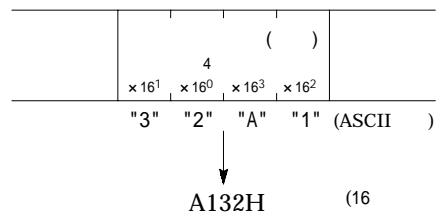
)

% 0 1 # R C S X 0 0 0 0 1 D c r
↑ ↑ ↑ ↑ ↑ ↑
1 X No.0 BCC
() 2

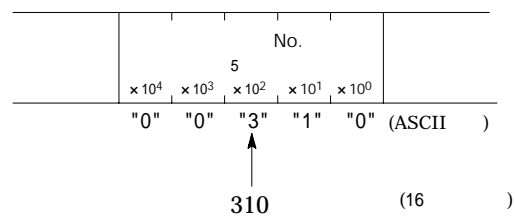


16 / 3 가 .

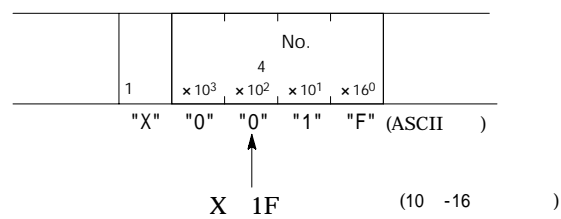
16
 $\times 16^0, \times 16^1$, 16
 () (RD)



10
 $\times 10^0, \times 10^1$, 10
 () (RD)

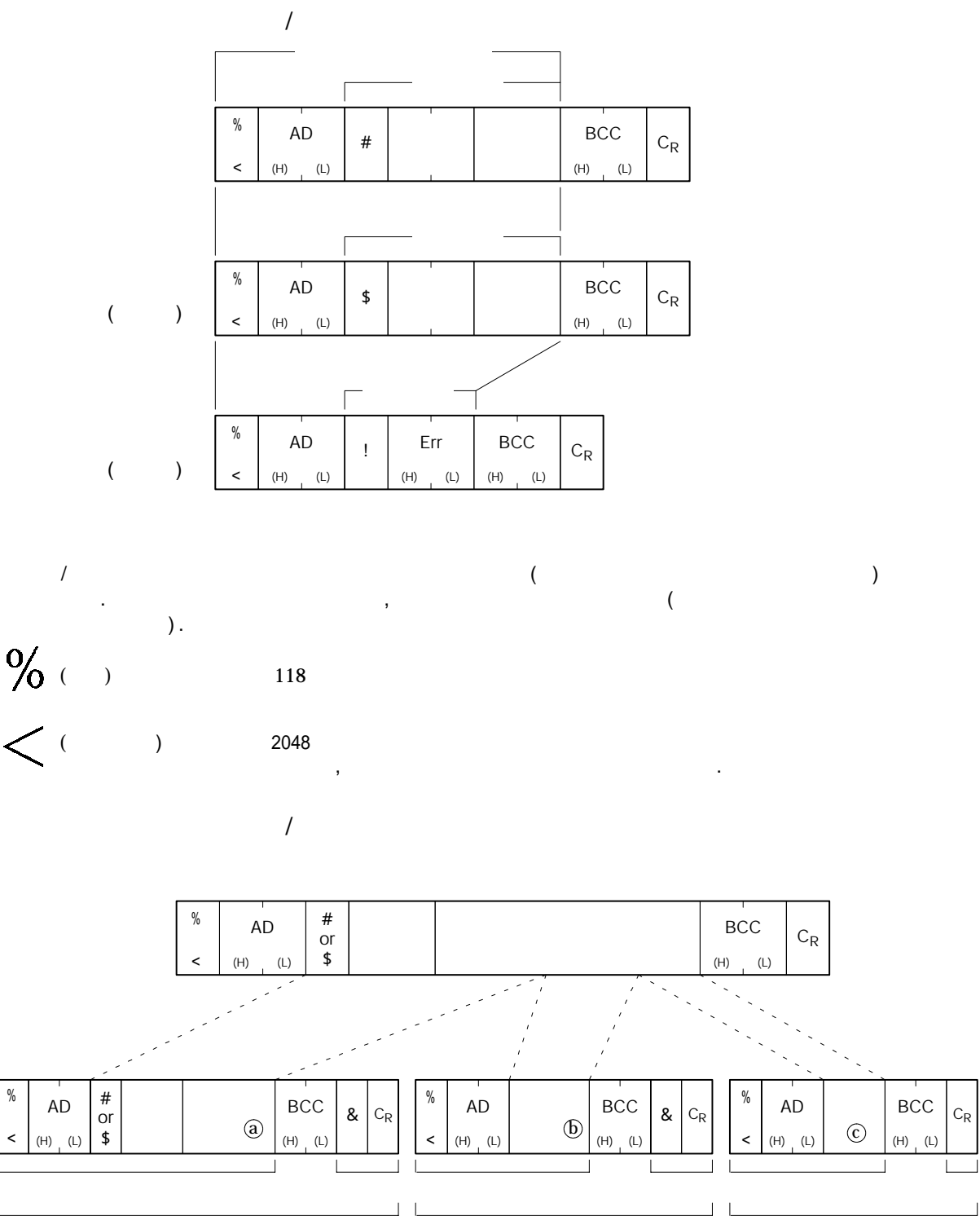


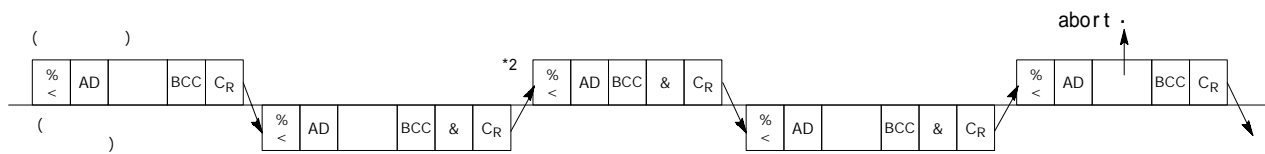
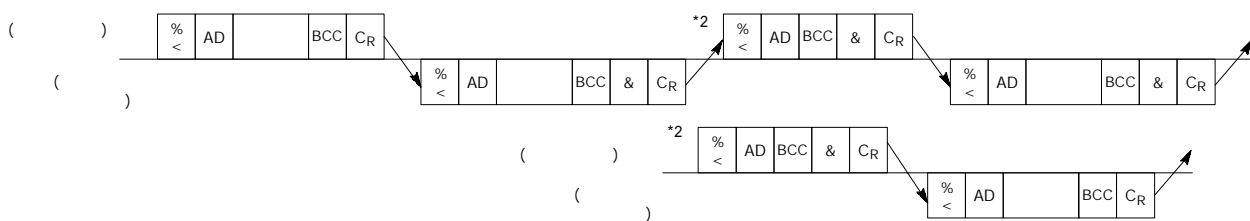
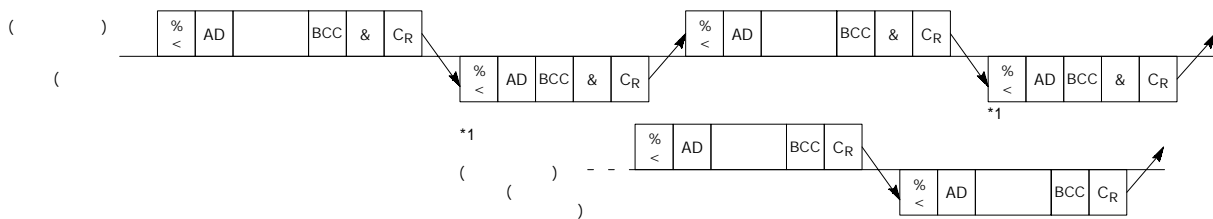
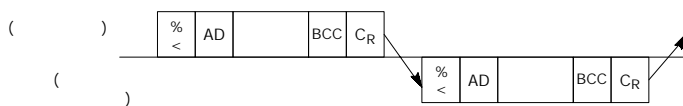
10 -16
 (X), (Y), (R), (L)
 가 , , 10 가 . (T/C , 16
 10 .)
 $\times 16^0, \times 10^1, \times 10^2$
 () (RCS)



:
 ,
 0 , 4 , X1F
 「 No. 」 , 4 (4)
 No. 「 001F 」

1.1.2





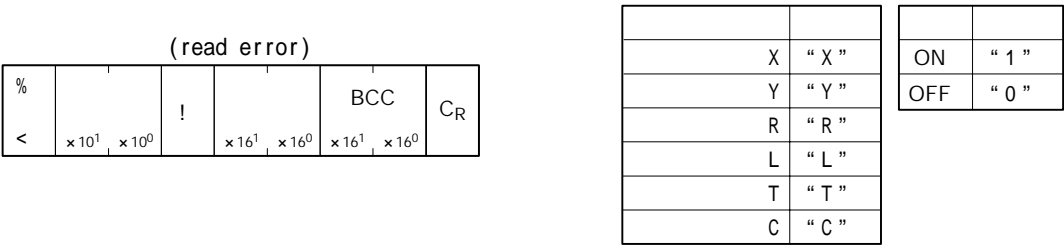
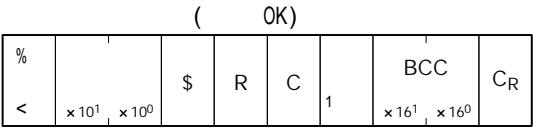
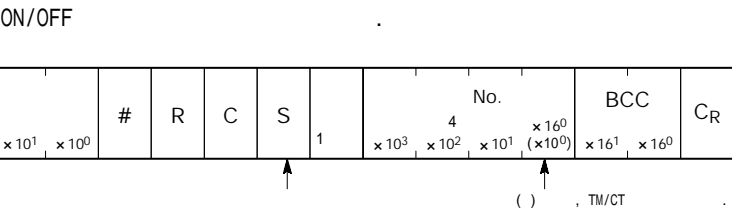
(*1)

(*2)

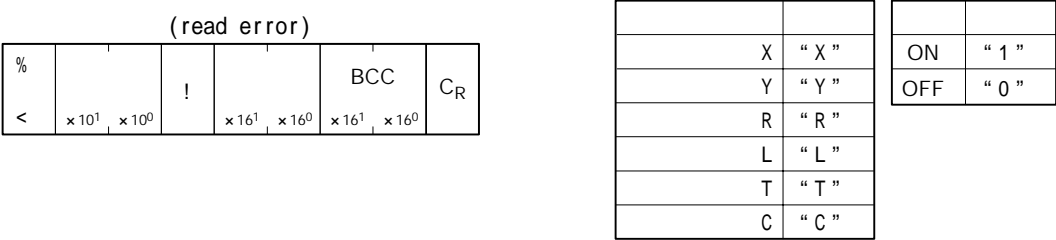
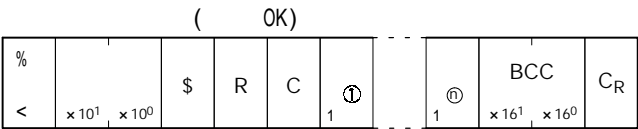
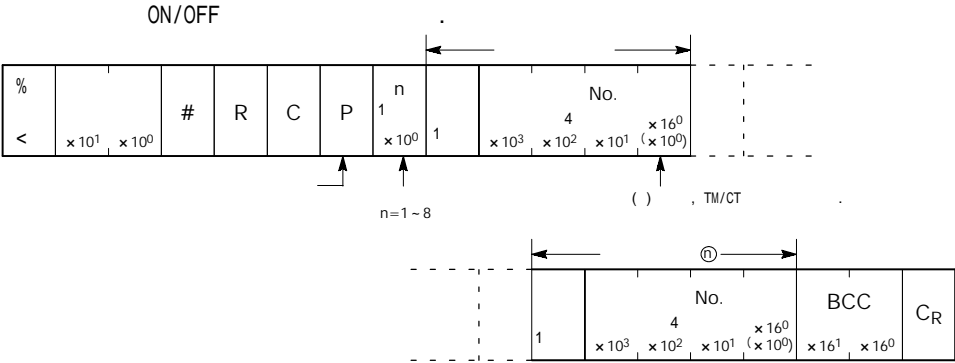
1.1.3 MEWTOCOL-COM

	RC (RCS) (RCP) (RCC)	ON/OFF
	WC (WCS) (WCP) (WCC)	ON/OFF
	RD	
	WD	
/	RS	/
/	WS	/
/	RK	/
/	WK	/
	MC	
	MD	
	MG	
() pre-set	SC	16 ON/OFF
() pre-set	SD	
	RR	
	WR	
PLC Status	RT	
	RM	
abort	AB	

[RCS] ()



[RCP] ()



[RCC] ()

ON/OFF .

%			#	R	C	C	1	No.				No.				BCC		C _R
<	$\times 10^1$	$\times 10^0$						$\times 10^3$	$\times 10^2$	$\times 10^1$	$\times 10^0$	$\times 10^3$	$\times 10^2$	$\times 10^1$	$\times 10^0$	$\times 16^1$	$\times 16^0$	

(OK)																16		
%			\$	R	C				()								BCC	C _R
<	x10 ¹	x10 ⁰							x16 ¹	x16 ⁰	x16 ³	x16 ²	x16 ¹	x16 ⁰	x16 ³	x16 ²	x16 ¹	x16 ⁰

(read error)																		
%			!														BCC	C _R
<	x10 ¹	x10 ⁰							x16 ¹	x16 ⁰	x16 ³	x16 ²	x16 ¹	x16 ⁰				

X	“ X ”	
Y	“ Y ”	
R	“ R ”	
L	“ L ”	
T	“ T ”	
C	“ C ”	

[WCS] ()

ON/OFF .

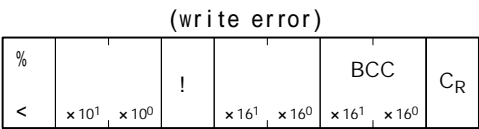
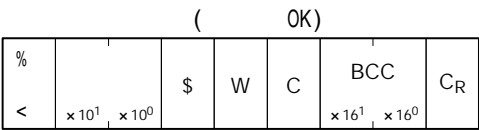
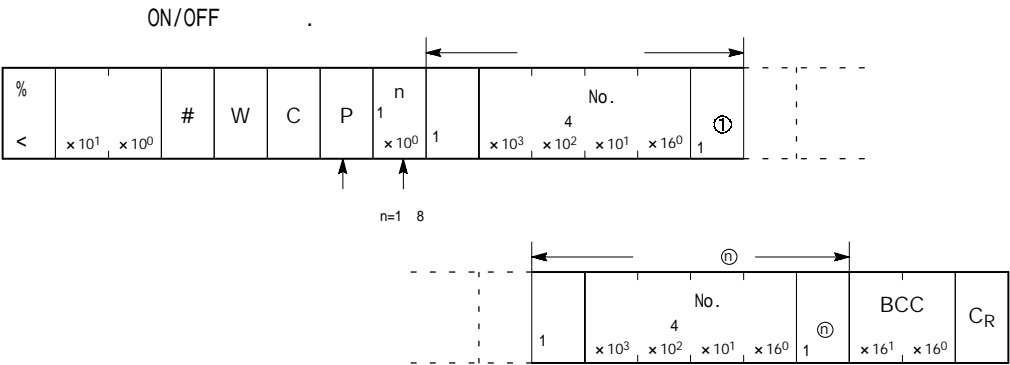
%			#	W	C	S	1		No.				BCC	C _R
<	x10 ¹	x10 ⁰							x10 ³	x10 ²	x10 ¹	x16 ⁰ (x10 ⁰)	x16 ¹	x16 ⁰

(OK)																		
%			\$	W	C												BCC	C _R
<	x10 ¹	x10 ⁰							x16 ¹	x16 ⁰								

(write error)																		
%			!														BCC	C _R
<	x10 ¹	x10 ⁰							x16 ¹	x16 ⁰	x16 ³	x16 ²	x16 ¹	x16 ⁰				

X	“ X ”		ON	“ 1 ”
R	“ R ”		OFF	“ 0 ”
L	“ L ”			

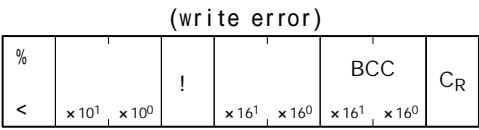
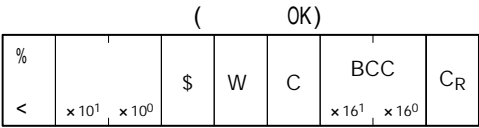
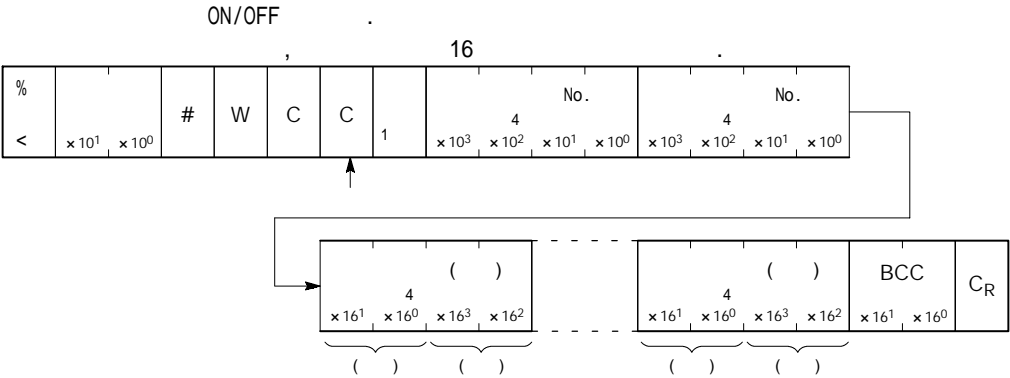
[WCP] ()



	Y	" Y "	
	R	" R "	
	L	" L "	

ON	" 1 "
OFF	" 0 "

[WCC] ()



	Y	" Y "	
	R	" R "	
	L	" L "	

DT, LD, FL

%			#	R	D		No.					No.					BCC	C _R
<	$\times 10^1$	$\times 10^0$			1		$\times 10^4$	$\times 10^3$	$\times 10^2$	$\times 10^1$	$\times 10^0$	$\times 10^4$	$\times 10^3$	$\times 10^2$	$\times 10^1$	$\times 10^0$	$\times 16^1$	$\times 16^0$

(OK)

%			\$	R	D		()					()				BCC	C _R
<	$\times 10^1$	$\times 10^0$					$\times 16^1$	$\times 16^0$	$\times 16^3$	$\times 16^2$		$\times 16^1$	$\times 16^0$	$\times 16^3$	$\times 16^2$	$\times 16^1$	$\times 16^0$
							()		()			()		()			

(read error)

%			!				BCC		C _R
<	$\times 10^1$	$\times 10^0$				$\times 16^1$	$\times 16^0$	$\times 16^1$	$\times 16^0$

DT	" D "
LD	" L "
FL	" F "

%			#	R	D											BCC	C _R
<	$\times 10^1$	$\times 10^0$			2		0	0	0	0	0	0	0	0		$\times 16^1$	$\times 16^0$
							9										

(OK)(I0 I1)

%			\$	R	D		()					BCC		C _R
<	$\times 10^1$	$\times 10^0$					$\times 16^1$	$\times 16^0$	$\times 16^3$	$\times 16^2$		$\times 16^1$	$\times 16^0$	
							()		()			()		

(OK)(I0 I1)

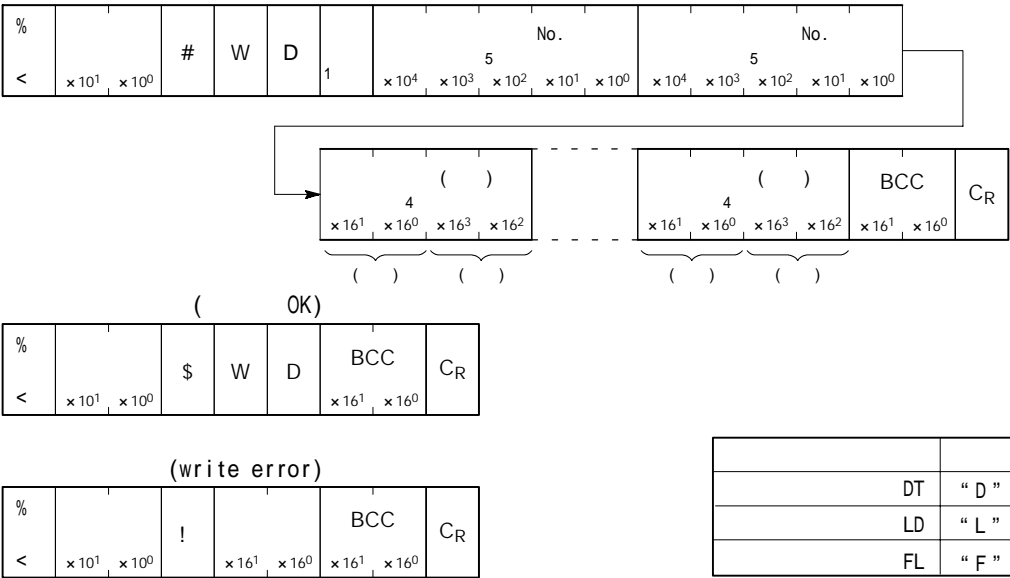
%			\$	R	D		(I0)					(I1)				BCC	C _R
<	$\times 10^1$	$\times 10^0$					$\times 16^1$	$\times 16^0$	$\times 16^3$	$\times 16^2$		$\times 16^1$	$\times 16^0$	$\times 16^3$	$\times 16^2$	$\times 16^1$	$\times 16^0$
							()		()			()		()			

(read error)

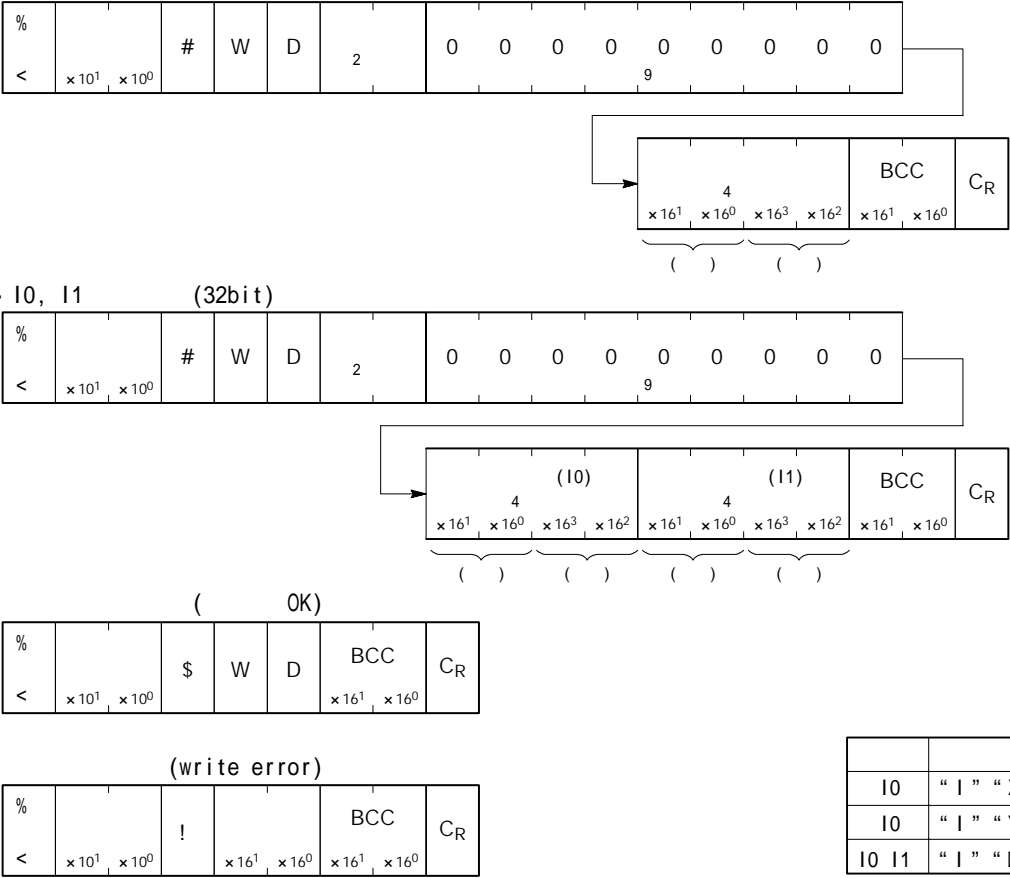
%			!				BCC		C _R
<	$\times 10^1$	$\times 10^0$				$\times 16^1$	$\times 16^0$	$\times 16^1$	$\times 16^0$

I0	" I " " X "
I1	" I " " Y "
I0,I1	" I " " D "

DT, LD, FL



· I0, I1



/ .

%		#	R	S	/ No.				/ No.				BCC	C_R
<	$\times 10^1$	$\times 10^0$			$\times 10^3$	$\times 10^2$	$\times 10^1$	$\times 10^0$	$\times 10^3$	$\times 10^2$	$\times 10^1$	$\times 10^0$	$\times 16^1$	$\times 16^0$

(OK)

%			\$	R	S	()				()				BCC	C _R
<	$\times 10^1$	$\times 10^0$				$\times 16^1$	$\times 16^0$	$\times 16^3$	$\times 16^2$	$\times 16^1$	$\times 16^0$	$\times 16^3$	$\times 16^2$	$\times 16^1$	$\times 16^0$
						()				()					

```
(read error)
```

%					BCC	C_R
<	$\times 10^1$	$\times 10^0$		$\times 16^1$	$\times 16^0$	

/ .

%						/	No.		/	No.
<	$\times 10^1$	$\times 10^0$	#	W	S	4			4	
							$\times 10^3$	$\times 10^2$	$\times 10^1$	$\times 10^0$

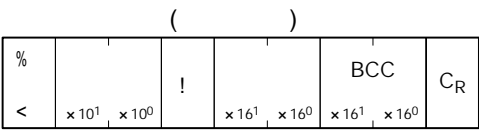
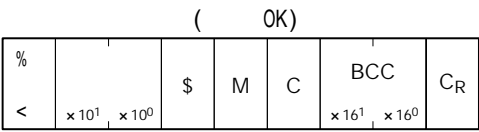
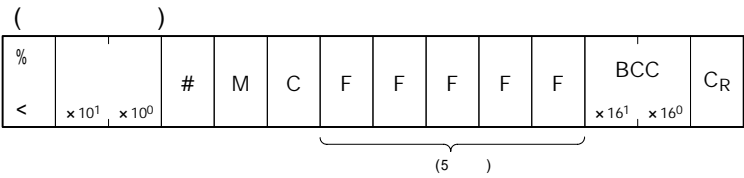
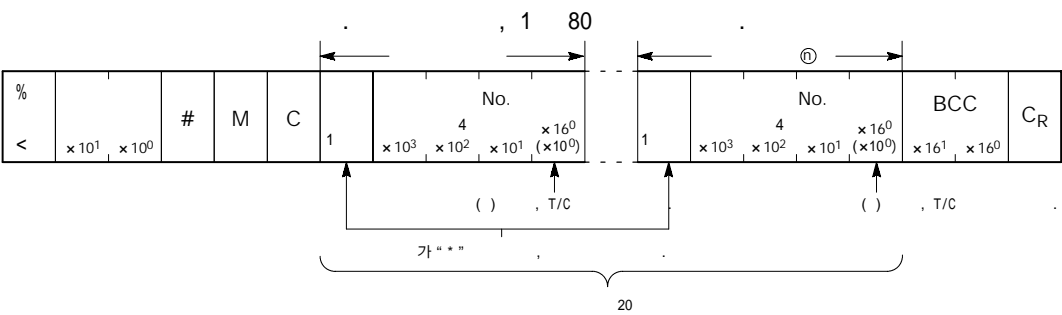
Figure 1 illustrates the bit-level structure of the proposed 16-bit multiplier. It shows two 16-bit inputs, A and B, each divided into four 4-bit segments. Segment 4 (bits 12-15) is the most significant, followed by segment 3 (bits 8-11), segment 2 (bits 4-7), and segment 1 (bits 0-3) as the least significant. An arrow indicates a carry from the least significant segment of input A to the least significant segment of input B. The output is a 32-bit result, with the first 16 bits corresponding to the product of the most significant 4-bit segments of A and B, and the last 16 bits corresponding to the product of the least significant 4-bit segments of A and B, plus a carry-in (CR).

(OK)

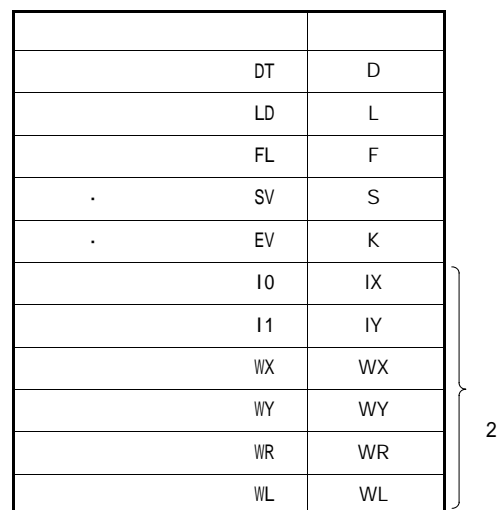
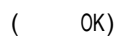
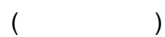
%			\$	W	S	BCC	C_R
<	$\times 10^1$	$\times 10^0$				$\times 16^1$	$\times 16^0$

```
(write error)
```

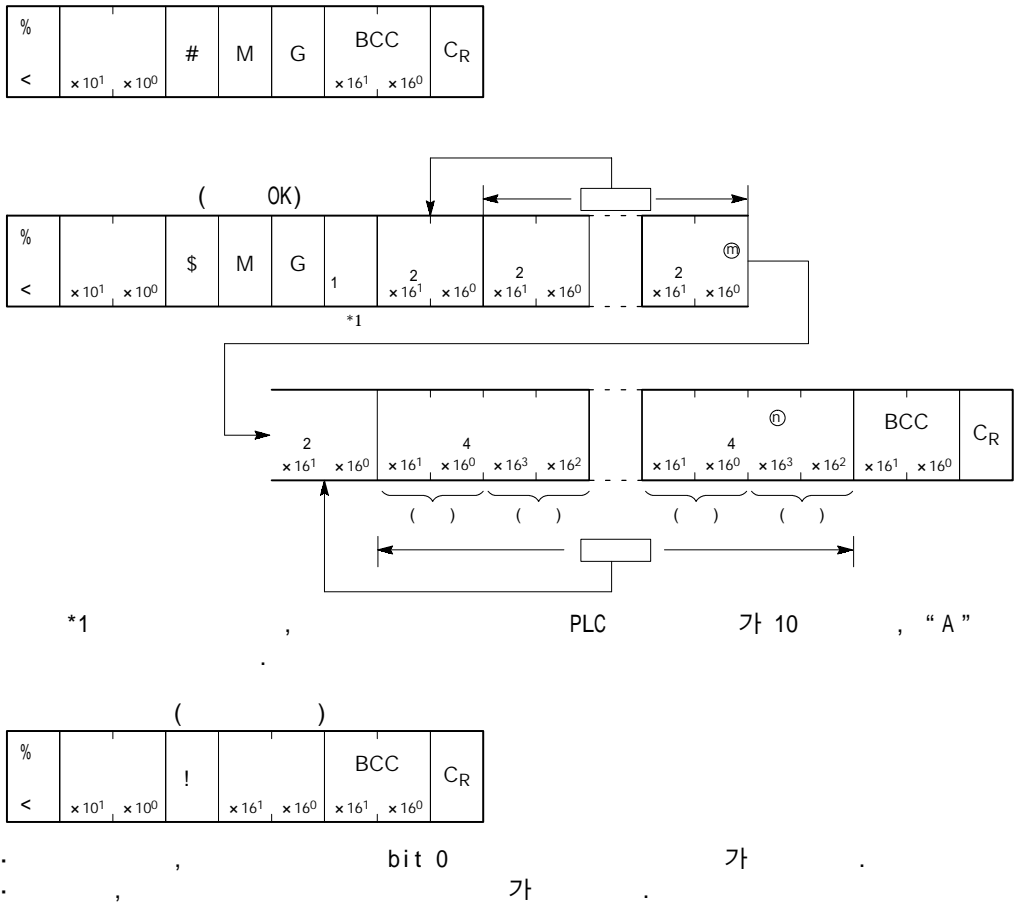
%					BCC	C_R	
<	$\times 10^1$	$\times 10^0$!	$\times 16^1$	$\times 16^0$		$\times 16^1$



X	" X "
Y	" Y "
R	" R "
L	" L "
T	" T "
C	" C "

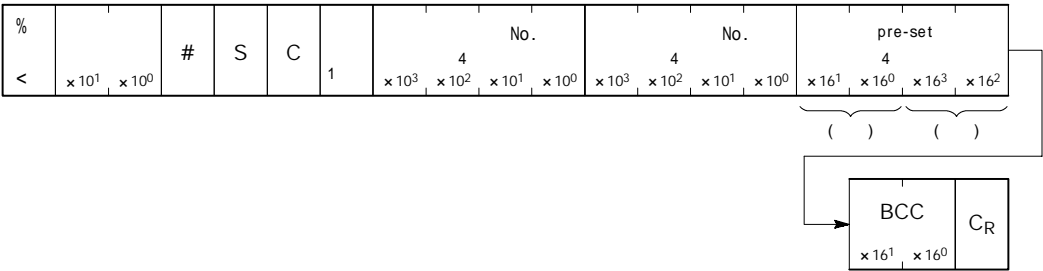


1-17

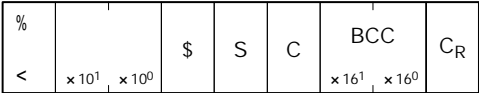


[SC]pre-set()

16 ON/OFF .



(pre-set OK)

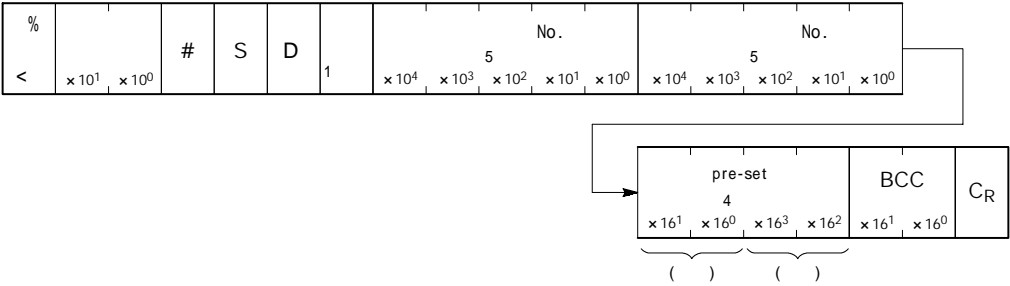


Y	“ Y ”
R	“ R ”
L	“ L ”

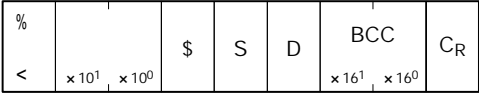
(pre-set)



[SD]pre-set

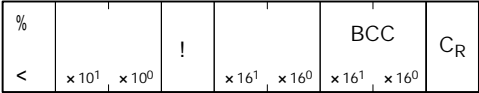


(pre-set OK)



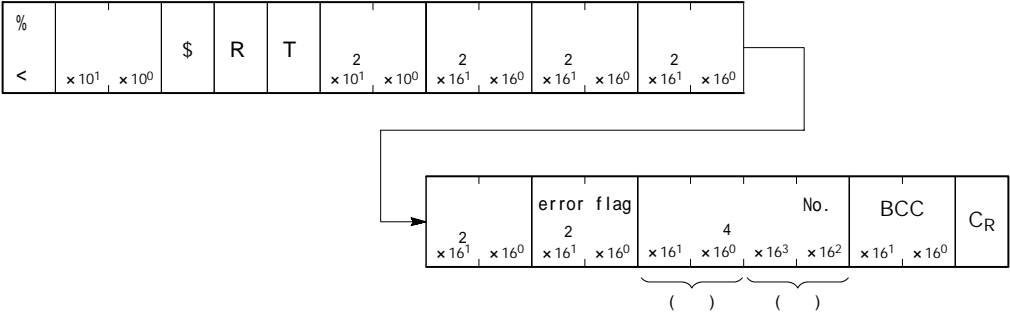
DT	“ D ”
LD	“ L ”
FL	“ F ”

(pre-set)



%			#	R	T	BCC	C _R
<	x10 ¹	x10 ⁰				x16 ¹	x16 ⁰

(OK)



(read error)

%			!			BCC	C _R
<	x10 ¹	x10 ⁰				x16 ¹	x16 ⁰

CPU102

20	FP2・FP2SH

CPU102

: 15 Ver. 1.5

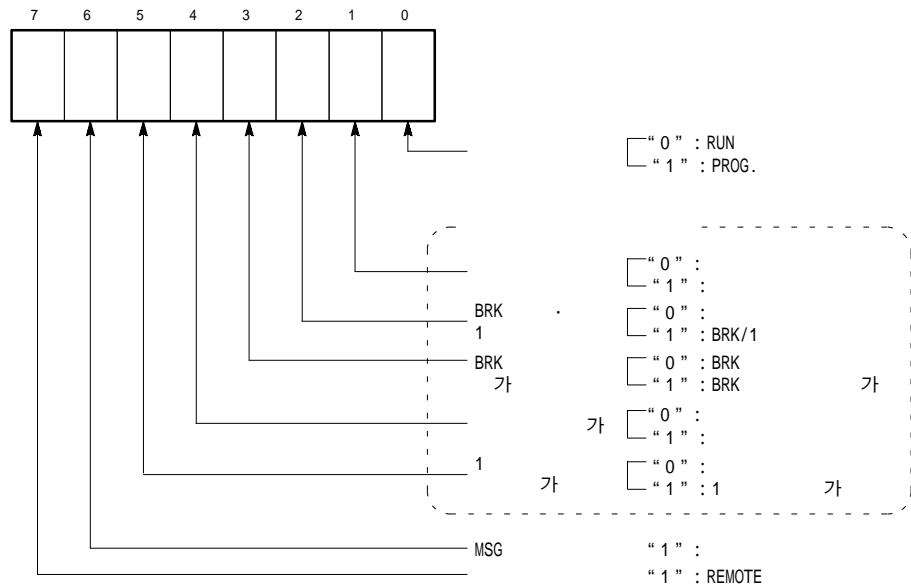
No.0102()

K

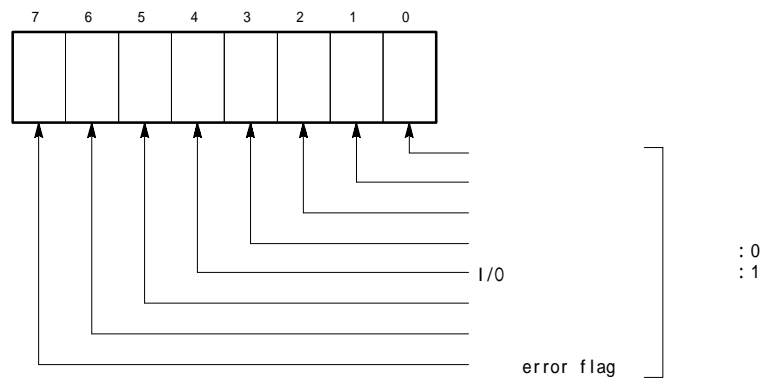
02	2K	1,534
n		1,024×n - 512 - 2 (: n=8 , 7678)
16	16K	15,870
32	32K	32,254

) FP2SH “ 0 ”

R9020 R9027 16 2
 (RUN/PROG. /REMOTE),
 CPU 가 가

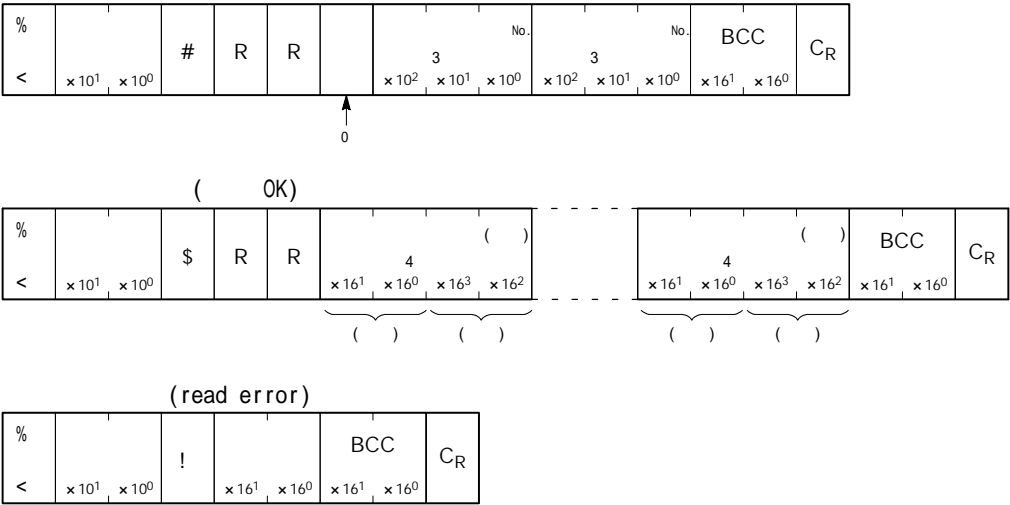


error flag
 8 () error flag(), R9000 R9007 16 2

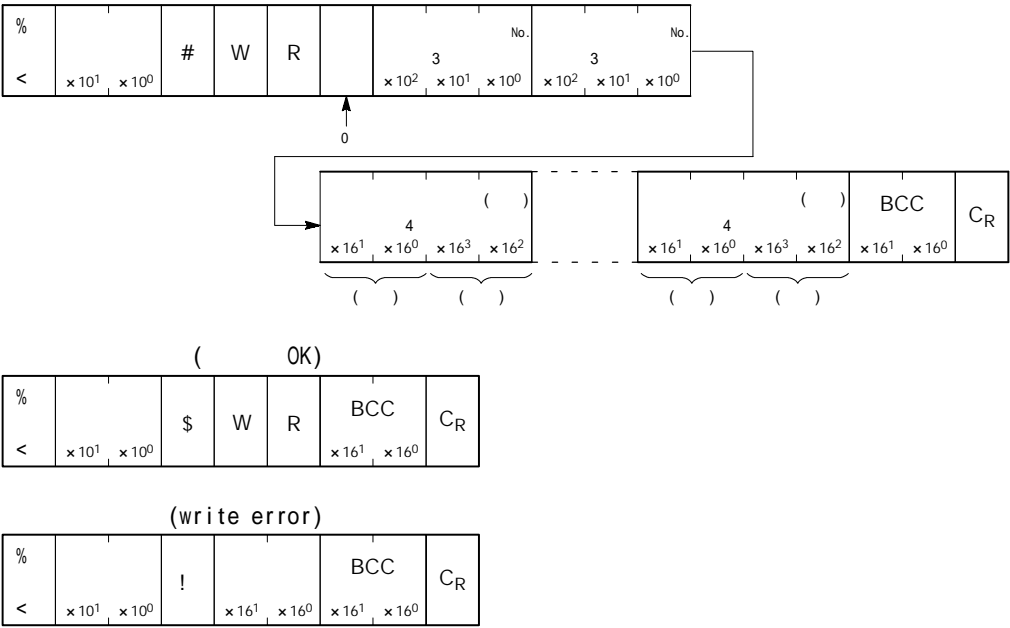


16 4
 10
 , 16
 " 2 D00 " ,
 10 " 45 " ()가 " 2 D " ,
 가 " 0000 "

[RR]



[WR]



[RM]

가

REMOTE

%			#	R	M		BCC	C _R
<	$\times 10^1$	$\times 10^0$			1		$\times 16^1$	$\times 16^0$

(OK)

%			\$	R	M		BCC	C _R
<	$\times 10^1$	$\times 10^0$					$\times 16^1$	$\times 16^0$

“ R ”	PROGRAM RUN ()
“ P ”	RUN PROGRAM ()

()

%			!				BCC	C _R
<	$\times 10^1$	$\times 10^0$		$\times 16^1$	$\times 16^0$	$\times 16^1$	$\times 16^0$	

[AB] abort

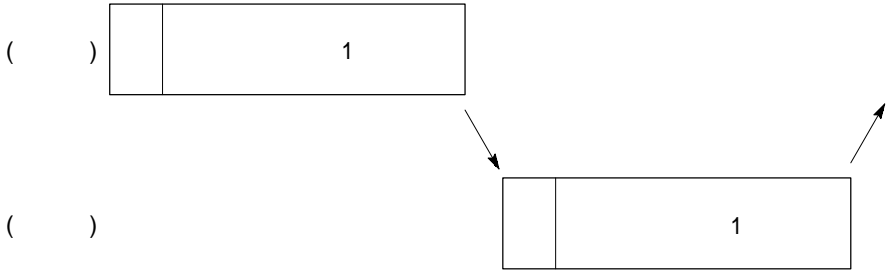
()

%			\$	A	B		BCC	C _R
<	$\times 10^1$	$\times 10^0$					$\times 16^1$	$\times 16^0$

1.2 MEWTOCOL-DAT()

1.2.1 MEWTOCOL - DAT

/



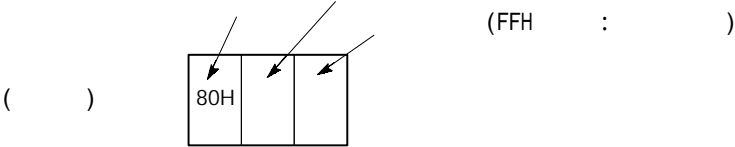
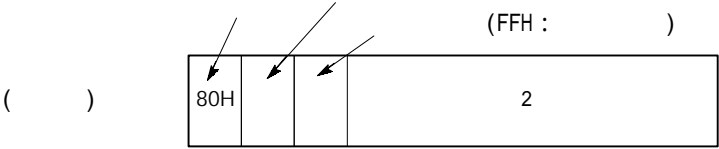
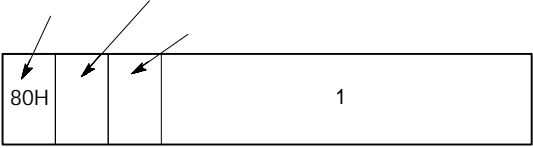
:
· , 가 .
· binary .
· .
· 1020
· PC , SEND() , RECV() .
가 .






· ET - LAN MEWTOCOL , MEWTOCOL - DAT
· 가 .

ET - LAN	MEWTOCOL /
----------	---------------

· , .



50H		D0H
51H		D1H
52H		D2H
53H		D3H

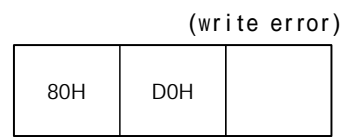
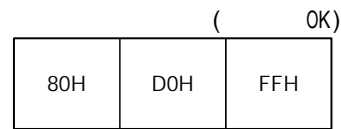
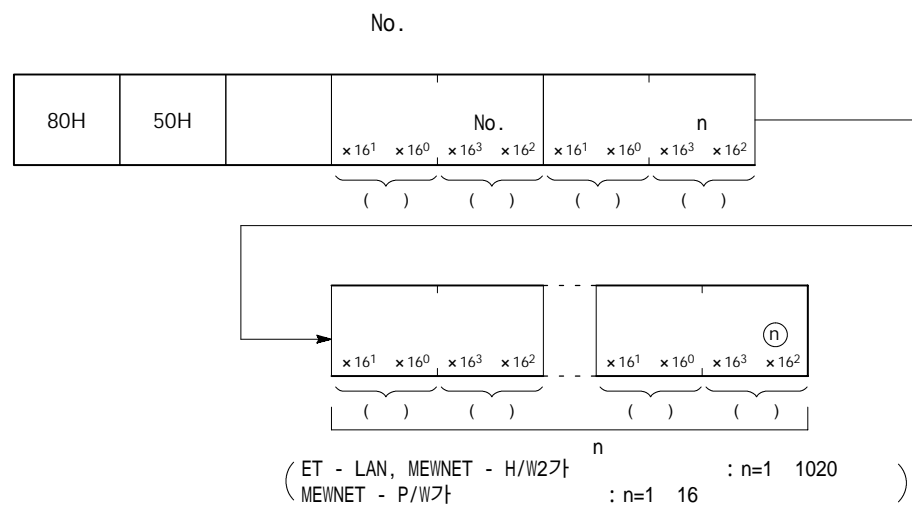
(1) (0 1)

FFH

(P.1-30 「MEWTOCOL」)

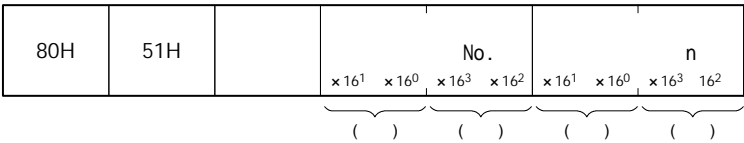
1.2.2 MEWTOCOL-DAT

[50H]

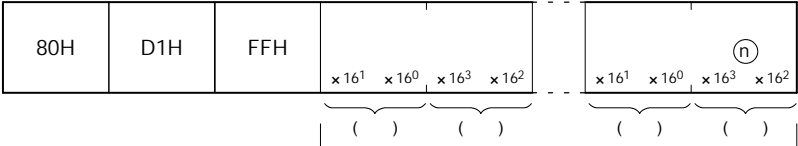


(WL)	00
(WR)	01
(WY)	02
(WX)	03
/ (SV)	04
/ (EV)	05
(LD)	06
(WR)	07
(DT)	08
(DT)	09
(FL)	0A

No.



(OK)



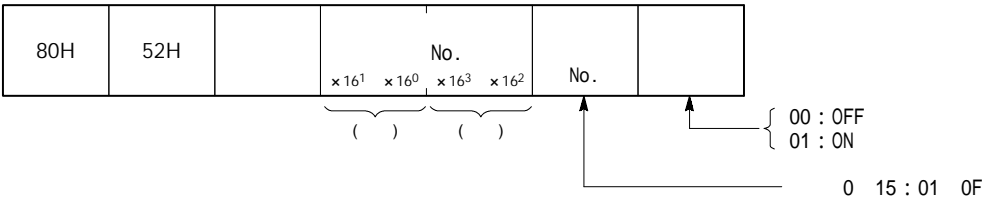
n

(ET - LAN, MEWNET - H/W2가 : n=1 1020)
MEWNET - P/W가 : n=1 16)

(read error)



(WL)	00
(WR)	01
(WY)	02
(WX)	03
/ (SV)	04
/ (EV)	05
(LD)	06
(WR)	07
(DT)	08
(DT)	09
(FL)	0A



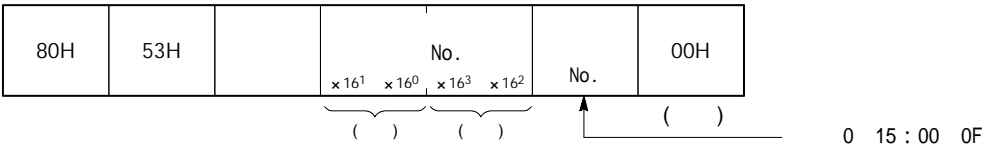
(OK)

80H	D2H	FFH
-----	-----	-----

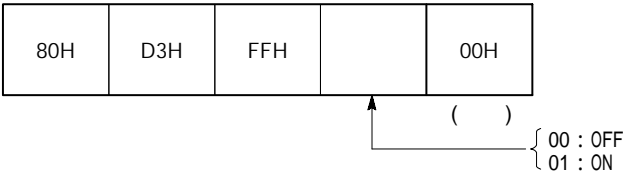
(write error)

80H	D2H	
-----	-----	--

(WL)	00
(WR)	01
(WY)	02
(WX)	03
/ (SV)	04
/ (EV)	05
(LD)	06
(WR)	07
(DT)	08
(DT)	09
(FL)	0A



(OK)



(read error)



(WL)	00
(WR)	01
(WY)	02
(WX)	03
/ (SV)	04
/ (EV)	05
(LD)	06
(WR)	07
(DT)	08
(DT)	09
(FL)	0A

1.3 MEWTOCOL

1.3.1

22H	WACK	< > 가 .
23H	MEWTOCOL	MEWTOCOL < > MEWTOCOL .
24H	ETLAN	< > OFF ON . , . . , .
26H	MEWTOCOL	MEWTOCOL 01 64 가 . < > MEWTOCOL 01 64 .
27H	NOT	< > .
28H		< > .
30H		< > 가 가 .
32H	가	< > 가 , .
33H		MEWNET - H 가 < > 가 OFF , . 가 ON .
36H		. 가 < > . . .
38H		< > .

1. 2 .
2. () , 가 , PC , .

40H	BCC	<p>< > BCC 가 .</p> <p>< ></p>
41H		<p>< ></p> <p>. 가 .</p> <p>< ></p> <p>. 가 .</p> <p>< ></p> <p>,</p>
42H	NOT	<p>< ></p> <p>< ></p> <p>,</p>
43H		<p>< > (PC) (가</p> <p>)</p> <p>< ></p>

50H		<p>< > No. .</p> <p>< > No. .</p>
51H		<p>< > () , 가</p> <p>< ></p>
52H		<p>< ></p> <p>< > OFF ON .</p> <p>. , .</p> <p>, ,</p>
53H	Busy	<p>< ></p> <p>< ></p>

60H		<p>< > 가 . (X, Y, D...) (0, 1, 2...)가 < ></p>
61H		<p>< > No. , , No. , (BCD, HEX...) < , , > 가 < > ,</p>
62H		<p>< > (,)... , < ></p>
63H		<p>< > 가, < ></p>
65H		<p>< > < > ,</p>
66H		<p>< > ((BCD, HEX...) , , ...) < ></p>
67H		<p>< > 가 (< >)</p>
72H		<p>< > WAIT < ></p>
73H		<p>< > WAIT < ></p>
74H		<p>< > WAIT < ></p>