

# ICS EXE 4

Fall, 2023

*\*Suppose all the following codes are running on a little-ending x86-64 machine.*

## 1. Jump Table

Read the C code and the assembly code below, answer the following questions.

```
1.
2. #define BUF_SIZE 8
3. void transfer(char *buf) {
4.     int i;
5.     for (i = 0; i < BUF_SIZE; i++) {
6.         switch (buf[i]) {
7.             case 0x1f:
8.                 if (i > 0) {
9.                     buf[i] += buf[i-1];
10.                }
11.            case 0x20: case 0x21:
12.                buf[i] |= 0xf0;
13.                break;
14.            case 0x22: case 0x25:
15.                *((unsigned *)buf + i/4) = (unsigned)buf[i];
16.                break;
17.            case 0x24:
18.                buf[i] = (char)((*(long *)buf) >> 8);
19.                break;
20.            default:
21.                if (i < BUF_SIZE - 1) {
22.                    *(short *)&buf[i] = 0x201f;
23.                }
24.                break;
25.        }
26.    }
27. }
```

1. transfer:	54. .L7:
2.     pushq    %rbp	55.     movl    -4(%rbp), %eax
3.     movq    %rsp, %rbp	56.     leal    3(%rax), %edx
4.     movq    %rdi, -24(%rbp)	57.     testl   %eax, %eax
5.     movl    \$0, -4(%rbp)	58.     cmovs   %edx, %eax
6.     jmp     .L2	59.     sarl    \$2, %eax
7. .L11:	60.     leaq    0(,%rax,4), %rdx
8.     movl    -4(%rbp), %eax	61.     movq    -24(%rbp), %rax
9.     movslq   %eax, %rdx	62.     addq    %rax, %rdx
10.    movq    -24(%rbp), %rax	63.     movl    -4(%rbp), %eax
11.    addq    %rdx, %rax	64.     movslq   %eax, %rcx
12.    movzbl   (%rax), %eax	65.     movq    -24(%rbp), %rax
13.    movsbl   %al, %eax	66.     addq    %rcx, %rax
14.    subl    __[1]__, %eax	67.     movzbl   (%rax), %eax
15.    cmpl    __[2]__, %eax	68.     movsbl   %al, %eax
16.    __[3]__ .L3	69.     movl    %eax, (%rdx)
17.     movl    %eax, %eax	70.     jmp     .L9
18.     movq    __[4]__, %rax	71. .L8:
19.     jmp     *%rax	72.     movl    -4(%rbp), %eax
20. .L4:	73.     movslq   %eax, %rdx
21.     cmpl    \$0, -4(%rbp)	74.     movq    -24(%rbp), %rax
22.     jle     .L6	75.     addq    %rax, %rdx
23.     movl    -4(%rbp), %eax	76.     movq    -24(%rbp), %rax
24.     movslq   %eax, %rdx	77.     movq    (%rax), %rax
25.     movq    -24(%rbp), %rax	78.     sarq    \$8, %rax

26.	addq	%rdx, %rax	79.	movb	%al, (%rdx)
27.	movl	-4(%rbp), %edx	80.	jmp	.L9
28.	movslq	%edx, %rcx	81.	.L3:	
29.	movq	-24(%rbp), %rdx	82.	cmpl	__[6]__, -4(%rbp)
30.	addq	%rcx, %rdx	83.	jg	.L9
31.	movzbl	(%rdx), %edx	84.	movl	-4(%rbp), %eax
32.	movl	%edx, %esi	85.	movslq	%eax, %rdx
33.	movl	-4(%rbp), %edx	86.	movq	-24(%rbp), %rax
34.	movslq	%edx, %rdx	87.	addq	%rdx, %rax
35.	leaq	-1(%rdx), %rcx	88.	_____	[7]_____
36.	movq	-24(%rbp), %rdx	89.	.L9:	
37.	addq	%rcx, %rdx	90.	addl	\$1, -4(%rbp)
38.	movzbl	(%rdx), %edx	91.	.L2:	
39.	addl	%esi, %edx	92.	cmpl	__[8]__, -4(%rbp)
40.	_____	[5]_____	93.	jle	.L11
41.	.L6:		94.	popq	%rbp
42.	movl	-4(%rbp), %eax	95.	ret	
43.	movslq	%eax, %rdx	96.	.section	.rodata
44.	movq	-24(%rbp), %rax	97.	.align	8
45.	addq	%rdx, %rax	98.	.align	4
46.	movl	-4(%rbp), %edx	99.	.L5:	
47.	movslq	%edx, %rcx	100.	/* hidden */	
48.	movq	-24(%rbp), %rdx			
49.	addq	%rcx, %rdx			
50.	movzbl	(%rdx), %edx			
51.	orl	\$-16, %edx			
52.	movb	%dl, (%rax)			
53.	jmp	.L9			

1. Jump table under label `.L5` is hidden. Please fill in the hidden part according to the C code given above.

- .quad .L4
- .quad .L6
- .quad .L6
- .quad .L7
- .quad .L3
- .quad .L8
- .quad .L7

2. Please fill in the blanks in the assembly code. (Hint: [5] and [7] use data movment instructions.)

```
[1] 31/0x1f          [2] 6
[3] ja               [4] .L5(,%rax,8)
[5] movb %dl, (%rax) [6] 6
[7] movw 0x201f, (%rax) [8] 7
```

3. For the input of function `transfer`, suppose `buf` is a char array with elements given below:

0	1	2	3	4	5	6	7
0x24	0xf0	0x22	0x1f	0x22	0x21	0x1f	0x21

After the execution of `transfer`, what will `buf` look like? Fill in the table below.

0	1	2	3	4	5	6	7
0xf0	0x1f	0xf0	0xff	0x22	0x1f	0xf0	0x00