

BIOS MCSDK 2.1.2

Release Notes

Applies to Product Release: 02.01.02
Publication Date: Nov 1, 2012



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Release Notes

BIOS MCSDK 2.01.02

1 Overview

This document is the Release Notes for **Release 2.1.2** of the Multicore Software Development Kit (MCSDK) for the BIOS operating system. This BIOS-MCSDK software release gives developers the ability to evaluate the hardware and software capabilities of the evaluation platform and provide core software to rapidly develop a multi-core application.

The main feature of this release is the introduction to OpenEM.

2 Licensing

The tables below describe the primary license for the packages that make up the BIOS MCSDK. As shown in the *Releases* section below, some of these packages are delivered with the Code Composer Studio installer and some are delivered with the BIOS MCSDK installer.

IMPORTANT: There may be portions of these packages under other licenses and you should refer to the individual package Software Manifest (after installation) for complete and specific licensing.

Target Content

This is software that runs on the device. For the location, replace <ver> with the version that is part of the component name (e.g., 2.0.0.11, etc.).

Package	Primary License	Manifest Location
MCSDK	BSD	[as_installed]/mcSDK_<ver>/bios_mcSDK_swmanifest_<ver>.pdf
PDK 6678	BSD	[as_installed]/pdk_c6678_<ver>/pdk-c6678_swmanifest.pdf
PDK 6670	BSD	[as_installed]/pdk_c6670_<ver>/pdk-c6670_swmanifest.pdf
PDK 6657	BSD	[as_installed]/pdk_c6657_<ver>/pdk-c6657_swmanifest.pdf

SYS/BIOS	BSD	[as_installed]/bios_<ver>/sysbios_<ver>_manifest.htm
IPC	BSD	[as_installed]/ipc_<ver>/ipc_<ver>_manifest.html
NDK	BSD	[as_installed]/ndk_<ver>/NDK_<ver>_Manifest.html
MATHLIB	BSD	[as_installed]/mathlib_c66x_<ver>/docs/MATHLIB_Software_Manifest.html
DSPLIB	BSD	[as_installed]/dsplib_c66x_<ver>/docs/DSPLIB_Software_Manifest.html
IMAGELIB	BSD	[as_installed]/imglib_c66x_<ver>/docs/IMGLIB_Software_Manifest.html
EDMA3	BSD	[as_installed]/edma3_lld_<ver>/software-manifest.pdf
MCSA Framework	BSD	[as_installed]/ccsv5/uia_<ver>/uia_<ver>_manifest.html
OMP	BSD/ GPL-3.0-wit h-GCC-exce ption	[as_installed]/omp_<ver>/omp_<ver>_manifest.html

Host Tools

These are development tools that run primarily on the development host. For the location, replace <ver> with the version that is part of the component name (e.g., 2.0.0.11, etc.).

Package	Primary License	Manifest
MCSDK (flash and eeprom tools)	BSD	[as_installed]/mcSDK_<ver>/bios_mcSDK_swmanifest_<ver>.pdf
CG XML	BSD	[as_installed]/cg_xml/cg_xml_<ver>_Manifest.pdf
XDC	EPL 1.0	[as_installed]/xdctools_<ver>/docs/xdctools.chm (see License section)
Code Composer Studio 5	Click Wrap	N/A

3 Documentation

- **EVM Quick Start Guide:** Provides information on hardware setup and running the demonstration application that is loaded on flash. This document is provided as part of the EVM kit.
- **BIOS MCSDK Getting Started Guide:** Provides information on installing the software from the two software packages (MCSDK, CCS), connect to the target EVM using JTAG, load pre-compiled libraries to the EVM, and run the application.
- **BIOS MCSDK Users Guide:** Provides detailed information regarding software elements and infrastructure to allow developers to start creating applications.

- [Image Processing Demonstration Guide](#): Provides information on the image processing demo including software design, performance results, and development concerns.

NOTE: The latest version of above documents can be found on the online links as specified and a snapshot is provided in PDF format in the release at [as_installed]/mcsdk_<ver>/docs.

4 Device Support

The device and platform supported with this release include:

- **C6678**: This release supports the Texas Instruments [TMS320C6678](#) high performance DSP. The EVM supported by the software is [TMDSEVM6678L](#).
- **C6670**: This release supports the Texas Instruments [TMS320C6670](#) high performance DSP. The EVM supported by the software is [TMDSEVM6670L](#).
- **C6657**: This release supports the Texas Instruments [TMS320C6657](#) high performance DSP. The EVM supported by the software is TMDSEVM6657L. **NOTE: OpenMP is not supported on this device.**
- **TCI6618**: This release supports the Texas Instruments [TMS320TCI6618](#) high performance DSP. The EVM supported by the software is TMDSEVM6618LXE. **NOTE: OpenMP is not supported on this device.**

5 Technical Support and Product Updates

For technical discussions and issues, please visit

- **C66x Multicore forum**: http://e2e.ti.com/support/dsp/c6000_multi-core_dsp/f/639.aspx
- **BIOS Embedded Software forum**: <http://e2e.ti.com/support/embedded/f/355.aspx>
- **Embedded Processors wiki**: http://processors.wiki.ti.com/index.php/Main_Page

For product updates, please visit

- **Multicore Software Development Kits**:
<http://focus.ti.com/docs/toolsw/folders/print/bioslinuxmcsdk.html>

6 IS NOT

- Support for BIOS5 or older releases
- Support for CCS 4.x or older releases
- DSP image format other than ELF (e.g., COFF)
- Security Accelerator LLD is not included in this package, but has been tested against SA LLD 1.0.4.1 Please find software download here:
http://software-dl.ti.com/sdoemb/sdoemb_public_sw/salld/

7 Installation

See the online *BIOS MCSDK Getting Started Guide* ([link](#)) for installation information.

8 Issue List

The list of issues can be found online using this [link](#).

9 Releases

9.1 Build 2.1.2.5

Component	Version	Installer
NDK	2.21.01.38	MCSDK
IPC	1.24.03.32	MCSDK
EDMA3 LLD	2.11.05.02	MCSDK
SYS/BIOS	6.33.06.50	MCSDK
DSPLIB	3.1.0.0	MCSDK
MATHLIB	3.0.1.1	MCSDK
IMGLIB	3.1.1.0	MCSDK
OMP	1.1.3.2	MCSDK
OpenEM	1.0.0.2	MCSDK
MCSDK Tools		
BIN2CCS	1.0.0.0	MCSDK
IBL	1.0.0.16	MCSDK
MAD-UTILS	1.0.1.1	MCSDK
EEPROM Writer	1.0.0.5	MCSDK
NAND Writer	1.0.0.1	MCSDK
NOR Writer	1.0.0.3	MCSDK
MCSA	1.0	MCSDK
POST	1.0.0.6	MCSDK
PDK-C6678	1.1.2.5	MCSDK
Platform Library	2.0.0.14	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.1	MCSDK
CSL	1.0.1.4	MCSDK
PA LLD	1.2.3.3	MCSDK
CPPI LLD	1.0.2.2	MCSDK
QMSS LLD	1.0.3.12	MCSDK
PCIe LLD	1.0.0.3	MCSDK
SRIO LLD	1.0.1.10	MCSDK
RM LLD	1.0.0.10	MCSDK
TSIP LLD	1.0.0.5	MCSDK
HYPLINK LLD	1.0.1.5	MCSDK
PDK-C6670	1.1.2.5	MCSDK
Platform Library	2.0.0.15	MCSDK

NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.1	MCSDK
CSL	1.0.1.5	MCSDK
PA LLD	1.2.3.3	MCSDK
CPPI LLD	1.0.2.2	MCSDK
QMSS LLD	1.0.3.12	MCSDK
PCIe LLD	1.0.0.3	MCSDK
SRIO LLD	1.0.1.10	MCSDK
RM LLD	1.0.0.10	MCSDK
AIF2 LLD	1.0.0.6	MCSDK
BCP LLD	1.0.1.0	MCSDK
FFTC LLD	1.0.0.16	MCSDK
HYPLINK LLD	1.0.1.5	MCSDK
TCP3D LLD	1.0.0.17	MCSDK
TCP3E LLD	1.0.0.11	MCSDK
PDK-C6657	1.1.2.5	MCSDK
Platform Library	2.0.0.2	MCSDK
NIMU Transport	1.0.0.1	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.1	MCSDK
CSL	1.0.0.5	MCSDK
CPPI LLD	1.0.2.2	MCSDK
EMAC LLD	1.0.3.0	MCSDK
HYPLINK LLD	1.0.1.5	MCSDK
PCIe LLD	1.0.0.3	MCSDK
QMSS LLD	1.0.3.12	MCSDK
RM LLD	1.0.0.10	MCSDK
SRIO LLD	1.0.1.10	MCSDK
TCP3D LLD	1.0.0.17	MCSDK
MCBSP LLD	1.0.0.0	MCSDK
Demos		
HUA Demo	2.0.0.4	MCSDK
Image Processing Demo	1.0.0.4	MCSDK
Other Tools		
CGT	7.4.0	download
CCS	5.2.1.00018	download
UIA	1.1.0.04	CCS
DVT	3.2.0.201206032146	CCS
XDCTools	3.23.04.60	MCSDK
CG_XML	2.30	MCSDK

Python	2.7.2	download
minGW	mingw-get-inst-201 10316	download

New Features:

- Introduction to OpenEM component

Issues Fixed:

ID	Severity	Headline
SDOCM00091470	S2 - Major	C6678 & C6670 PLL sequence needs to be updated in PlatformLib (used by POST and HUA)
SDOCM00096323	S2 - Major	QPEND Configuration of QMSS IPC transport does not work on cores 4 through 7
SDOCM00096519	S2 - Major	Installer needs to remove references to CCS 5.0.x since that release is not supported
SDOCM00096522	S2 - Major	Installer needs to remove MCSA 1.0 since that component is bundled with CCS
SDOCM00096562	S3 - Minor	program_evm document update
SDOCM00096631	S3 - Minor	Location of MCSDK driver help files is changed but driver documentation is still talking about old paths
SDOCM00096703	S3 - Minor	HUA Demos: C6657_small.gif and C6657_small.h files are missing

Known Issues:

ID	Severity	Headline	Workaround (If applicable)
SDOCM00082173	S2 - Major	PDK TSIP_testProject -readme needs to indicate dependency on clock for running the sample code.	
SDOCM00092167	S2 - Major	PDK 6657: srioChipToChipConsumer and srioChipToChipProducer have linker undefined symbol failures	SRIO IPC Transport examples are not yet available for C6657. Please do not use these examples; these examples will be validated and available in upcoming BIOS MCSDK releases.
SDOCM00092350	S2 - Major	PDK 6657: srioChiptoChip Producer and Consumer get an out of memory error on core 0 when trying load .out file on C6657 EVM	SRIO IPC Transport examples are not yet available for C6657. Please do not use these examples; these examples will be validated and available in upcoming BIOS MCSDK releases.
SDOCM00092825	S2 - Major	Need to configure VMIN_EXP in SrioDevice_init()	
SDOCM00095998	S2 - Major	C6657: EMAC LLD does not work in BE mode	
SDOCM00096412	S2 - Major	SRIO LLD has no way to clean up for restart	

SDOCM00096519	S2 - Major	Installer needs to remove references to CCS 5.0.x since that release is not supported	
SDOCM00096522	S2 - Major	Installer needs to remove MCSA 1.0 since that component is bundled with CCS	
SDOCM00079559	S3 - Minor	NIMU driver do not support JUMBO packets	None
SDOCM00081766	S3 - Minor	IMGLIB: IMGLIB package expanded in the graphical editor the information displayed is not meaningful	
SDOCM00081767	S3 - Minor	DSPLIB: DSPLIB package expanded in the graphical editor the information displayed is not meaningful	
SDOCM00092953	S3 - Minor	Extend Current ipcBenchmark Examples to all cores on a given device	N/A
SDOCM00096155	S3 - Minor	DSP local reset via PCIE sometimes doesn't work	
SDOCM00096424	S3 - Minor	Extra 'free' operation in NIMU code	

9.2 Previous Releases

9.2.1 Build 2.1.1.4

Component	Version	Installer
NDK	2.21.01.38	MCSDK
IPC	1.24.03.32	MCSDK
EDMA3 LLD	2.11.05.02	MCSDK
SYS/BIOS	6.33.06.50	MCSDK
DSPLIB	3.1.0.0	MCSDK
MATHLIB	3.0.1.1	MCSDK
IMGLIB	3.1.1.0	MCSDK
OMP	1.1.2.6	MCSDK
MCSDK Tools		
BIN2CCS	1.0.0.0	MCSDK
IBL	1.0.0.15	MCSDK
MAD-UTILS	1.0.1.1	MCSDK
EEPROM Writer	1.0.0.5	MCSDK
NAND Writer	1.0.0.1	MCSDK
NOR Writer	1.0.0.3	MCSDK
MCSA	1.0	MCSDK
POST	1.0.0.6	MCSDK
PDK-C6678	1.1.1.4	MCSDK

Platform Library	2.0.0.13	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.1.4	MCSDK
PA LLD	1.2.3.3	MCSDK
CPPI LLD	1.0.2.2	MCSDK
QMSS LLD	1.0.3.12	MCSDK
PCIe LLD	1.0.0.3	MCSDK
SRIO LLD	1.0.1.10	MCSDK
RM LLD	1.0.0.10	MCSDK
TSIP LLD	1.0.0.5	MCSDK
HYPLINK LLD	1.0.1.5	MCSDK
PDK-C6670	1.1.1.4	MCSDK
Platform Library	2.0.0.14	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.1.5	MCSDK
PA LLD	1.2.3.3	MCSDK
CPPI LLD	1.0.2.2	MCSDK
QMSS LLD	1.0.3.12	MCSDK
PCIe LLD	1.0.0.3	MCSDK
SRIO LLD	1.0.1.10	MCSDK
RM LLD	1.0.0.10	MCSDK
AIF2 LLD	1.0.0.6	MCSDK
BCP LLD	1.0.1.0	MCSDK
FFTC LLD	1.0.0.16	MCSDK
HYPLINK LLD	1.0.1.5	MCSDK
TCP3D LLD	1.0.0.17	MCSDK
TCP3E LLD	1.0.0.11	MCSDK
PDK-C6657	1.1.1.4	MCSDK
Platform Library	2.0.0.1	MCSDK
NIMU Transport	1.0.0.1	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.0.5	MCSDK
CPPI LLD	1.0.2.2	MCSDK
EMAC LLD	1.0.3.0	MCSDK
HYPLINK LLD	1.0.1.5	MCSDK
PCIe LLD	1.0.0.3	MCSDK

QMSS LLD	1.0.3.12	MCSDK
RM LLD	1.0.0.10	MCSDK
SRI0 LLD	1.0.1.10	MCSDK
TCP3D LLD	1.0.0.17	MCSDK
MCBSP LLD	1.0.0.0	MCSDK
Demos		
HUA Demo	2.0.0.4	MCSDK
Image Processing Demo	1.0.0.4	MCSDK
Other Tools		
CGT	7.4.0	download
CCS	5.2.1.00018	download
UIA	1.1.0.04	CCS
DVT	3.2.0.201206032146	CCS
XDCTools	3.23.04.60	MCSDK
CG_XML	2.30	MCSDK
Python	2.7.2	download
mingw-get-inst-201 10316		download

New Features:

- Production quality support for TMXEV6657
- McBSP LLD for TMXEV6657

Issues Fixed:

id	Severity	Headline
SDOCM00082117	S3 - Minor	IPC example projects can not be re-built even after the projects get cleaned
SDOCM00087159	S2 - Major	IBL: IBL Build process does not properly populate IBL Boot Device Table for parameter indexes other than '0'
SDOCM00087288	S3 - Minor	PDK6678 -qmInsRegionTestProject won't build
SDOCM00088229	S3 - Minor	Some LLD release notes are missing from the pdk\docs directory
SDOCM00089747	S2 - Major	Need Pre-Build Big Endian IBL binary in MCSDK2.0 release.
SDOCM00092170	S2 - Major	Platform Init is initializing the DDR PLL even if i disable DDR_init flag.
SDOCM00092586	S3 - Minor	CC657 - qmssIpcBenchmark_c6657.out fails to run
SDOCM00092732	S2 - Major	PDK 6657: srioChiptoChipConsumer gets an exception when trying to run the test
SDOCM00092854	S3 - Minor	SRIO boot example: mcsdk_2_00_09_21\tools\boot_loader\examples\srio\docs\README.pdf does not have information for C6657
SDOCM00092946	S2 - Major	Cannot communicate between Core 0 and Core 1 using IPC QMSS Transport

SDOCM00092952	S2 - Major	IPC QMSS Transport QPEND functionality does not work for Core2+
SDOCM00092969	S2 - Major	C6657 RBL Emac Boot does not work
SDOCM00092992	S2 - Major	C6657 TFTP Boot does not work
SDOCM00093080	S2 - Major	C6657: Include McBSP LLD
SDOCM00093161	S3 - Minor	program_evm: Add support for XDS 200
SDOCM00093162	S2 - Major	Image Processing Demo needs to be supported for C6657
SDOCM00093163	S2 - Major	The POST emac loopback test checks if the no. of recv packets is less than 0 instead of <= 0.
SDOCM00093679	S2 - Major	C6657: NAND driver has issues in Platformlib
SDOCM00093831	S3 - Minor	Inter core interrupts using IPCGR registers via CSL from pdk_C6670_1_0_0_20
SDOCM00093865	S2 - Major	C6657 NAND boot does not work
SDOCM00094063	S2 - Major	C6657 EMAC osal needs to fix critical section locks
SDOCM00094242	S2 - Major	Platform library: (C6670, C6678) does not set the Ensat bit and BWADJ calculations is wrong for ODD PLLM values
SDOCM00094925	S2 - Major	PlatformLib: Provide public APIs to read and write FPGA Configuration Registers
SDOCM00095028	S2 - Major	CSL: Issue in csl file: pdk_C6678_1_0_0_21/packages/ti/csl/csl_cpsgmiiAux.h
SDOCM00095589	S2 - Major	C6657: IPC tranport examples may need memory map update to fit into 1Mb MSMC size
SDOCM00095599	S2 - Major	pdkProjectCreate.bat has the wrong versions for most of the packages
SDOCM00095787	S2 - Major	C6657: SRIO projects do not link properly
SDOCM00095797	S3 - Minor	6657 NIMU version is not tracked

Known Issues:

ID	Severity	Headline	Workaround (If applicable)	Targeted Release
SDOCM00082173	S2 - Major	PDK TSIP_testProject -readme needs to indicate dependency on clock for running the sample code.		None

Revision A:

SDOCM000 92167	S2 - Major	PDK 6657: srioChipToChipConsumer and srioChipToChipProducer have linker undefined symbol failures	SRIO IPC Transport examples are not yet available for C6657. Please do not use these examples; these examples will be validated and available in upcoming BIOS MCSDK releases.	None
SDOCM000 92350	S2 - Major	PDK 6657: srioChiptoChip Producer and Consumer get an out of memory error on core 0 when trying load .out file on C6657 EVM	SRIO IPC Transport examples are not yet available for C6657. Please do not use these examples; these examples will be validated and available in upcoming BIOS MCSDK releases.	None
SDOCM000 92825	S2 - Major	Need to configure VMIN_EXP in SrioDevice_init()		None
SDOCM000 79559	S3 - Minor	NIMU driver do not support JUMBO packets	None	None
SDOCM000 81766	S3 - Minor	IMGLIB: IMGLIB package expanded in the graphical editor the information displayed is not meaningful		None
SDOCM000 81767	S3 - Minor	DSPLIB: DSPLIB package expanded in the graphical editor the information displayed is not meaningful		None
SDOCM000 92953	S3 - Minor	Extend Current ipcBenchmark Examples to all cores on a given device	N/A	None
SDOCM000 95998	S2 - Major	6657: EMAC LLD does not work with BE		None

9.2.2 Build 2.1.0.3

Component	Version	Installer
NDK	2.21.01.38	MCSDK
IPC	1.24.02.27	MCSDK
EDMA3 LLD	2.11.05.02	MCSDK

SYS/BIOS	6.33.04.39	MCSDK
DSPLIB	3.1.0.0	MCSDK
MATHLIB	3.0.1.1	MCSDK
IMGLIB	3.1.1.0	MCSDK
OMP	1.1.2.6	MCSDK
MCSDK Tools		
BIN2CCS	1.0.0.0	MCSDK
IBL	1.0.0.15	MCSDK
MAD-UTILS	1.0.1.1	MCSDK
EEPROM Writer	1.0.0.5	MCSDK
NAND Writer	1.0.0.4	MCSDK
NOR Writer	1.0.0.3	MCSDK
MCSA	1.0	MCSDK
POST	1.0.0.5	MCSDK
PDK-C6678	1.1.0.3	MCSDK
Platform Library	2.0.0.13	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.1.3	MCSDK
PA LLD	1.2.2.1	MCSDK
CPPI LLD	1.0.2.2	MCSDK
QMSS LLD	1.0.3.12	MCSDK
PCIe LLD	1.0.0.3	MCSDK
SRIO LLD	1.0.1.9	MCSDK
RM LLD	1.0.0.7	MCSDK
TSIP LLD	1.0.0.5	MCSDK
HYPLINK LLD	1.0.1.5	MCSDK
PDK-C6670	1.1.0.3	MCSDK
Platform Library	2.0.0.14	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.1.4	MCSDK
PA LLD	1.2.2.1	MCSDK
CPPI LLD	1.0.2.2	MCSDK
QMSS LLD	1.0.3.12	MCSDK
PCIe LLD	1.0.0.3	MCSDK
SRIO LLD	1.0.1.9	MCSDK
RM LLD	1.0.0.7	MCSDK
AIF2 LLD	1.0.0.6	MCSDK

BCP LLD	1.0.1.0	MCSDK
FFTC LLD	1.0.0.16	MCSDK
HYLINK LLD	1.0.1.5	MCSDK
TCP3D LLD	1.0.0.17	MCSDK
TCP3E LLD	1.0.0.11	MCSDK
PDK-C6657	1.1.0.3	MCSDK
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IPC QMSS Transport	1.0.1.0	MCSDK
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CPPI LLD	1.0.2.2	MCSDK
EMAC LLD	1.0.3.0	MCSDK
HYLINK LLD	1.0.1.5	MCSDK
PCIe LLD	1.0.0.3	MCSDK
QMSS LLD	1.0.3.12	MCSDK
RM LLD	1.0.0.7	MCSDK
SRIO LLD	1.0.1.9	MCSDK
TCP3D LLD	1.0.0.17	MCSDK
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CCS	5.2.1.00018	download
XDCTools	3.23.02.47	MCSDK
CG_XML	2.30	MCSDK
Python	2.7.2	download
minGW	mingw-get-inst-20 110316	download

New Features:

- Updated baseline to BIOS-MCSDK 2.0.9
- Integrated CGT 7.4 production release
- Updated OMP production release

Issues Fixed:

ID	Headline
SDOCM00081042	DOC: Example code and documentation to demonstrate UART/GPIO/Timer and DDR3
SDOCM00085566	BIOS MCSDK installer crashes while installing MCSA in CCS 5.1
SDOCM00085799	There is an assertion failure when shutting down a telnet connection to EVM

SDOCM00085875	Linux Host: Compilation fails for HyperLink, PA & PCIE projects
SDOCM00086668	KeyStone CSL update needed for chip-level interrupt controller and EDMA naming conventions
SDOCM00089551	IBL: pscDelay may cause pscWait trapped
SDOCM00089616	PDK (C6670 & C6678): XDC, BIOS and IPC version are incorrect in the pdkProjectCreate.bat file
SDOCM00090706	IBL: DDR3 configuration for NAND boot needs to be updated
SDOCM00092084	IMGLIB 3.1.0.1: Import and build issues in CCSv5
SDOCM00092302	C6657 (Gauss) build "warnings" for HUA project
SDOCM00093003	NDK: UDP and TCP throughput degraded
SDOCM00093371	CSL: Syntax Error in cslr_spi.h
SDOCM00093402	CSL: cslr_tcp3d_dma.h CSL definition error
SDOCM00094877	AIF2 LLD is missing from BIOS-MCSDK 2.1.0 Beta release
SDOCM00095010	BIOS MCSDK Users Guide has the wrong memory address for eepromwriter data

Known Issues:

ID	Severity	Headline	Workaround (If applicable)	Targeted Release
SDOCM00082173	S2 - Major	PDK TSIP_testProject -readme needs to indicate dependency on clock for running the sample code.		None
SDOCM00092167	S2 - Major	PDK 6657: srioChipToChipConsumer and srioChipToChipProducer have linker undefined symbol failures	SRIO IPC Transport examples are not yet available for C6657. Please do not use these examples; these examples will be validated and available in upcoming BIOS MCSDK releases.	None
SDOCM00092350	S2 - Major	PDK 6657: srioChiptoChip Producer and Consumer get an out of memory error on core 0 when trying load .out file on C6657 EVM	SRIO IPC Transport examples are not yet available for C6657. Please do not use these examples; these examples will be validated and available in upcoming BIOS MCSDK releases.	None
SDOCM00092825	S2 - Major	Need to configure VMIN_EXP in SrioDevice_init()		None

SDOCM000 92946	S2 - Major	Cannot communicate between Core 0 and Core 1 using IPC QMSS Transport	<p>1) To workaround the bug affecting QMSS transport's storing of connection parameters make the following changes to C:\<c6670_pdk_install_dir>\packages\ti\transport\ipc\qmss\transports\TransportQmss.c.</p> <p>The change involves moving code lines 482 - 487 to line 280 within the TransportQmss_Instance_init function.</p> <p>2) Rebuild the TransportQmss transport</p> <p>Open a Windows command window and navigate to:</p> <pre>>cd c:\<pdk_install_path>\packages\ti\transport\ipc\qmss\transports</pre> <p>Set up the build environment with the following commands. Make sure to fill xx_yy_zz in with the BIOS, IPC, and XDC versions used in your environment</p> <pre>set XDCPATH::c:\ti\bios_6_xx_yy_zz\packages\ set XDCPATH::%XDCPATH%;c:\ti\ipc_1_24_yy_zz\packages\ # Path to directory which contains the compiler \bin directory set XDCCGROOT::c:\ti\ccsv5\tools\compiler\c6000 set PATH::%PATH%;c:\Program Files\Texas Instruments\xdctools_3_23_yy_zz\ # Use the following command to rebuild the QMSS transport > xdc -PR .</pre> <p>3) After rebuilding the QMSS transport with the workaround the QPEND configuration will still not work due to an interrupt configuration bug. So the transport needs to be configured to use the Accumulator queues instead of the QPEND queues. This is done in the .cfg file.</p> <p>Change</p> <pre>TransportQmss.numDescriptors :: Program.global.numDescriptors; TransportQmss.descriptorIsInSharedMem :: true; TransportQmss.descriptorSize :: Program.global.descriptorSize; TransportQmss.useAccumulatorLogic :: false; TransportQmss.pacingEnabled :: false; TransportQmss.intThreshold :: 1; TransportQmss.timerLoadCount :: 0; // timer ticks. This value only has effect when the packingEnabled is true. TransportQmss.accuHiPriListSize :: 1100; // this number should be >:: twice the threshold+2</pre>	None
---------------------------	------------	---	--	------

```

to

TransportQmss.numDescriptors :: 
Program.global.numDescriptors;
TransportQmss.descriptorIsInSharedMem :: true;
TransportQmss.descriptorSize :: 
Program.global.descriptorSize;
TransportQmss.useAccumulatorLogic :: true;
TransportQmss.pacingEnabled :: false;
TransportQmss.intThreshold :: 1;
TransportQmss.timerLoadCount :: 0; // timer ticks.
This value only has effect when the packingEnabled
is true.
TransportQmss.accuHiPriListSize :: 1100; // this
number should be >:: twice the threshold+2

```

4) Extend the example project to all cores on the EVM
(c6670 used as an example). This is done in the .cfg
file as well.

Change

```

case "ti.platforms.evm6670":
    Program.global.USING_C6670 :: 1;
    procNameList :: ["CORE0", "CORE1"];
    Program.global.shmBase :: 0x0C000000;
    Program.global.shmSize :: 0x00050000; /*
Sized for greater than 2000 128 byte messageQ
messages */
    break;

to
case "ti.platforms.evm6670":
    Program.global.USING_C6670 :: 1;
    procNameList :: ["CORE0", "CORE1", "CORE2",
"CORE3"];
    Program.global.shmBase :: 0x0C000000;
    Program.global.shmSize :: 0x00050000; /*
Sized for greater than 2000 128 byte messageQ
messages */
    break;

```

5) Rebuild the CCS project.

SDOCM000 92952	S2 - Major	IPC QMSS Transport QPEND functionality does not work for Core2+	<p>1) To workaround the bug affecting QMSS transport's storing of connection parameters make the following changes to C:\<c6670_pdk_install_dir>\packages\ti\transport\ipc\qmss\transports\TransportQmss.c.</p> <p>The change involves moving code lines 482 - 487 to line 280 within the TransportQmss_Instance_init function.</p> <p>2) Rebuild the TransportQmss transport</p> <p>Open a Windows command window and navigate to:</p> <pre>>cd c:\<pdk_install_path>\packages\ti\transport\ipc\qmss \transports</pre> <p>Set up the build environment with the following commands. Make sure to fill xx_yy_zz in with the BIOS, IPC, and XDC versions used in your environment</p> <pre>set XDCPATH::c:\ti\bios_6_xx_yy_zz\packages\ set XDCPATH::%XDCPATH%;c:\ti\ipc_1_24_yy_zz\pac kages\ # Path to directory which contains the compiler \bin directory set XDCCGROOT::c:\ti\ccsv5\tools\compiler\c6000 set PATH::%PATH%;c:\Program Files\Texas Instruments\xdctools_3_23_yy_zz\ # Use the following command to rebuild the QMSS transport > xdc -PR .</pre> <p>3) After rebuilding the QMSS transport with the workaround the QPEND configuration will still not work due to an interrupt configuration bug. So the transport needs to be configured to use the Accumulator queues instead of the QPEND queues. This is done in the .cfg file.</p> <p>Change</p> <pre>TransportQmss.numDescriptors :: Program.global.numDescriptors; TransportQmss.descriptorIsInSharedMem :: true; TransportQmss.descriptorSize :: Program.global.descriptorSize; TransportQmss.useAccumulatorLogic :: false; TransportQmss.pacingEnabled :: false; TransportQmss.intThreshold :: 1; TransportQmss.timerLoadCount :: 0; // timer ticks. This value only has effect when the packingEnabled is true. TransportQmss.accuHiPriListSize :: 1100; // this number should be >:: twice the threshold+2</pre>	None
---------------------------	------------	---	--	------

to

```
TransportQmss.numDescriptors ::  
Program.global.numDescriptors;  
TransportQmss.descriptorIsInSharedMem :: true;  
TransportQmss.descriptorSize ::  
Program.global.descriptorSize;  
TransportQmss.useAccumulatorLogic :: true;  
TransportQmss.pacingEnabled :: false;  
TransportQmss.intThreshold :: 1;  
TransportQmss.timerLoadCount :: 0; // timer ticks.  
This value only has effect when the packingEnabled  
is true.  
TransportQmss.accuHiPriListSize :: 1100; // this  
number should be >:: twice the threshold+2
```

4) Extend the example project to all cores on the EVM
(c6670 used as an example). This is done in the .cfg
file as well.

Change

```
case "ti.platforms.evm6670":  
    Program.global.USING_C6670 :: 1;  
    procNameList :: ["CORE0", "CORE1"];  
    Program.global.shmBase :: 0x0C000000;  
    Program.global.shmSize :: 0x00050000; /*  
Sized for greater than 2000 128 byte messageQ  
messages */  
    break;  
  
to  
case "ti.platforms.evm6670":  
    Program.global.USING_C6670 :: 1;  
    procNameList :: ["CORE0", "CORE1", "CORE2",  
"CORE3"];  
    Program.global.shmBase :: 0x0C000000;  
    Program.global.shmSize :: 0x00050000; /*  
Sized for greater than 2000 128 byte messageQ  
messages */  
    break;
```

5) Rebuild the CCS project.

SDOCM000 92969	S2 - Major	C6657 RBL Emac Boot does not work	This boot option is not yet available for C6657 and will be implemented in future BIOS-MCSDK releases.	BIOS-MCSDK 2.1.1
SDOCM000 79559	S3 - Minor	NIMU driver do not support JUMBO packets	None	None
SDOCM000 81766	S3 - Minor	IMGLIB: IMGLIB package expanded in the graphical editor the information displayed is not meaningful		None

SDOCM000 81767	S3 - Minor	DSPLIB: DSPLIB package expanded in the graphical editor the information displayed is not meaningful	None
SDOCM000 82117	S3 - Minor	IPC example projects can not be re-built even after the projects get cleaned	In order to rebuild the project after import simply delete the Debug and/or Release directories. Afterwards the projects will clean and build without any issues.
SDOCM000 92953	S3 - Minor	Extend Current ipcBenchmark Examples to all cores on a given device	N/A

9.2.3 Build 2.1.0.2

This is the Beta release for R2.1.0.

Component	Version	Installer
NDK	2.21.00.32	MCSDK
IPC	1.24.02.27	MCSDK
EDMA3 LLD	2.11.05.02	MCSDK
SYS/BIOS	6.33.04.39	MCSDK
DSPLIB	3.1.0.0	MCSDK
MATHLIB	3.0.1.1	MCSDK
IMGLIB	3.1.0.1	MCSDK
OMP	1.1.2.3	MCSDK
MCSDK Tools		
BIN2CCS	1.0.0.0	MCSDK
IBL	1.0.0.15	MCSDK
MAD-UTILS	1.0.1.0	MCSDK
EEPROM Writer	1.0.0.5	MCSDK
NAND Writer	1.0.0.4	MCSDK
NOR Writer	1.0.0.3	MCSDK
MCSA	1.0	MCSDK
POST	1.0.0.5	MCSDK
PDK-C6678	1.0.0.21	MCSDK
Platform Library	2.0.0.12	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK

IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.1.2	MCSDK
PA LLD	1.2.2.1	MCSDK
CPPI LLD	1.0.2.2	MCSDK
QMSS LLD	1.0.3.12	MCSDK
PCIe LLD	1.0.0.3	MCSDK
SRIO LLD	1.0.1.9	MCSDK
RM LLD	1.0.0.7	MCSDK
TSIP LLD	1.0.0.5	MCSDK
HYPLINK LLD	1.0.1.5	MCSDK
PDK-C6670	1.0.0.21	MCSDK
Platform Library	2.0.0.13	MCSDK
NIMU Transport	1.0.0.8	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.1.3	MCSDK
PA LLD	1.2.2.1	MCSDK
CPPI LLD	1.0.2.2	MCSDK
QMSS LLD	1.0.3.12	MCSDK
PCIe LLD	1.0.0.3	MCSDK
SRIO LLD	1.0.1.9	MCSDK
RM LLD	1.0.0.7	MCSDK
AIF2 LLD	1.0.0.2	MCSDK
BCP LLD	1.0.0.9	MCSDK
FFTC LLD	1.0.0.16	MCSDK
TCP3D LLD	1.0.0.17	MCSDK
TCP3E LLD	1.0.0.10	MCSDK
PDK-C6657	1.0.0.0	MCSDK
Platform Library	2.0.0.1	MCSDK
NIMU Transport	1.0.0.0	MCSDK
IPC SRIO Transport	1.0.1.0	MCSDK
IPC QMSS Transport	1.0.1.0	MCSDK
CSL	1.0.0.3	MCSDK
CPPI LLD	1.0.2.2	MCSDK
EMAC LLD	1.0.3.0	MCSDK
HYPLINK LLD	1.0.1.5	MCSDK
PCIe LLD	1.0.0.3	MCSDK
QMSS LLD	1.0.3.12	MCSDK
RM LLD	1.0.0.7	MCSDK
SRIO LLD	1.0.1.9	MCSDK
TCP3D LLD	1.0.0.17	MCSDK

Demos		
HUA Demo	2.0.0.4	MCSDK
Image Processing Demo	1.0.0.4	MCSDK
Other Tools		
CodeGen	7.4B2	download
CCS (Windows)	5.1.1.00031	download
CCS (Linux)	5.1.1.00033	download
C6670/C6678 Simulator	1.0.1	CCS
C6618 Simulator	0.4.0	CCS
XDCTools	3.23.02.47	MCSDK
CG_XML	2.30	MCSDK
Python	2.7.2	download
mingW	mingw-get-inst-20110316	download

9.2.4 Build 2.1.0.1

This is the second alpha release.

Component	Version	Installer
NDK	2.20.06.35	MCSDK
IPC	1.24.00.16	MCSDK
EDMA3 LLD	2.11.03.02	MCSDK
SYS/BIOS	6.32.05.54	MCSDK
DSPLIB	3.1.0.0	MCSDK
MATHLIB	3.0.0.0	MCSDK
IMGLIB	3.0.2.1	MCSDK
OMP	1.00.01.05	MCSDK
MCSDK Tools		
BIN2CCS	1.0.0.0	MCSDK
IBL	1.0.0.10	MCSDK
MAD-UTILS	1.0.0.3	MCSDK
EEPROM Writer	1.0.0.4	MCSDK
NAND Writer	1.0.0.4	MCSDK
NOR Writer	1.0.0.3	MCSDK
MCSA	1.0	MCSDK
POST	1.0.0.4	MCSDK
PDK-C6678	1.1.0.1	MCSDK
Platform Library	2.0.0.10	MCSDK
NIMU Transport	1.0.0.7	MCSDK
IPC SRIO Transport	1.0.0.0	MCSDK
IPC QMSS Transport	1.0.0.0	MCSDK

CSL	1.0.1.1	MCSDK
PA LLD	1.1.1.0	MCSDK
CPPI LLD	1.0.0.16	MCSDK
QMSS LLD	1.0.0.17	MCSDK
PCIe LLD	1.0.0.2	MCSDK
SRIO LLD	1.0.1.1	MCSDK
TSIP LLD	1.0.0.4	MCSDK
HYPLINK LLD	1.0.1.1	MCSDK
PDK-C6670	1.1.0.1	MCSDK
Platform Library	2.0.0.9	MCSDK
NIMU Transport	1.0.0.7	MCSDK
IPC SRIO Transport	1.0.0.0	MCSDK
IPC QMSS Transport	1.0.0.0	MCSDK
CSL	1.0.1.1	MCSDK
PA LLD	1.1.1.0	MCSDK
CPPI LLD	1.0.0.16	MCSDK
QMSS LLD	1.0.0.17	MCSDK
PCIe LLD	1.0.0.2	MCSDK
SRIO LLD	1.0.1.1	MCSDK
BCP LLD	1.0.0.7	MCSDK
FFTC LLD	1.0.0.14	MCSDK
TCP3D LLD	1.0.0.13	MCSDK
TCP3E LLD	1.0.0.10	MCSDK
Demos		
HUA Demo	2.0.0.4	MCSDK
Image Processing Demo	2.1.0.0	MCSDK
Other Tools		
CodeGen	7.4.0A12012	download
CCS	5.1.0.09000	download
C6670/C6678 Simulator	1.0.1	CCS
C6618 Simulator	0.4.0	CCS
XDCTools	3.22.04.46	MCSDK
CG_XML	2.30	MCSDK
Python	2.7.2	download
minGW	mingw-get-inst-20 110316	download

9.2.5 Build 2.1.0.0

This is the Alpha release.

Component	Version	Installer
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NDK	2.20.04.26	MCSDK
IPC	1.23.01.26	MCSDK
EDMA3 LLD	2.11.02.04	MCSDK
SYS/BIOS	6.32.04.49	MCSDK
DSPLIB	3.0.0.8	MCSDK
MATHLIB	3.0.0.0	MCSDK
IMGLIB	3.0.1.0	MCSDK
OMP	1.00.00.36	MCSDK
MCSDK Tools		
BIN2CCS	1.0.0.0	MCSDK
IBL	1.0.0.10	MCSDK
MAD-UTILS	1.0.0.3	MCSDK
EEPROM Writer	1.0.0.4	MCSDK
NAND Writer	1.0.0.4	MCSDK
NOR Writer	1.0.0.3	MCSDK
MCSA	1.0	MCSDK
POST	1.0.0.4	MCSDK
PDK-C6678	1.1.0.0	MCSDK
Platform Library	2.0.0.10	MCSDK
NIMU Transport	1.0.0.7	MCSDK
IPC SRIO Transport	1.0.0.0	MCSDK
IPC QMSS Transport	1.0.0.0	MCSDK
CSL	1.0.1.1	MCSDK
PA LLD	1.1.1.0	MCSDK
CPPI LLD	1.0.0.16	MCSDK
QMSS LLD	1.0.0.17	MCSDK
PCIe LLD	1.0.0.2	MCSDK
SRIO LLD	1.0.1.1	MCSDK
TSIP LLD	1.0.0.4	MCSDK
HYPLINK LLD	1.0.1.1	MCSDK
PDK-C6670	1.1.0.0	MCSDK
Platform Library	2.0.0.9	MCSDK
NIMU Transport	1.0.0.7	MCSDK
IPC SRIO Transport	1.0.0.0	MCSDK
IPC QMSS Transport	1.0.0.0	MCSDK
CSL	1.0.1.1	MCSDK
PA LLD	1.1.1.0	MCSDK
CPPI LLD	1.0.0.16	MCSDK
QMSS LLD	1.0.0.17	MCSDK
PCIe LLD	1.0.0.2	MCSDK
SRIO LLD	1.0.1.1	MCSDK

BCP LLD	1.0.0.7	MCSDK
FFTC LLD	1.0.0.14	MCSDK
TCP3D LLD	1.0.0.13	MCSDK
TCP3E LLD	1.0.0.10	MCSDK
Demos		
HUA Demo	2.0.0.4	MCSDK
Image Processing Demo	2.0.0.0	MCSDK
Other Tools		
CodeGen	7.4. 0A11278	download
CCS	5.1.0.09000	download
C6670/C6678 Simulator	1.0.1	CCS
C6618 Simulator	0.4.0	CCS
XDCTools	3.22.01.21	MCSDK
CG_XML	2.30	MCSDK
Python	2.7.2	download
minGW	mingw-get-inst-20 110316	download

New features:

- OpenMP

Issues Fixed:

- None

Known Issues:

- SDOCM00083333: “NDK Task clean conflicts with SYS/BIOS’ Task.deleteTerminatedTasks”