

DESCRIPTION

The M66004 is a 16-digit 5×7-segment vacuum fluorescent display (VFD) controller using the silicon gate CMOS technology.

It contains 160 ROM characters and 16 user-defined RAM characters and receives display character codes and various commands from MCU to control VFD.

FEATURES

- Built in 5×7-dot segment character ROM (160 characters based on JIS-C-6220. Customization is possible.)
- Built in character RAM for user definition (16 characters)
- Display digit length can be changed. (from 9 to 16 digits)
- One-digit display cycle setting can be changed. (2 cycles)
- Dimmer is adjustable. (8 grades)
- Cursor display ON or OFF is selectable. (SEG35 output)
- Built in automatic display digit increment function
- Auto increment of display digit
- Built in simultaneous all-digit display ON/OFF command
- Connection to MCU by three signal lines, CS, SCK and SDATA
- Instruction execution with one byte*
- 2 general purpose output ports
- Positive supply voltage $V_{CC}=5V \pm 10\%$
- Pull-down voltage $V_P=V_{CC}-40V$ typ.
 $V_{CC}-45V$ max. (peak)

*: Except for character RAM setting

APPLICATION

AV equipments, vacuum fluorescent display controller for POS system, etc.

FUNCTION

The M66004 is a 16-digit 5×7-segment VFD (Vacuum Fluorescent Display) controller and contains 160 ROM characters based on JIS-C-6220 which can be customized and 16 RAM characters that user can define freely.

Character display codes and various commands are received as 8-bit serial data from MCU.

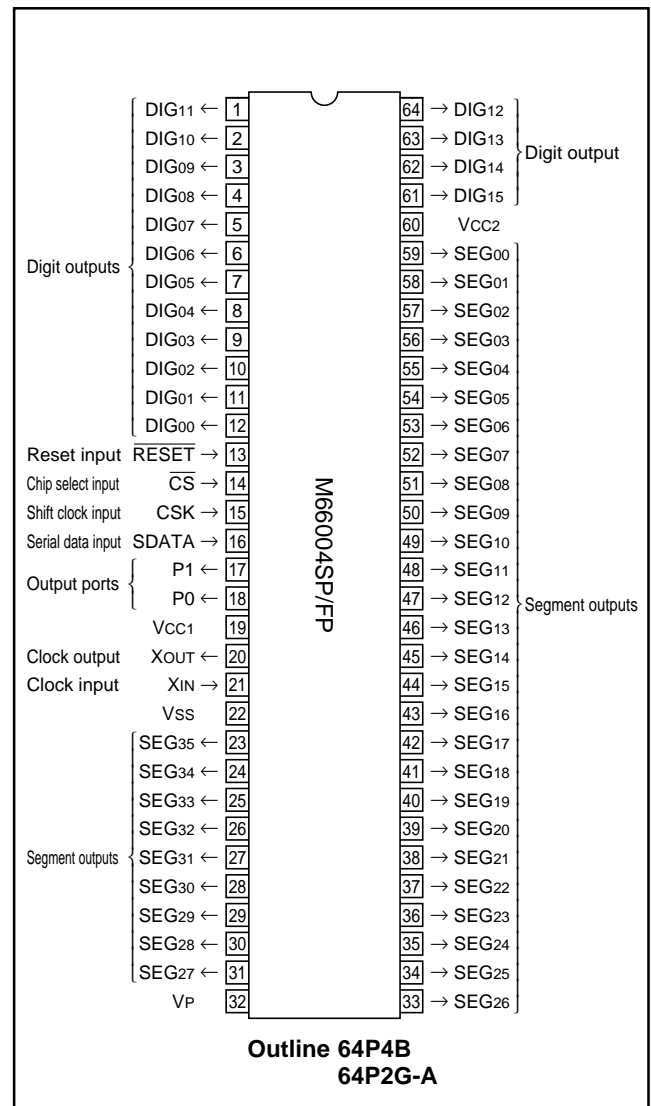
Main functions of this IC are display digit length setting, one-digit display cycle setting and dimmer adjustment.

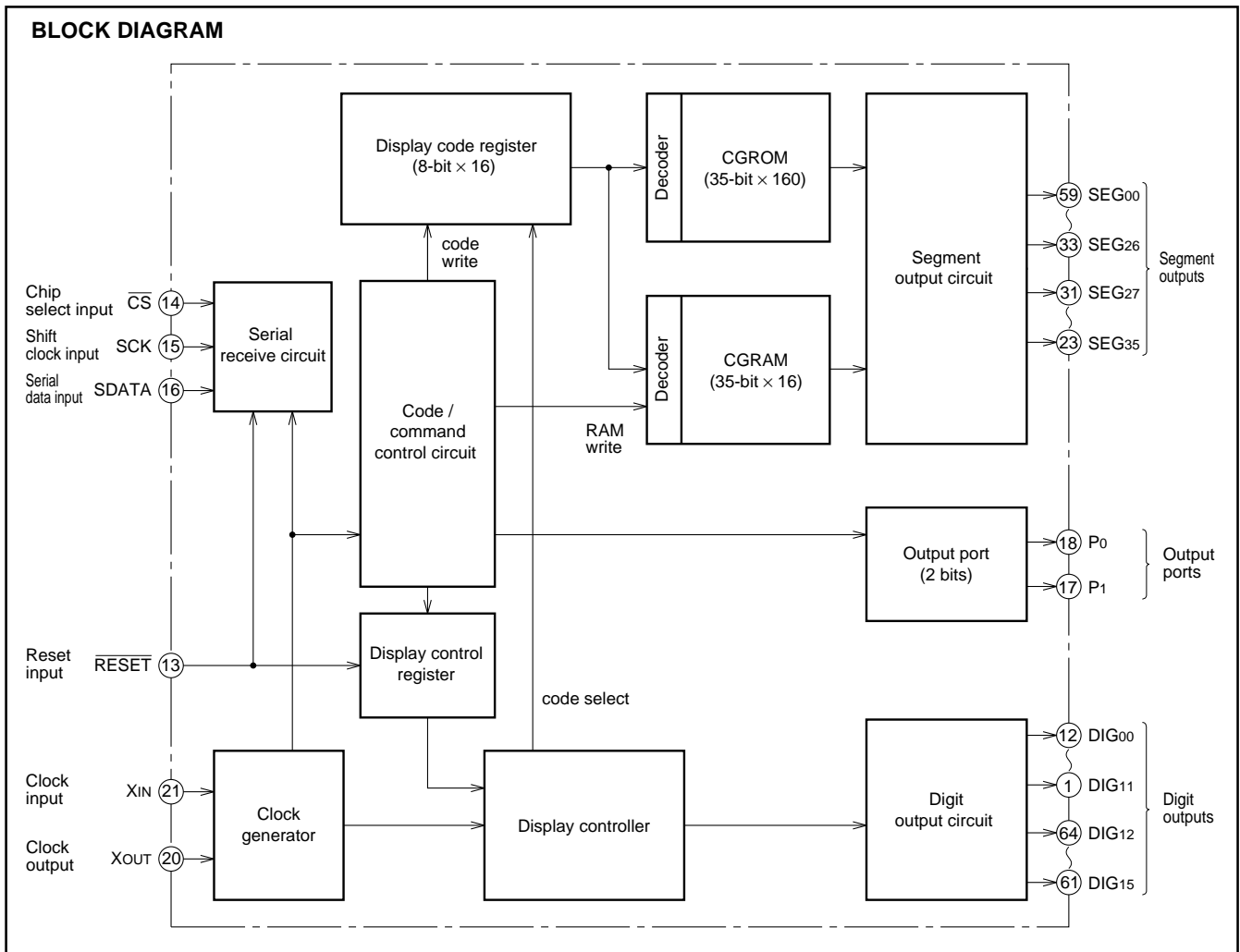
It has 36 segment output pins from SEG00 to SEG35 and uses 35 pins from SEG00 to SEG34 for character display.

SEG35 output, which is independent of other segment, is used for cursor display by inputting either cursor ON or OFF command.

Once display data is fixed, display continues automatically until new data is input. Therefore, MCU can execute other jobs and is lightened of its load.

Additionally, the IC can control other I/O elements because it contains two static operation output ports.





OPERATION FLOW

The serial receive circuit receives 8-bit serial data, latches the data by an internal latch signal, and then masks inputs from the MCU.

When the code/command circuit receives a command, it sets the display control register to the fixed state, and when it receives a character code, it writes the code to one of 16 display code registers which corresponds to the digit to display. The display controller outputs digit ON/OFF timing pulse to the digit output circuit and selects one character code to display among display code registers. The selected character code is input to either CGROM decoder or CGRAM decoder and then converted into a 35-bit character pattern to input the segment output circuit.

PIN DESCRIPTIONS

Symbol	Pin name	Function
$\overline{\text{RESET}}$	Reset input	This pin is used to initialize the internal state of the M66004.
$\overline{\text{CS}}$	Chip select input	In "L" state, communication with the MCU is possible. In "H" state, any instruction from the MCU is neglected.
SCK	Shift clock input	At the rising edge from "L" to "H", input data is shifted.
SDATA	Serial data input	Character code or command data to display is input from MSB.
XIN, XOUT	Clock input Clock output	This pin is used to connect a resistor and a capacitor externally to set oscillation frequency. (maximum oscillation frequency $f_{osc(max)}=1\text{MHz}$) An external clock can be also connected. In this case, an external clock is connected to pin XIN, and pin XOUT is set to be in open state.
DIG00~ DIG15	Digit output	These pins are used to connect to digit pins of VFD. Pins from DIG00 to DIG15 correspond to digits of VFD from the first digit to the 16th digit.
SEG00~ SEG35	Segment output	These pins are used to connect to segment pins of VFD. Pins from SEG00 to SEG35 correspond to segment pins of VFD as shown in the table below.
P0, P1		Output port (static operation)
VCC1		Positive power supply for internal logic
VCC2		Positive power supply for high-pressure-resistant output port
VSS		GND
VP		Negative power supply for VFD drive

(Connection of segment output pins)

Each ☐ in the table shows one-dot segment and each figure in ☐ shows the output segment pin number (from 00 to 35) to connect.

00	01	02	03	04
05	06	07	08	09
10	11	12	13	14
15	16	17	18	19
20	21	22	23	24
25	26	27	28	29
30	31	32	33	34

☐ 35

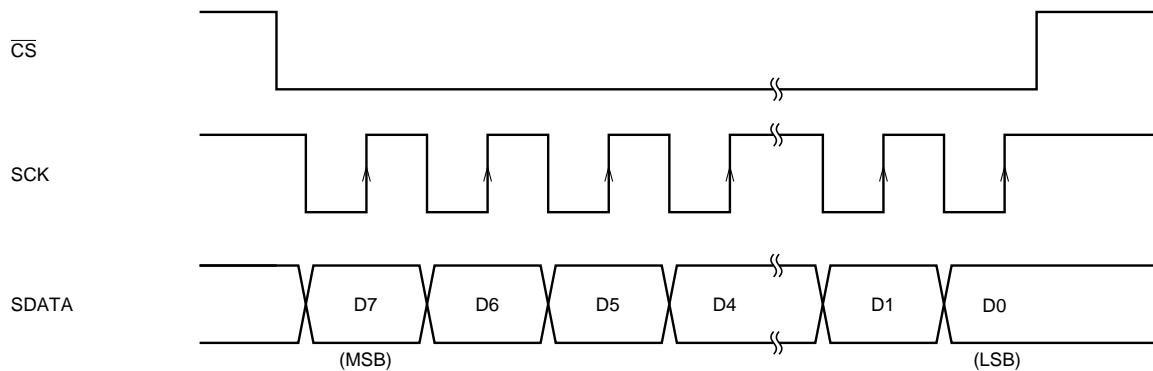
DATA TRANSMISSION FORMAT

The MCU transmits 8-bit serial data (MSB-first) to the M66004. In either case of command data or character code data, the M66004 operates by transmitting only one-byte data.

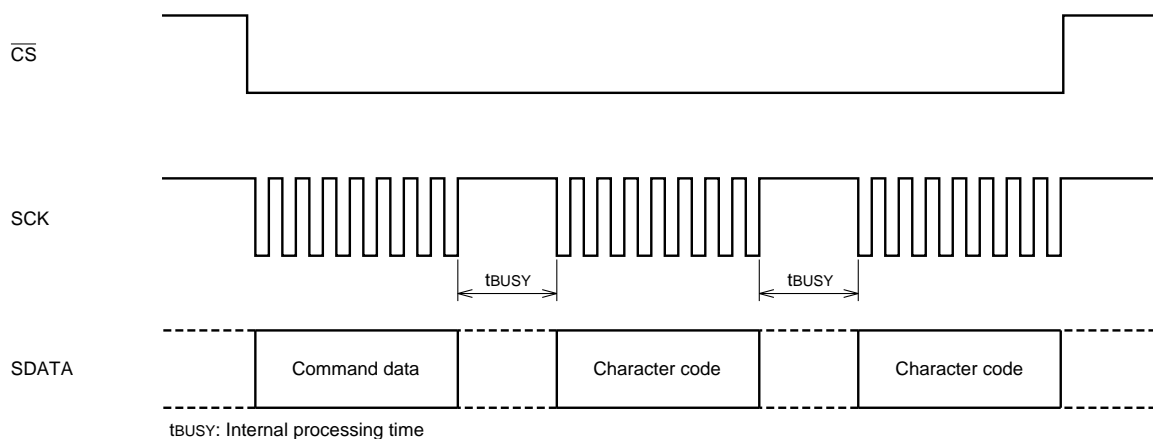
However, in case of character RAM data setting, it is necessary to send 7-byte data continuously.

(Transmission timing)

Standard timing



Continuous data transmission timing (In the following example, command data is transmitted and then character codes are transmitted continuously.)



COMMAND LIST

Function	Command code								Contents
	D7	D6	D5	D4	D3	D2	D1	D0	
Display digit length setting	0	0	0	0	0	*	*	*	The number of digits to display is fixed. Eight types of setting (from 9 digits to 16 digits) are possible.
Dimmer value setting	0	0	0	0	1	*	*	*	Eight types of setting (from 1/16 to 14/16) are possible for dimmer value.
One-digit display frequency setting	1	1	1	1	0	1	1	*	Either 128/fosc or 256/fosc is selected as one-digit display frequency.
Display digit setting	1	1	1	0	*	*	*	*	The first character code received after executing this command is displayed as designated by this command.
Auto increment setting	1	1	1	1	0	1	0	*	This command is executed to set or cancel the automatic display digit increment function.
Cursor ON	0	0	0	1	*	*	*	*	This command is executed to make SEG35 ON.
Cursor OFF	1	0	0	0	*	*	*	*	This command is executed to make SEG35 OFF.
All digit display ON/OFF	1	1	1	1	0	0	*	*	This command is executed to make all-digit display OFF or all-digit/segment display ON.
Write to user RAM	1	1	1	1	1	1	×	×	Character data is written into RAM for user definition.
Output port state setting	1	1	1	1	1	0	*	*	Output ports P0 and P1 are set or reset.

×: Optional

*: Refer to the description of command.

DESCRIPTION OF COMMAND

1. Display digit length setting

D7	D6	D5	D4	D3	D2	D1	D0
0	0	0	0	0			

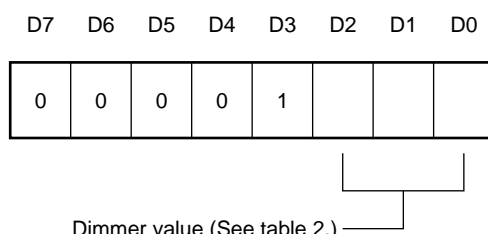
Display digit length (See table 1.)

Table 1 Display digit length

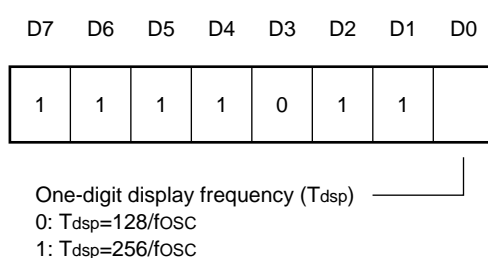
D2	D1	D0	Display digit length
0	0	0	9
0	0	1	10
0	1	0	11
0	1	1	12
1	0	0	13
1	0	1	14
1	1	0	15
1	1	1	16

2. Setting of dimmer value and one-digit display frequency(Tdsp)

1) Dimmer value setting

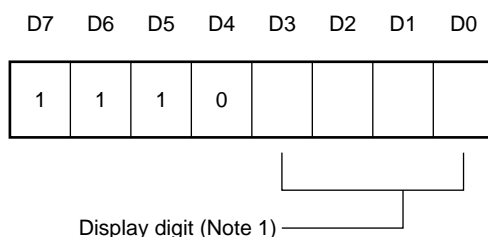


2) One-digit display frequency (Tdsp) setting

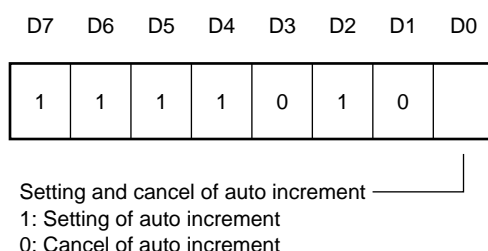


3. Display digit setting, setting and cancel of auto increment

1) Display digit setting



2) Setting and cancel of auto increment



Note 1: When the display digit is defined as N, N equals [decimal values to which D3, D2, D1 and D0 are converted + 1]. In case of (D3, D2, D1, D0)=(1, 0, 0, 0), N is 9. The first character code received after executing this command is despoiled to the 9th digit.

Table 2 Dimmer value

D2	D1	D0	Dimmer value (Tdig/Tdsp)
0	0	0	1/16
0	0	1	2/16
0	1	0	4/16
0	1	1	6/16
1	0	0	8/16
1	0	1	10/16
1	1	0	12/16
1	1	1	14/16

Tdig : One-digit-ON pulse width
Tdsp: One-digit display frequency

4. All-digit display ON/OFF

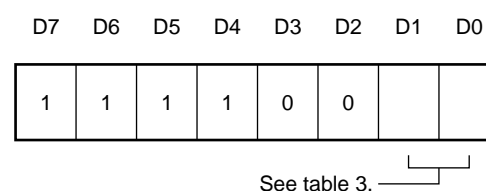
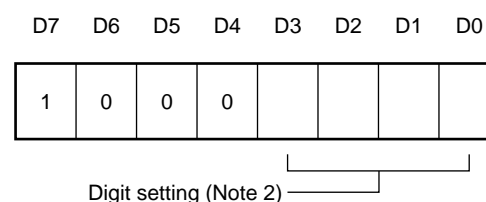


Table 3 ON/OFF control

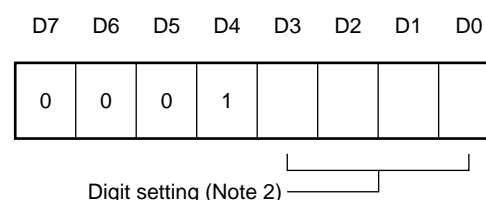
D1	D0	Operation
×	0	All-digit display OFF (All-OFF)
0	1	Normal operation mode setting
1	1	All-digit/segment display ON (All-ON)

×: Don't care

5. Cursor OFF (SEG35 output OFF)



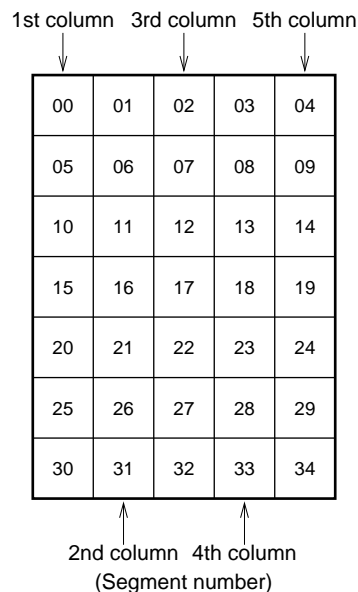
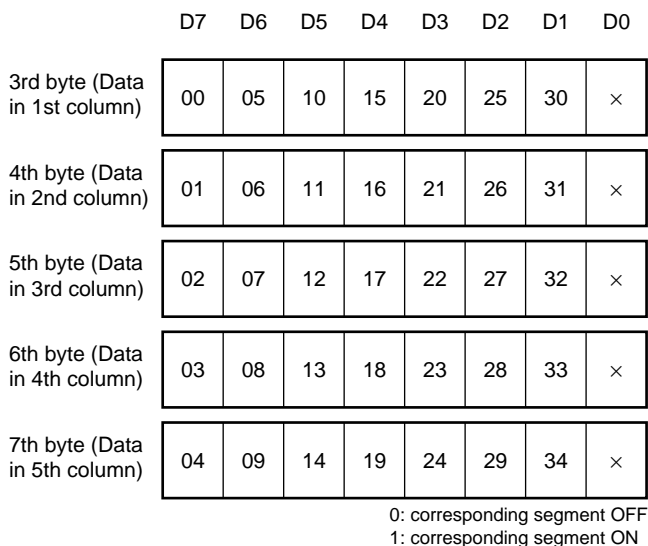
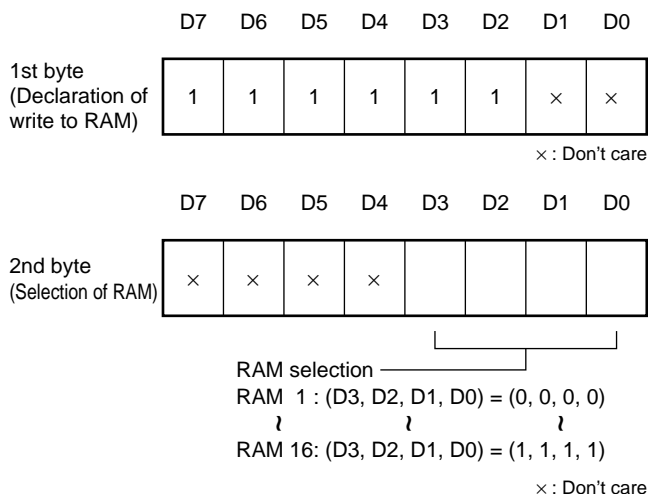
6. Cursor ON (SEG35 output ON)



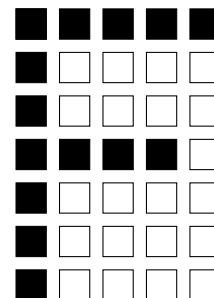
Note 2: The digit to which the cursor to make display ON or OFF belongs is designated. When digit is defined as N, N equals [decimal values to which D3, D2, D1 and D0 are converted + 1]. In case of (D3, D2, D1, D0)=(1, 0, 0, 0), N is 9. ON/OFF control of the cursor which belongs to the 9th digit is possible.

7. Write to character RAM for user definition

Character codes from RAM1 to RAM 16 are assigned to data from 90₁₆ to 9F₁₆ respectively.



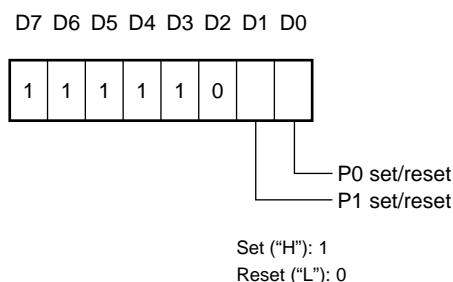
Ex. In order to define "F" (see the figure) as character RAM 1, 7-byte data as shown in the following table should be transmitted in serial order.



	Transmission data	Remarks
1	FC16	Declaration of write to RAM 1
2	0016	Selection of RAM 1
3	FE16	Setting of 1st-column segment data
4	9016	Setting of 2nd-column segment data
5	9016	Setting of 3rd-column segment data
6	9016	Setting of 4th-column segment data
7	8016	Setting of 5th-column segment data

All optional bits are set to “0”.

8. Output port setting



DESCRIPTION OF FUNCTION

1. Display digit length setting

The display digit length can be set within the range from 9-digit to 16-digit according to the number of digits of VFD. In case that the display digit length is set to 12 when a 16-digit VFD is used, digits from 13 to 16 are displayed as blank.

2. Dimmer value and one-digit display frequency (T_{dsp}) setting

Regarding dimmer value, 8-stage setting is possible. One-

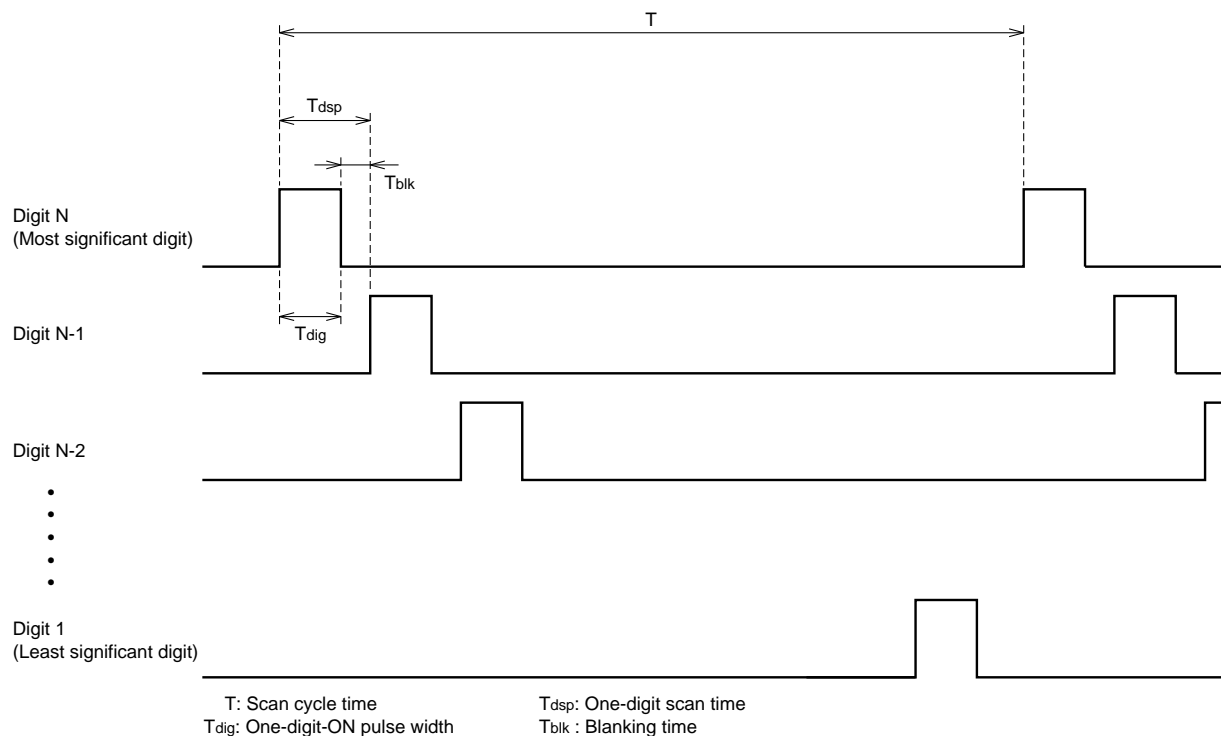
digit display scan time (T_{dsp}) can be set to either $T_{dsp}=128/f_{OSC}$ or $T_{dsp}=256/f_{OSC}$.

Scan cycle time T is set to $T=T_{dsp} \times N$ (N : display digit length).

Ex. $f_{OSC}=455\text{kHz}$, $N=16$

$T_{dsp}=281\mu\text{s}$, $T=4.5\text{ms}$ (222Hz) or $T_{dsp}=562\mu\text{s}$, $T=9.0\text{ms}$ (111Hz)

DISPLAY TIMING



3. Character display

A character (refer to the character code table) which corresponds to the character code transmitted from the MCU is displayed as the designated digit.

The display digit is designated by the display digit setting command. In case that the display digit designated by this command is set to increase automatically, the display digit is automatically increased by 1. After a character is displayed to the most significant digit, it is displayed to the least significant digit.

When the auto increment setting is canceled, the display digit is fixed.

Ex. In order to display a character A to digits 14, 15, 16, 1 and 2 in this order and characters A, B and C to digit 8 successively, 12-byte data must be transmitted in the following order.

(Display digit length = 16)
A (code 41₁₆)
B (code 42₁₆)
C (code 43₁₆)

	Transmission data	Display digit	Remarks
1	F5 ₁₆	—	Setting of auto increment
2	ED ₁₆	—	Display digit is set to the 14th digit.
3	41 ₁₆	14	Transmission of character code
4	41 ₁₆	15	
5	41 ₁₆	16	
6	41 ₁₆	1	
7	41 ₁₆	2	
8	F4 ₁₆	—	Cancel of auto increment setting
9	E7 ₁₆	—	Display digit is set to the 8th digit.
10	41 ₁₆	8	Transmission of character code
11	42 ₁₆	8	
12	43 ₁₆	8	

4. All-digit display ON/OFF

All digits are simultaneously lighted or put off by inputting all-digit display ON command or all-digit display OFF command. When all-digit display ON command is transmitted, all digits including a cursor and all segments are lighted.

Therefore, this command can be used for the lamp test. When all-digit display ON setting is canceled (normal operation mode is selected), all digits are displayed according to the setting before all-digit display ON operation. It is also possible to set the next character to the designated digit during the all-digit display ON operation.

When all-digit display OFF command is transmitted, all digits including a cursor are put off. When all-digit display OFF setting is canceled (normal operation mode is selected), all digits are displayed according to the setting before all-digit display OFF operation. It is also possible to set the next character to the designated digit in the all-digit display OFF state.

OPERATION AT RESET

When a reset signal is input, the internal state is initialized as shown in Table 4.

Table 4 Initial state

Contents	Setting after reset
Display (characters and cursor)	All digits: OFF
Display digit length, dimmer value, display digit, one-digit display frequency, user character RAM state, auto increment setting	Values immediately before reset are held.
Serial receive circuit	Reset
Output port setting	Set

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter		Conditions	Ratings	Unit
VCC	Supply voltage	VCC1, VCC2		-0.3 ~ + 7	V
VP	Pull-down voltage		(Note 3)	VCC-45 ~ VCC + 0.3	V
VI	Input voltage			-0.3 ~ VCC + 0.3	V
VO	Output voltage	XOUT, P0, P1		-0.3 ~ VCC + 0.3	V
Pd	Maximum power dissipation	SP	Any external circuits are not connected to the IC. Ta=25°C (Note 4)	1.6	W
		FP	The IC is mounted on a board. Ta=25°C (Note 5)	1.4	
Tstg	Storage temperature			-65 ~ 150	°C

Notes 3: Frequency of pulse waveform exceeding VCC-40V:450μs or above, pulse width: 30μs or below, output load: CL≤200pF

4: When Ta is 25°C or above, derating (12.8mW/°C) should be conducted.

5: When Ta is 25°C or above, derating (11.2mW/°C) should be conducted.

RECOMMENDED OPERATIONAL CONDITIONS (Ta=-10 ~ 70°C unless otherwise noted)

Symbol	Parameter		Conditions	Limits			Unit
				Min.	Typ.	Max.	
VCC	Supply voltage	VCC1, VCC2		4.5	5	5.5	V
VP	Pull-down voltage			VCC-40		VCC	V
VSS	Supply voltage				0		V
VI	Input voltage			0		VCC	V
Topr	Operating temperature			-10		70	°C

DC CHARACTERISTICS (Ta = -10 ~ 70°C, VCC = 5V ± 10%, VSS = 0V)

Symbol	Parameter		Conditions	Limits			Unit
				Min.	Typ.	Max.	
VIH	"H" input voltage	SDATA		0.7VCC			V
VIL	"L" input voltage					0.3VCC	V
VT+	Positive direction threshold voltage	RESET		0.35VCC	2.8	0.7VCC	V
VT-	Negative direction threshold voltage	CS SCK, XIN		0.2VCC	2	0.55VCC	V
IiH	"H" input current		Vi=VCC			1	μA
IiL	"L" input current		Vi=0V			-1	μA
VOH	"H" output voltage	DIG output	IOH=-20mA	VCC-2			V
		SEG output	IOH=-5mA	VCC-2			
		P0, P1	IOH=-2mA	VCC-0.8			
VOL	"L" output voltage	P0, P1	IOL=2mA			0.55	V
Io	Output pull-down current	DIG, SEG	VP=VCC-40V, Vo=VCC	250	500	850	μA
ICC	Positive supply current	VCC1, VCC2	Vi=0V, Output pin: open VP=VCC-40V			100	μA
			Output pin: open Output "H", clock stop VP=VCC-40V			40	mA
IP	Negative supply current	VP	Output pin: open Output "H", clock stop VP=VCC-40V			-40	mA

Note 6: Total of current at pin VCC1 and current at pin VCC2.

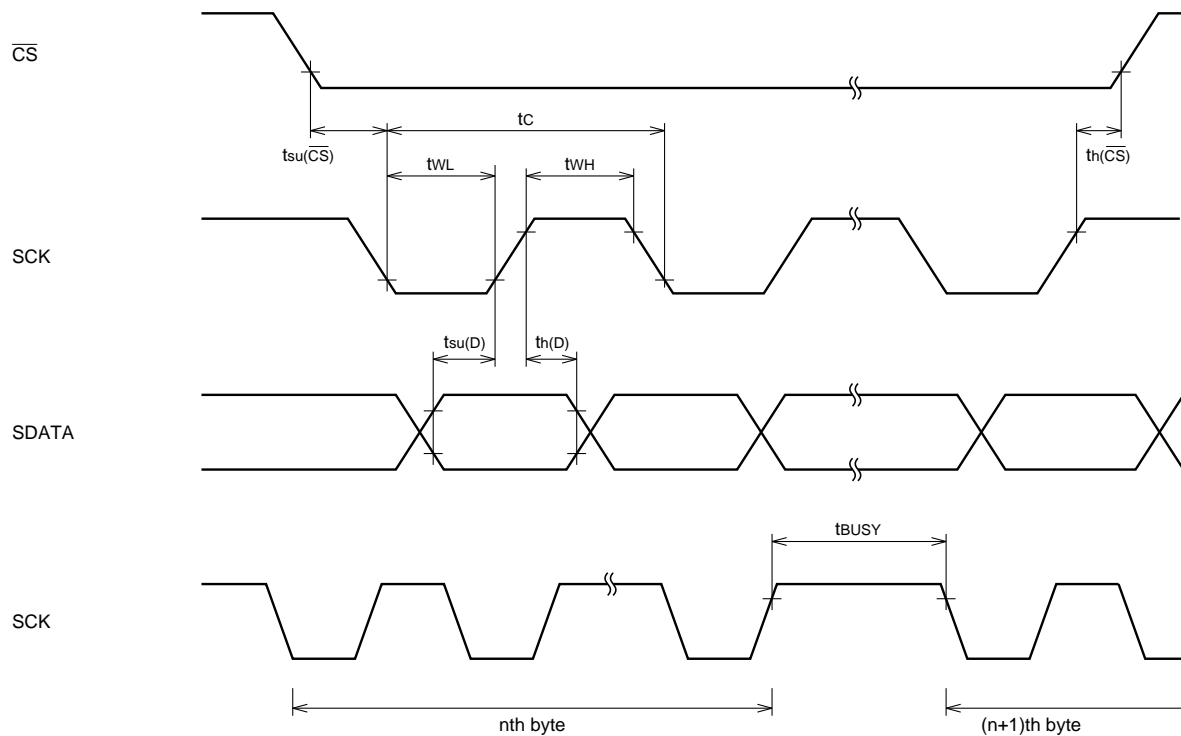
TIMING CONDITIONS ($T_a = -10 \sim 70^\circ\text{C}$, $V_{CC} = 5V \pm 10\%$, $V_{SS} = 0V$)

Symbol	Parameter	Conditions	Limits			Unit
			Min.	Typ.	Max.	
t _C	Clock cycle time	SCK	500			ns
		XIN	1000			
t _{WH}	Clock cycle pulse width	SCK	230			ns
		XIN	460			
t _{WL}	Clock low pulse width	SCK	230			ns
		XIN	460			
t _{su(D)}	SDATA setup time		130			ns
t _{h(D)}	SDATA hold time		100			ns
t _{su(CS)}	$\overline{\text{CS}}$ setup time		0			ns
t _{h(CS)}	$\overline{\text{CS}}$ hold time		130			ns
t _{BUSY} (Note 7)	Internal processing time				6	1/f _{OSC}

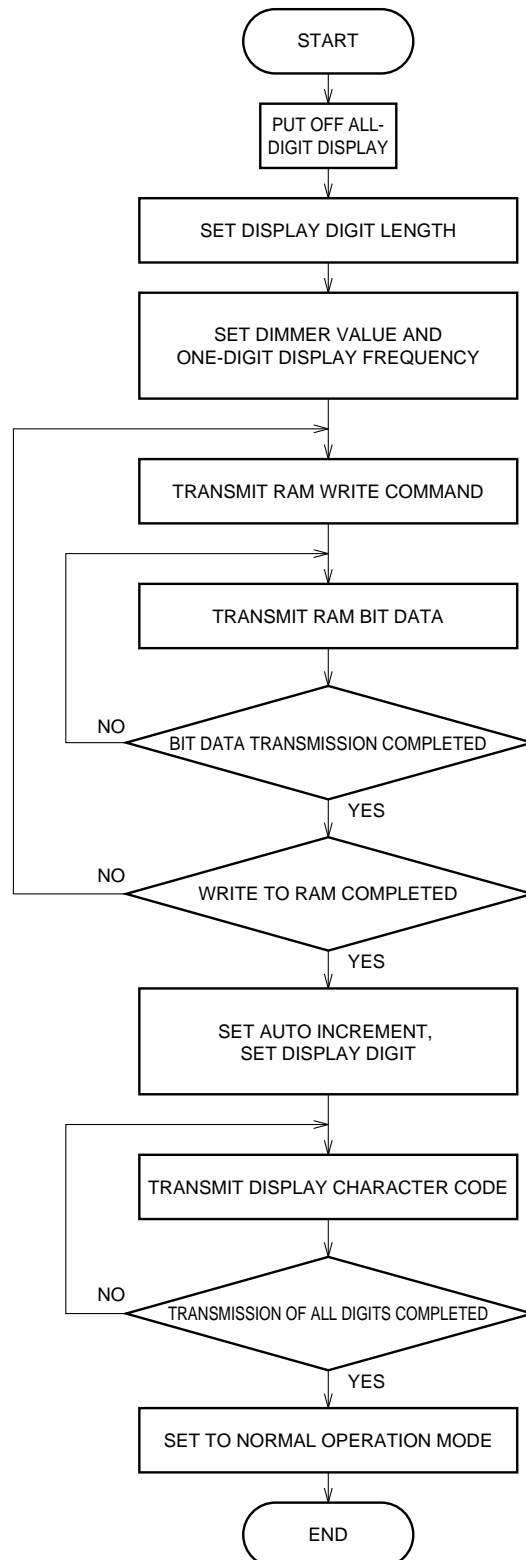
Note 7: This shows the internal operation time after receiving the serial data. Next data is prohibited in this period. Next data is available after 6T of the operation clock.

OSCILLATION CHARACTERISTICS ($T_a = -10 \sim 70^\circ\text{C}$, $V_{CC} = 5V \pm 10\%$, $V_{SS} = 0V$)

Symbol	Parameter	Conditions	Limits			Unit
			Min.	Typ.	Max.	
f _{OSC}	Oscillation frequency	C=100pF, R=27k Ω		500		kHz

TIMING WAVEFORM (Reference voltage: 0.3V_{CC}, 0.7V_{CC})


OPERATION FLOWCHART (Initial setting operation)



CHARACTER CODE LIST (conforming to JIS-C-6220)

Upper codes Lower codes	0000 (016)	0001 (116)	0010 (216)	0011 (316)	0100 (416)	0101 (516)	0110 (616)	0111 (716)	1000 (816)	1001 (916)	1010 (A16)	1011 (B16)	1100 (C16)	1101 (D16)	1110 (E16)	1111 (F16)
0000 (016)			SPACE							RAM1	SPACE					
0001 (116)										RAM2						
0010 (216)										RAM3						
0011 (316)										RAM4						
0100 (416)										RAM5						
0101 (516)										RAM6						
0110 (616)										RAM7						
0111 (716)										RAM8						
1000 (816)										RAM9						
1001 (916)										RAM10						
1010 (A16)										RAM11						
1011 (B16)										RAM12						
1100 (C16)										RAM13						
1101 (D16)										RAM14						
1110 (E16)										RAM15						
1111 (F16)										RAM16						

Note: Character codes which can be defined by users are 160 codes from 2016 to 7F16 and from A016 to DF16.
Codes except those from 2016 to 7F16 and from 9016 to DF16 are command codes.

APPLICATION EXAMPLE

