Maneesh Withanagamage

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EDUCATION

York University Toronto, Canada

Honours Bachelor of Engineering, Specialized in Computer Engineering *April 2021*

- Notable Courses: Software Design Patterns, Operating Systems, VLSI, Computer Architecture, Relational Databases.

SKILLS

Languages: JavaScript, Java, SQL, HTML/CSS, C, System Verilog, Embedded C
Software Tools: Node.js, Express.js, React.js, MySQL, MATLAB, RESTful APIs
Hardware: Cadence Virtuoso, HSPICE, Innovus

EXPERIENCE

4PAY Toronto, Canada

Technical Analyst *Apr 2020 - Sept 2020*

- Designed a proof of concept Block-chain using React for front end and Express for back-end.
- Proposed AWS Quantum Ledger Database as a solution to increase security of the company's ledgers.
- Increased efficiency by **over 50%**, automating the documentation of the company's main system with **500+** APIs.
- Assisted another team with creating a MVP for an app that was in the research phase.

CIBC Toronto, Canada

IT Project Coordinator - Payments Modernization *Jan 2019 - September 2019*

- Assisted the Quality Assurance (QA) team in writing requirements/user-stories for an automated reporting tool.
- Published project reports for the executive team: closure reports, requirements reports, and other project reports.
- Reconciled monthly finances for seven projects and thus, improved efficiency for monthly budget allocations by **50%**.
- Developed a proof of concept mobile app on improving their current mortgage application.

PROJECTS

Hospital Communication Application

Capstone Project

- Built a real time service for physicians and nurses to communicate efficiently in a busy hospital environment.
- Designed and implemented a database schema that would capture the complex communication network in hospitals.
- Built a RESTFul APIs that allowed the front-end to access and manage data.
- Technologies used: React, Express, REST API, MySQL, Figma

ASIC - Least Recently Used Algorithm

Course Project

- Developed and implemented the LRU algorithm on a 1.5x1.5 mm^2 40-pin chip in a 0.6-µm processor.
- Designed behavioural System Verilog code and tested using test benches before performing a RTL synthesis.
- The schematics and layouts for certain modules were custom created using Cadence Virtuoso.
- Technologies used: System Verilog, Virtuoso, Innovus, HSPICE, Synopsys Design Compiler

Automated Desk Heater

Personal Project

- Built a desk heater system using a 32-bit ARM based micro-controller (LPC802).
- Temperature, humidity and proximity sensors were used to automate the system.
- Hardware used: LPC802 Development board, 16 channel ADC-MUX, 2-channel 5V relay, seven segment display.