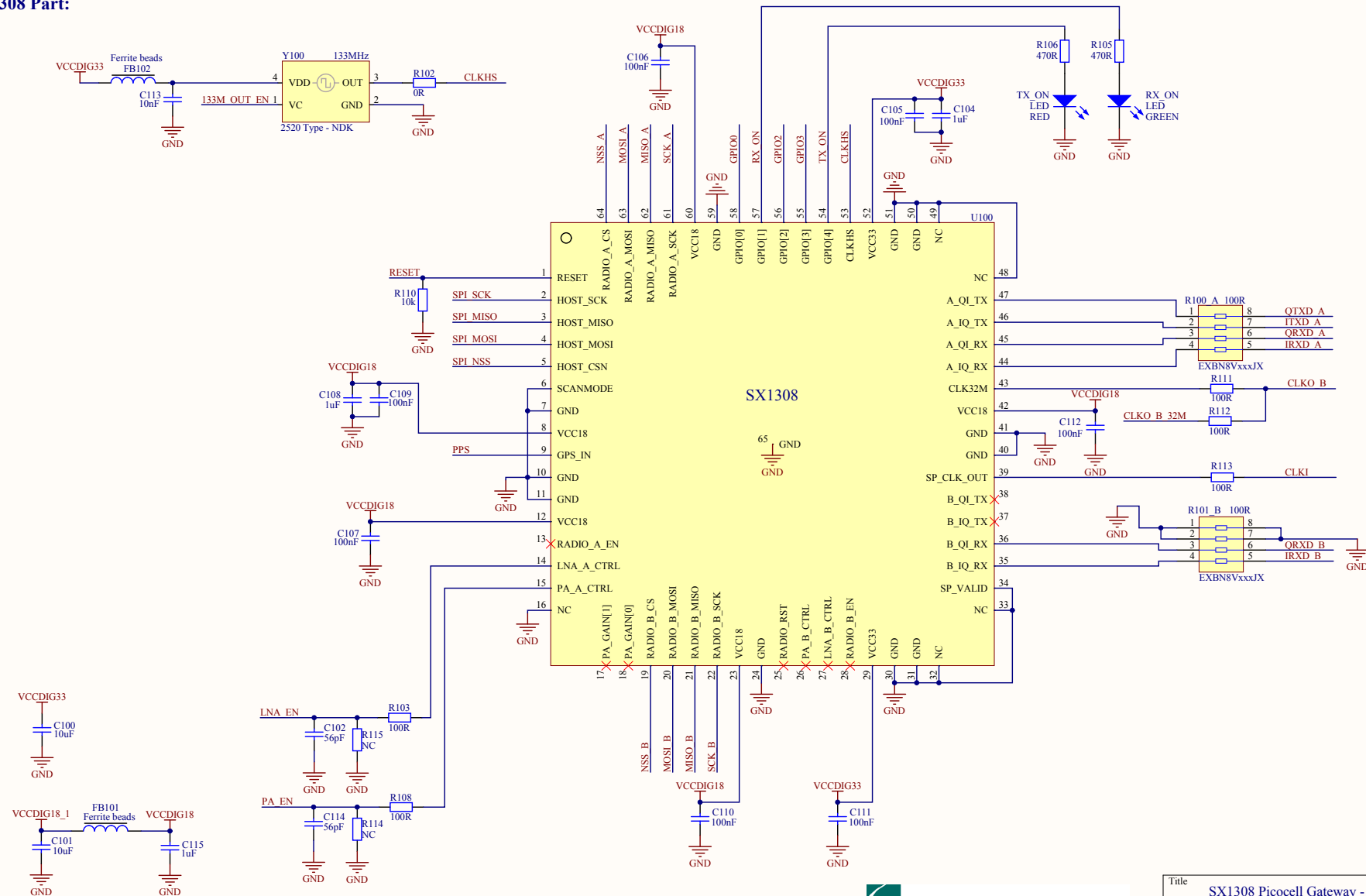
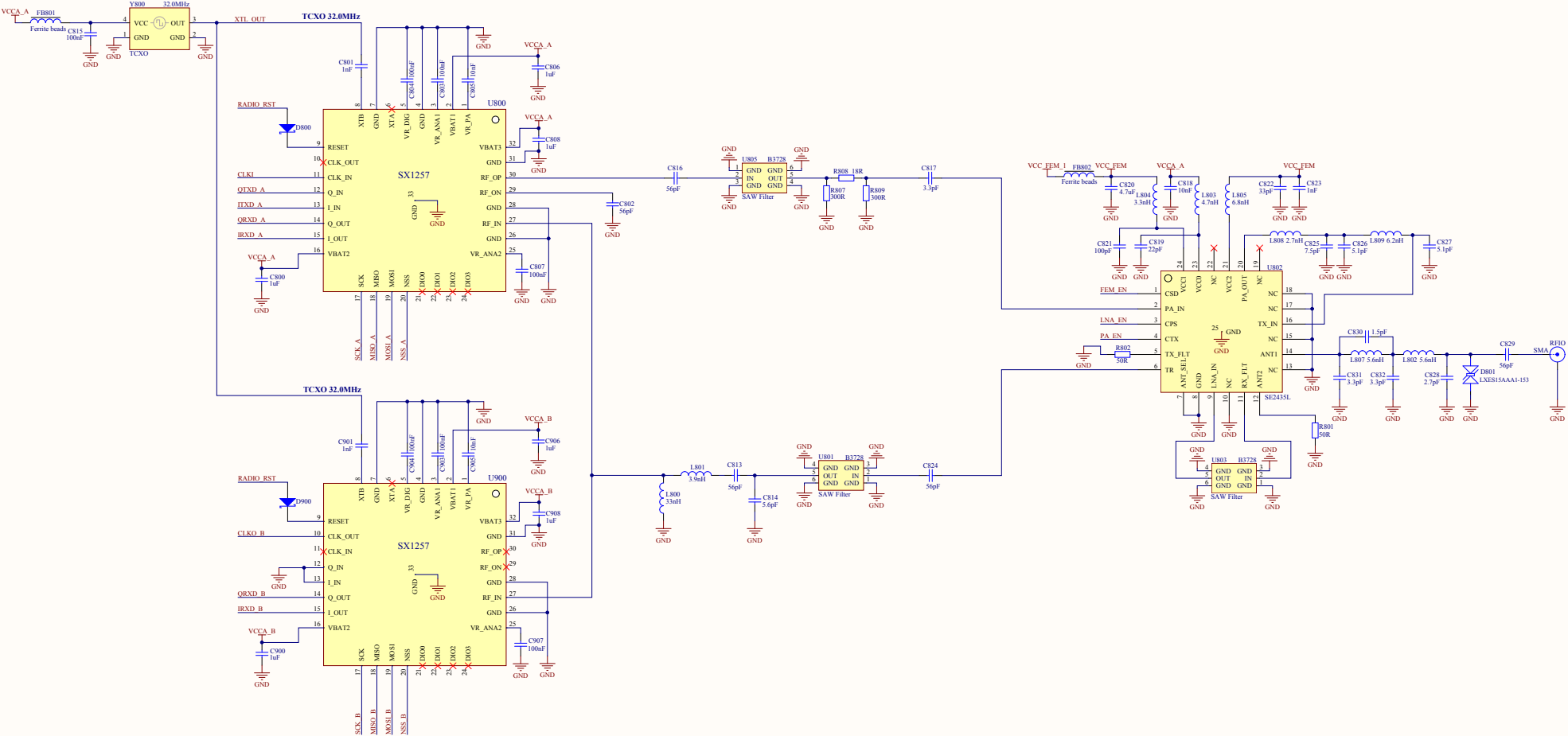


SX1308 Part:

Title SX1308 Picocell Gateway - SX1308 part			
Size B	Number PCB_E381V02C		Revision V1a
Date:	20.12.2017		Sheet of
File:	\\...SX1308 GTW_e381v02c_SX1308 part.dwg		

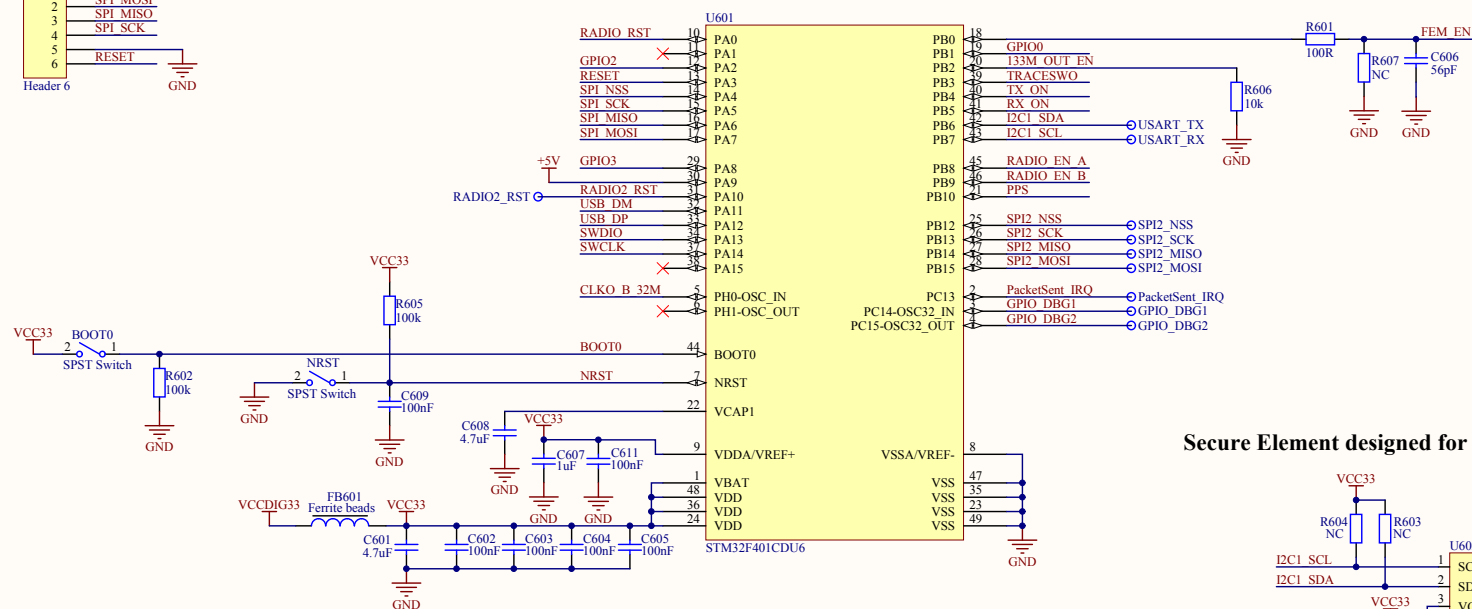
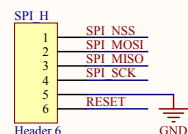
SX1308 Picocell Gateway - RF Part:



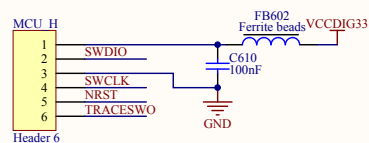
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SX1308 Picocell Gateway - RF part			
Size	Number	Revision	
A2	PCH_E381V02C	V1a	
Date:	20.12.2017	Sheet 1 of 1	
File:	\\SX1308_GTW_0381V02C_RF_Part_Sch	Drawn by:	

STM32 MCU Part :

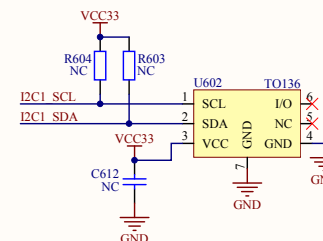
SPI Header for debug



MCU Prog Header :



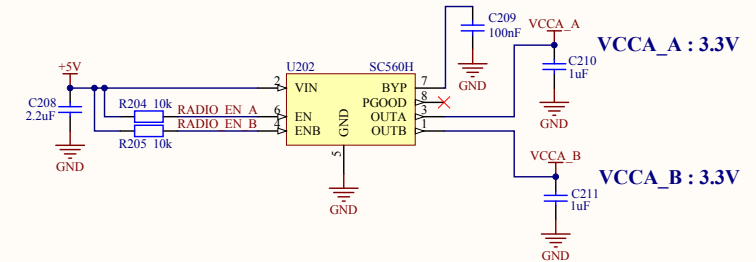
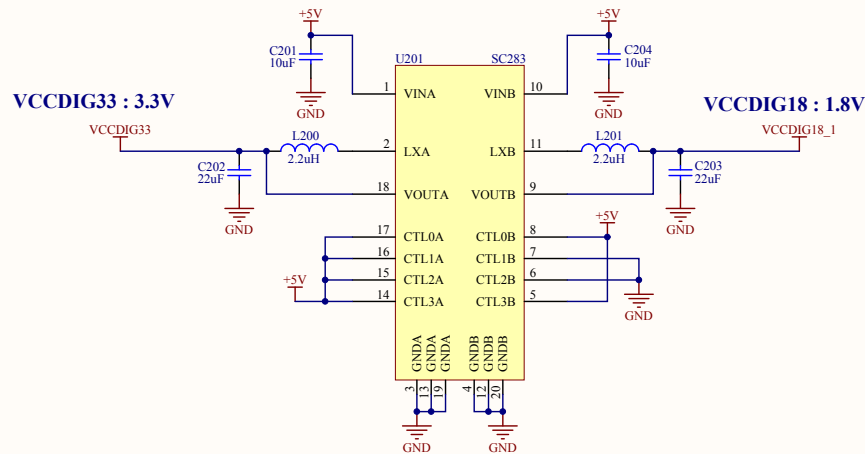
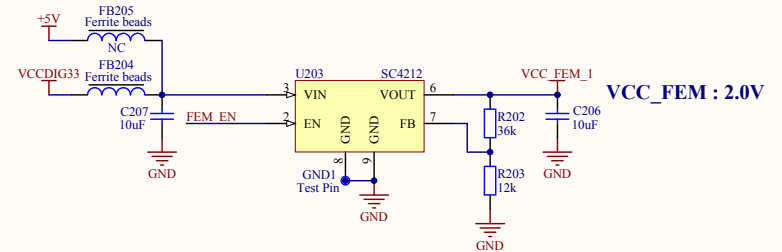
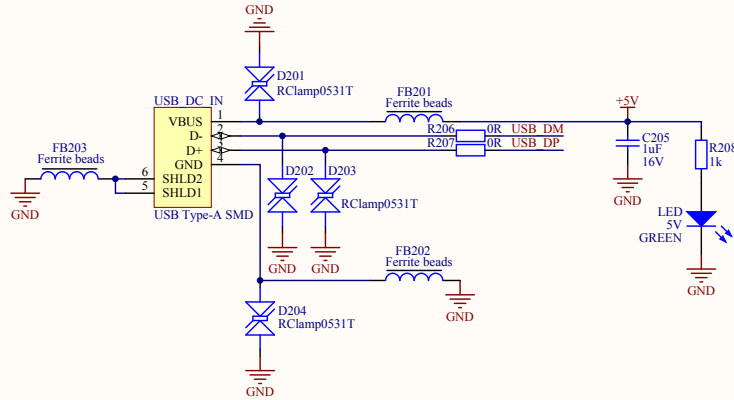
Secure Element designed for IoT and M2M apps (optional)



Title				SX1308 Picocell Gateway - STM32 MCU part			
Size		Number		Revision			
B		PCB_E381V02C		V1a			
Date:		20.12.2017		Sheet 3 of 5			
File:		\\.\SX1308 GTW_e381v02c_STM32 MCU		DischDoc		Erik Fountain	

DC Power Management for Picocell Gateway:

Main +5V USB power Supply



Title			
SX1308 Picocell Gateway - DC Power Management			
Size	Number	Revision	
B	PCB_E381V02C	V1a	
Date:	20.12.2017	Sheet 4 of 5	
File:	\\.\SX1308 GTW_e381v02c Power.Sch Drawn By: Erik Fountain		

SX1308 Picocell Gateway @915MHz - Reference Design :

TOP Schematic:

Designator
SX1308_GTW_e381v02c_Power.SchDoc



Designator
SX1308_GTW_e381v02c_RF_Part.SchDoc



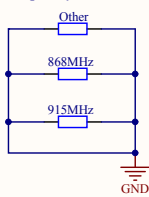
Designator
SX1308_GTW_e381v02c_SX1308_part.SchDoc



Designator
SX1308_GTW_e381v02c_STM32_MCU.SchDoc



Frequency reference:



Semtech logos + Fiducial_Marks:

- .0 Logo Recycling2
- .0 Logo ESD 2
- .0 Logo LoRa 1
- .0 Logo LoRa 2
- .0 Fiducial Mark1
- .0 Logo Semtech 1
- .0 Logo Semtech 2



Title			
SX1308 Picocell Gateway - TOP schematic			
Size	Number		Revision
B	PCB_E381V02C		V1a
Date:	20.12.2017	Sheet of	
File:	\\.\SX1308_GTW_e381v02c_Top.SchDoc		
	Drawn By:		