



Khulna University of Engineering & Technology (KUET), Khulna
Department of Computer Science and Engineering (CSE)

CSE 4224: Digital System Design Laboratory

Title: Design of an Arithmetic Circuit.

Name : Md. Noyan Ali

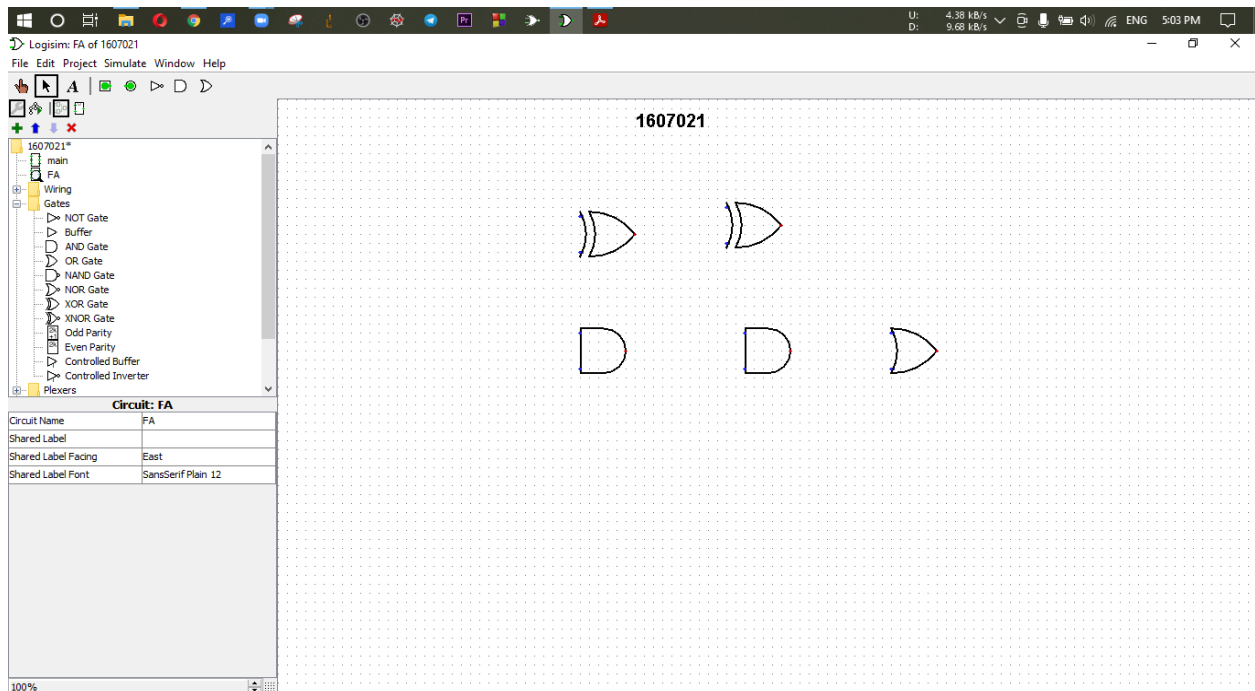
Roll : 1607021

Lab : 1

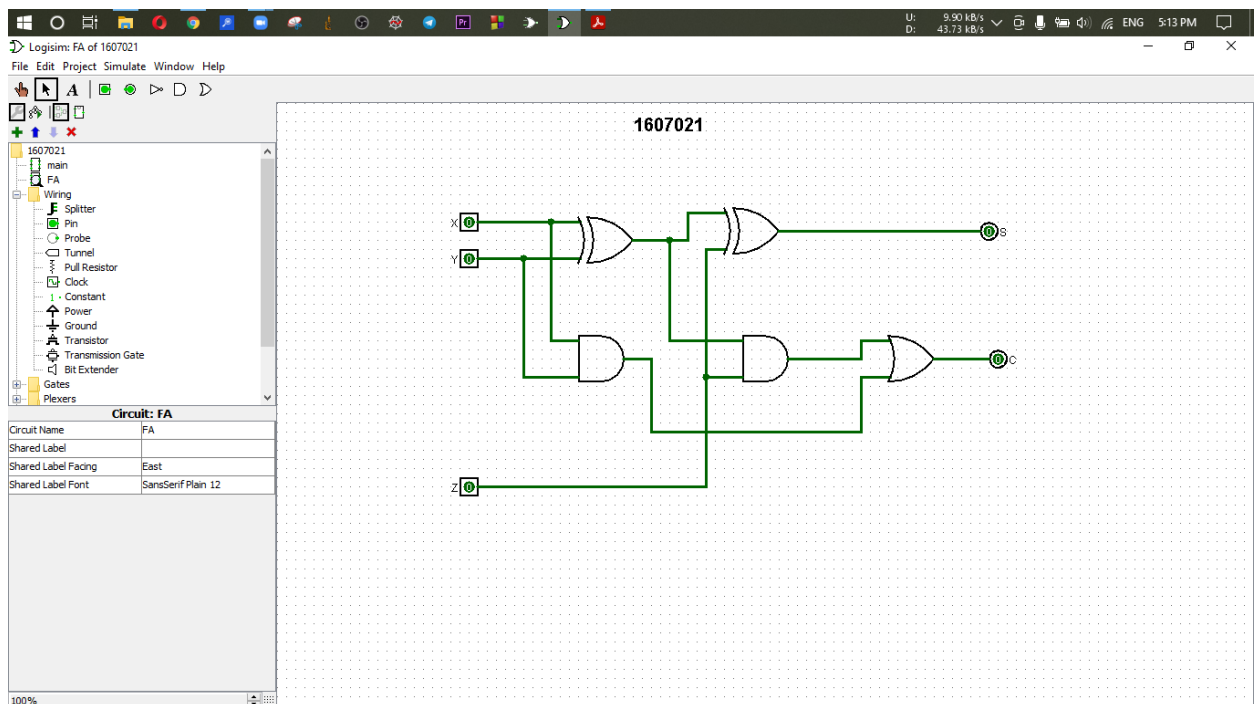
Date : 20/04/2021

The screenshots at regular interval of my work are noted below:

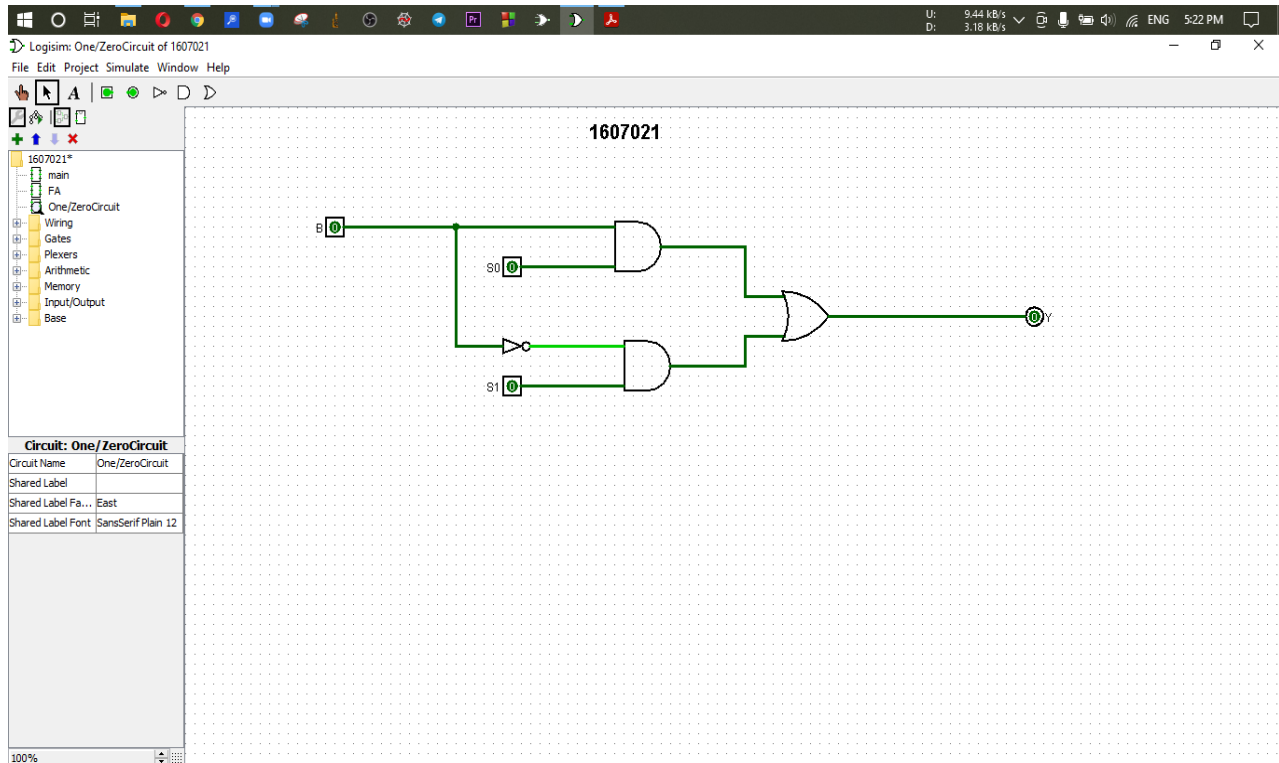
i) Very beginning of my work.



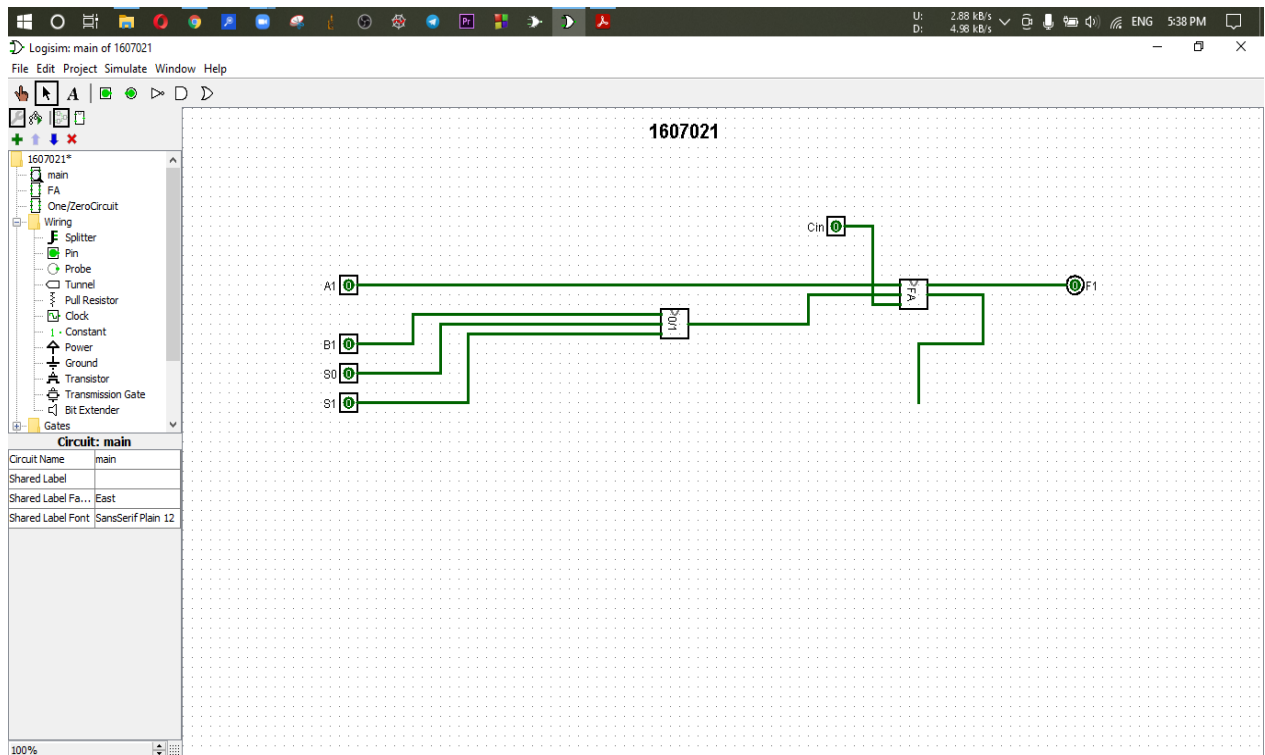
ii) Full adder circuit is completed.



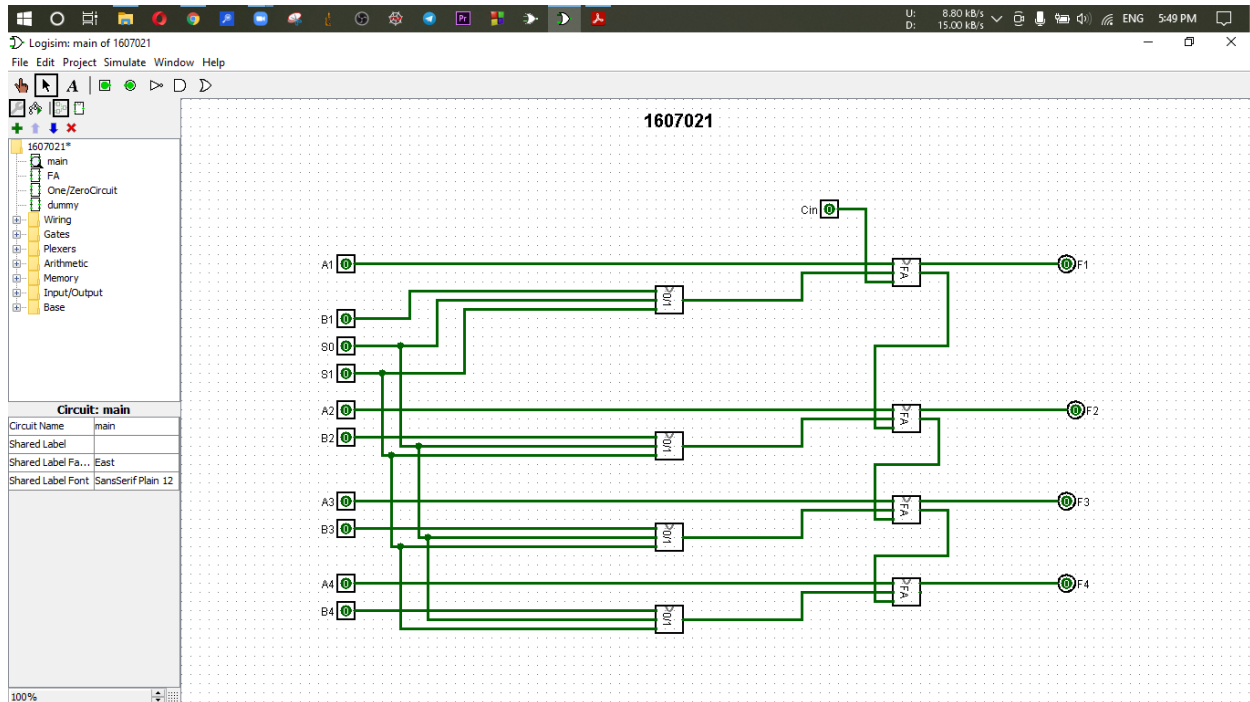
iii) One/Zero circuit is completed.



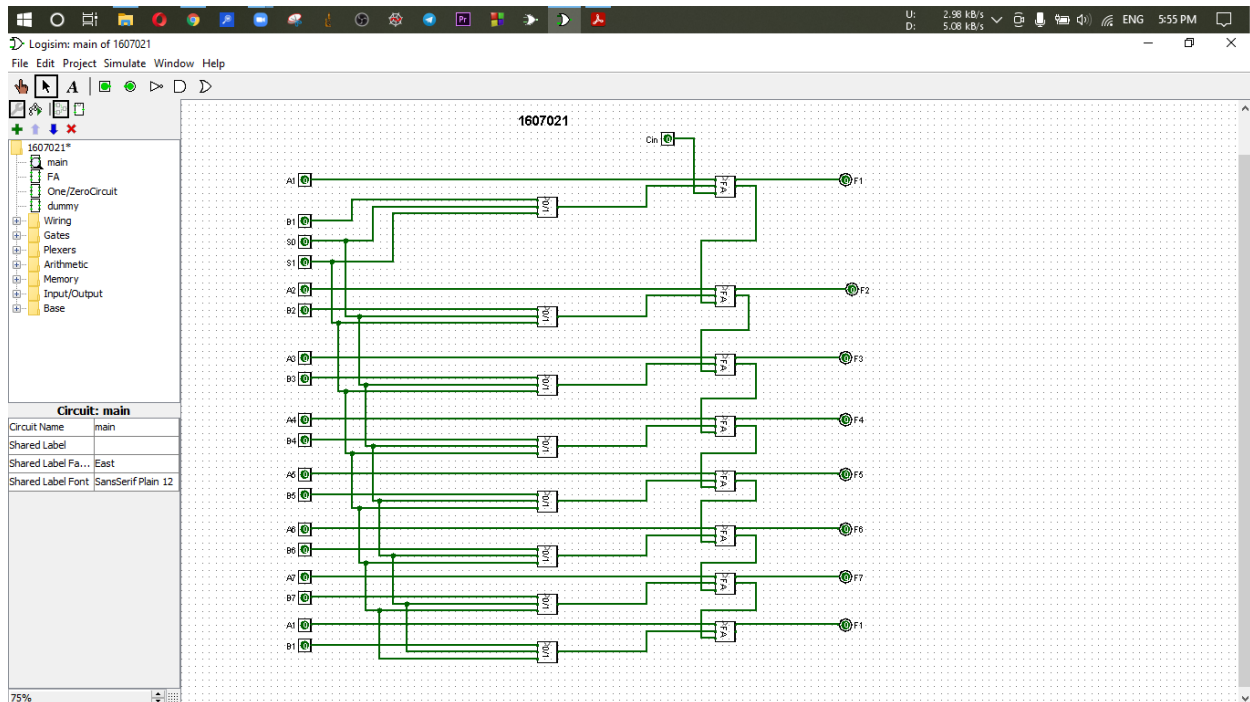
iv) Beginning of arithmetic circuit.



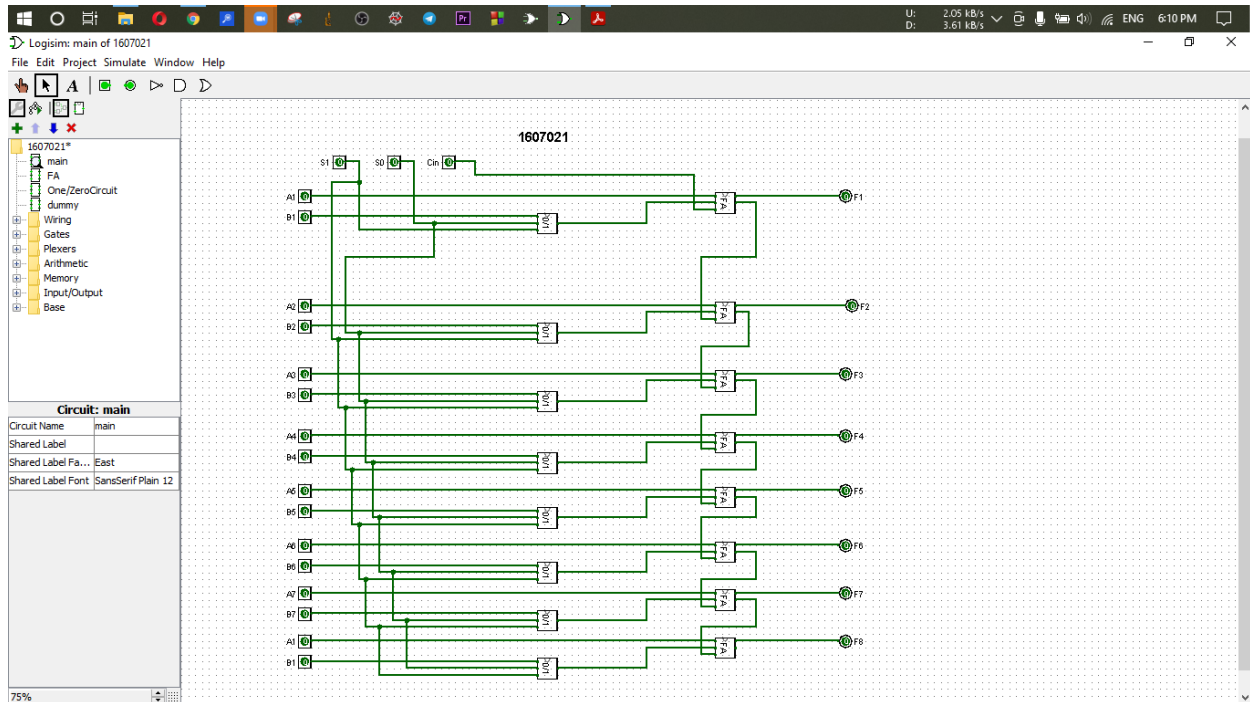
v) 50% progress of the arithmetic circuit.



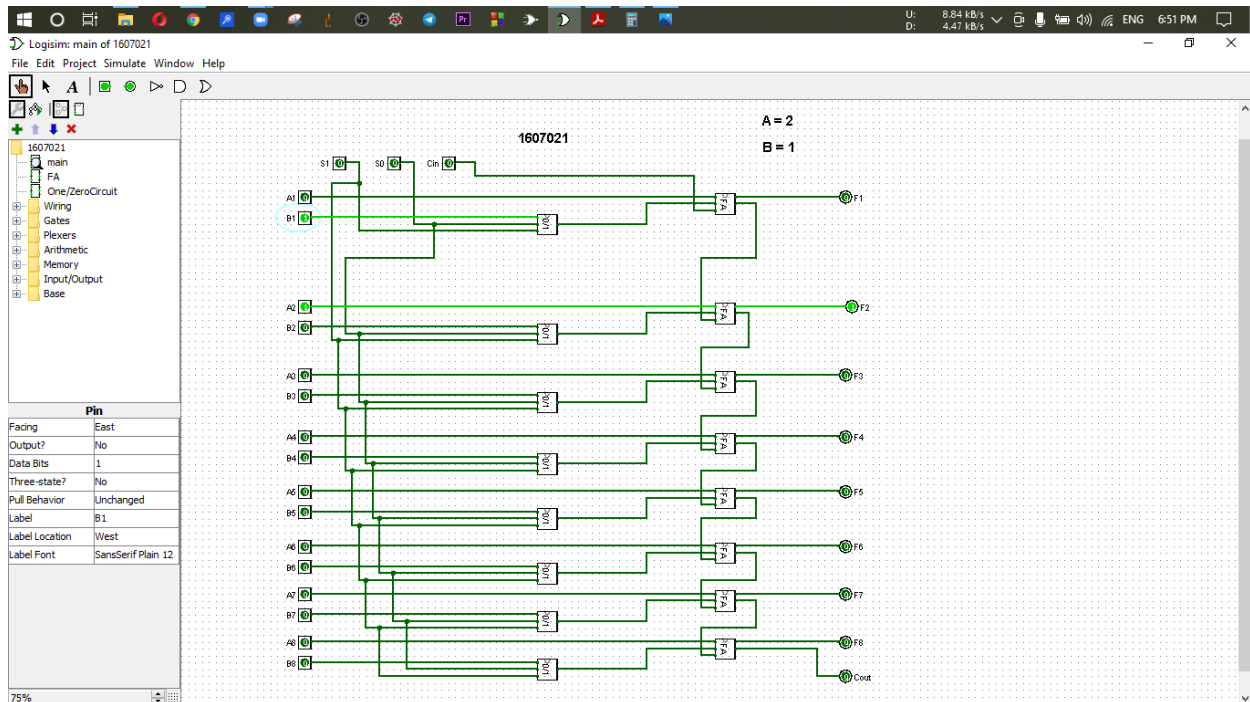
vi) Arithmetic circuit is completed.



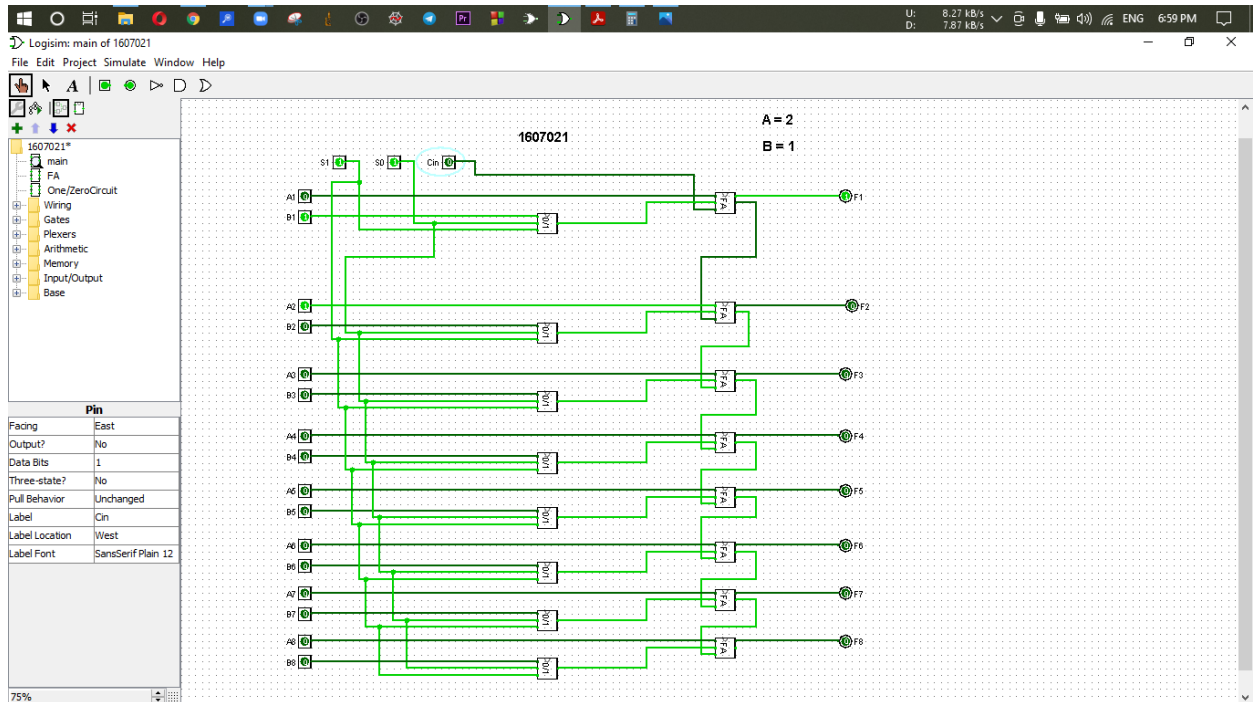
vii) Inputs of the arithmetic circuit are rearranged.



viii) Output verification for 1st out of 8 inputs.



ix) Output verification for 7th out of 8 inputs.



x) Output verification for last input.

