

# NVMemProg

## Non-Volatile Memory Programmer

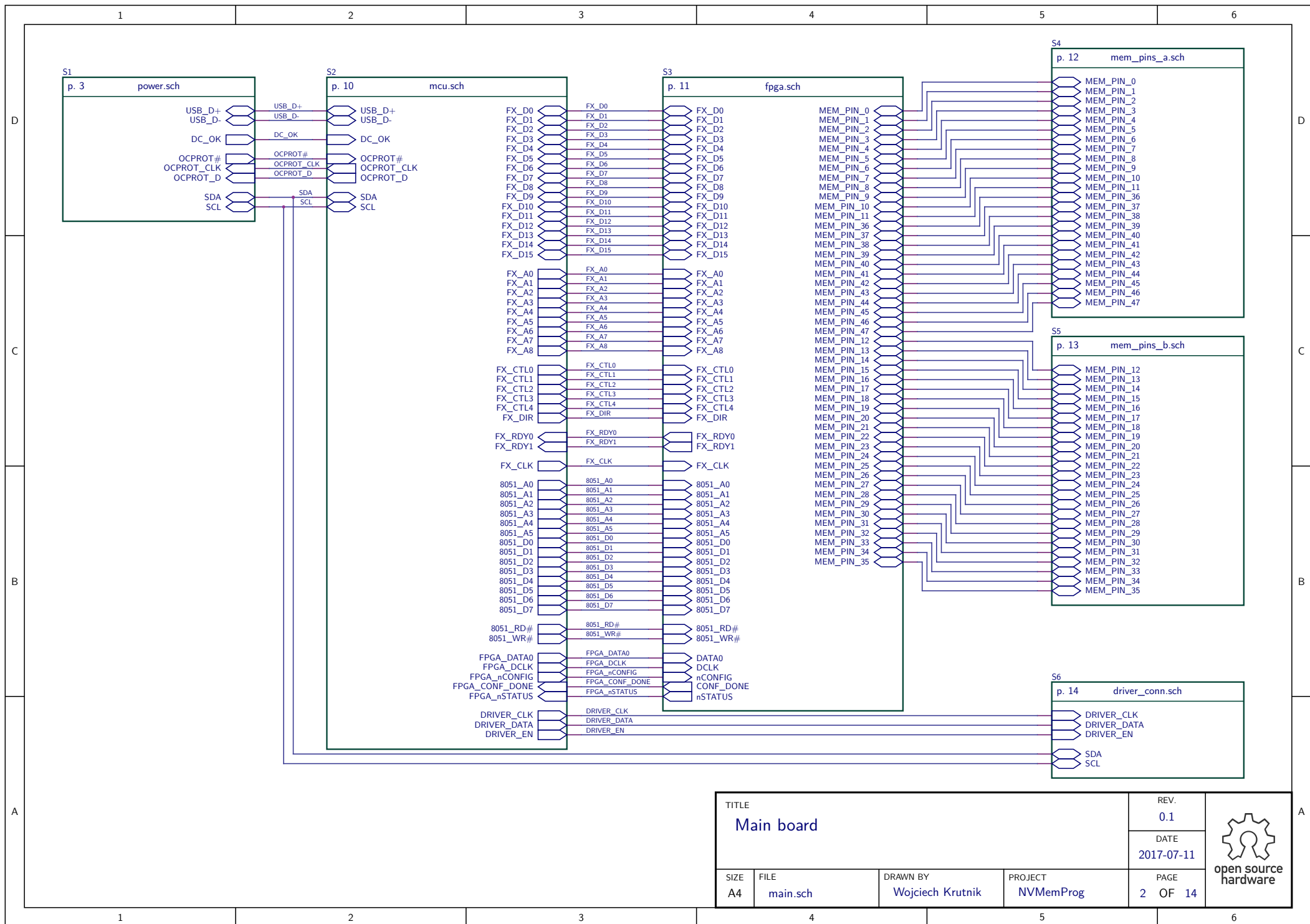
### Main Board Schematics

2017-07-11  
rev. 0.1

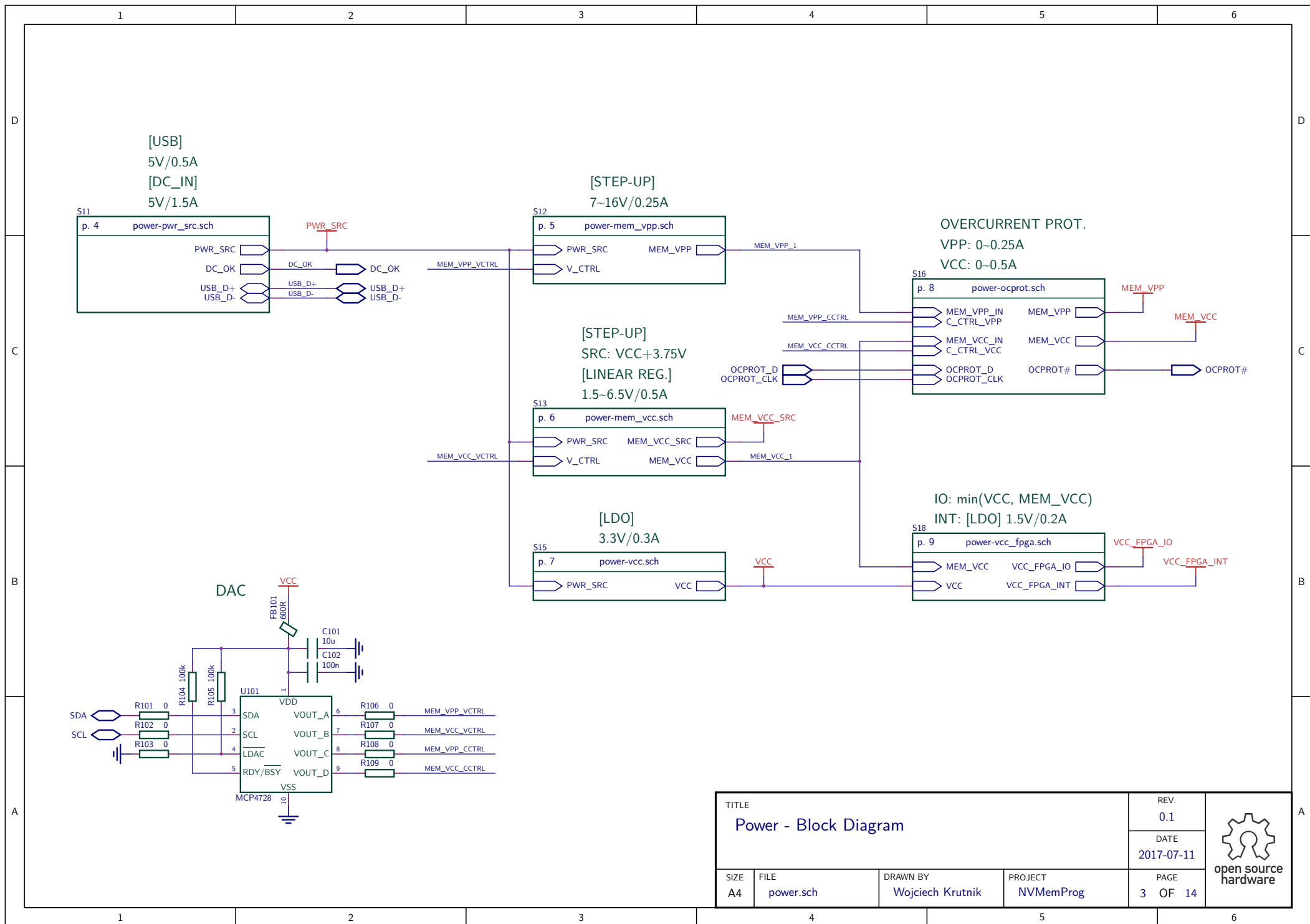
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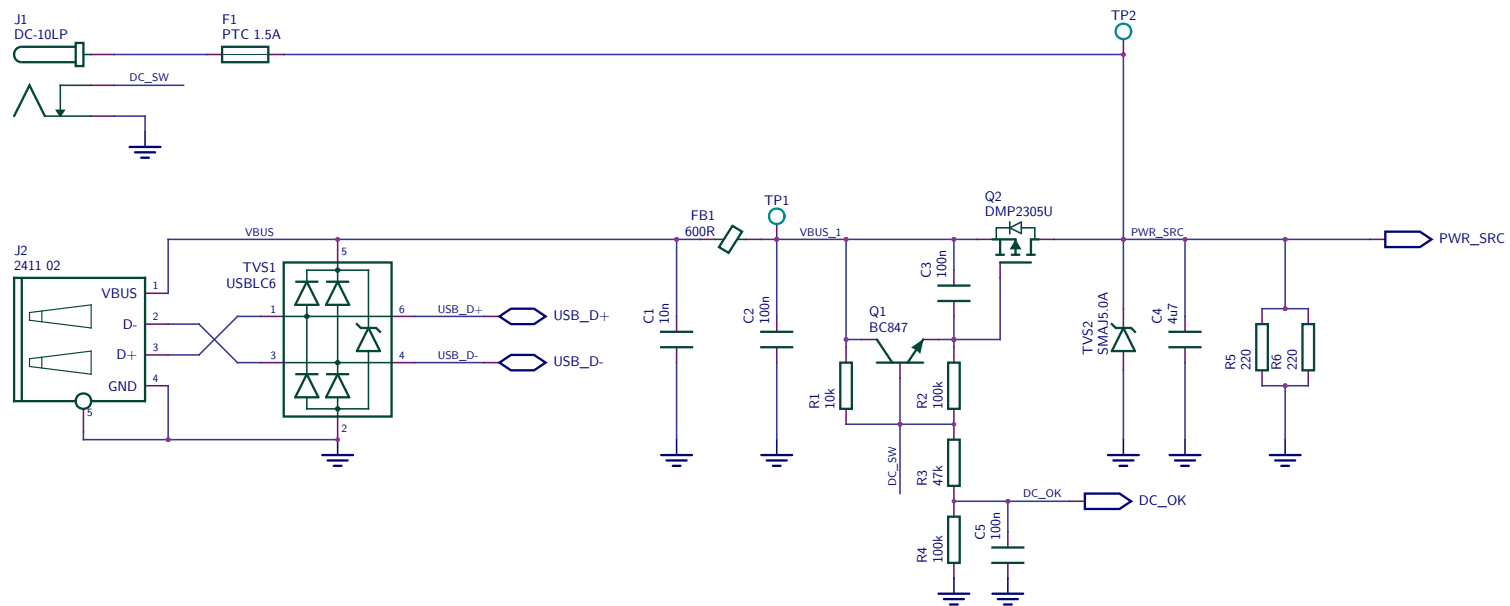



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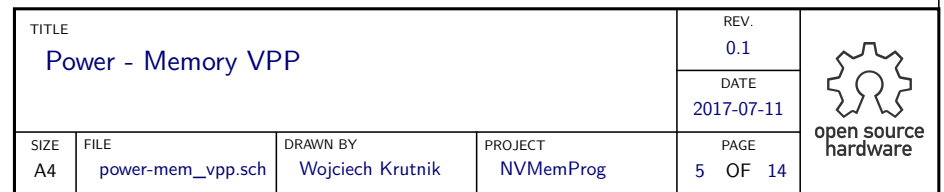


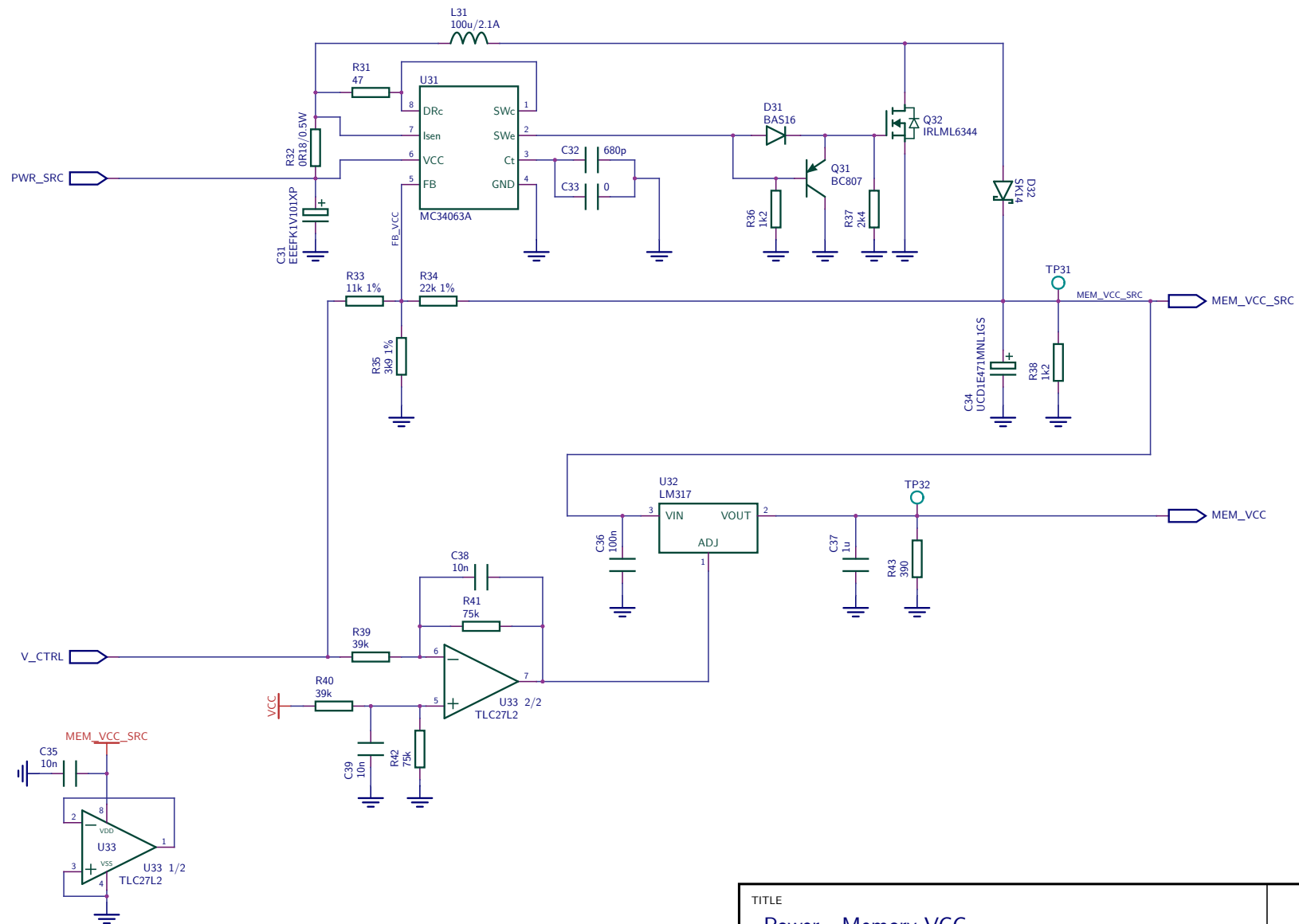
TITLE Main board			REV. 0.1	 open source hardware
			DATE 2017-07-11	
SIZE A4	FILE main.sch	DRAWN BY Wojciech Krutnik	PROJECT NVMemProg	PAGE 2 OF 14




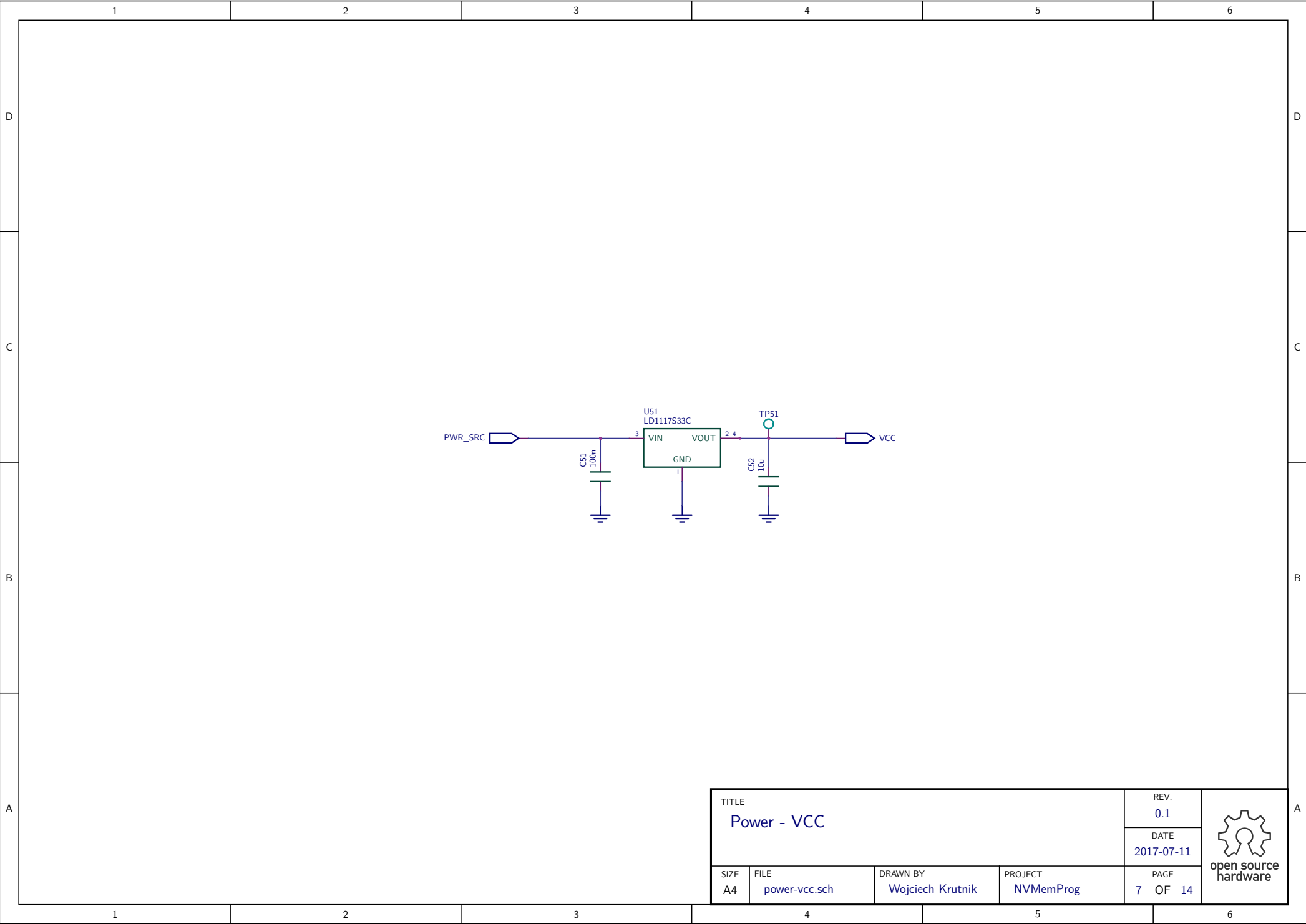



TITLE <b>Power - Source</b>				REV. 0.1	 open source hardware
				DATE 2017-07-11	
SIZE A4	FILE power-src.sch	DRAWN BY Wojciech Krutnik	PROJECT NVMemProg	PAGE 4 OF 14	





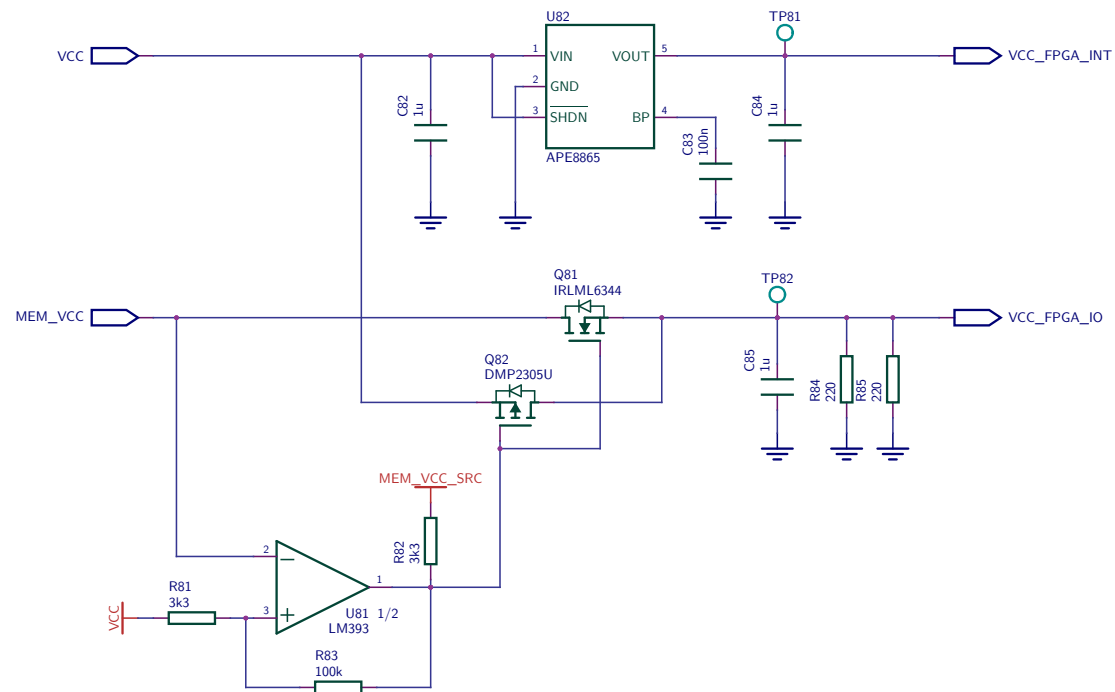
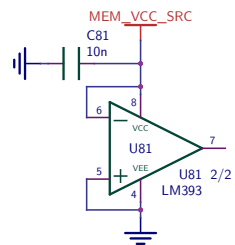
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SIZE A4	FILE power-mem_vcc.sch	DRAWN BY Wojciech Krutnik	PROJECT NVMemProg	PAGE 6 OF 14	




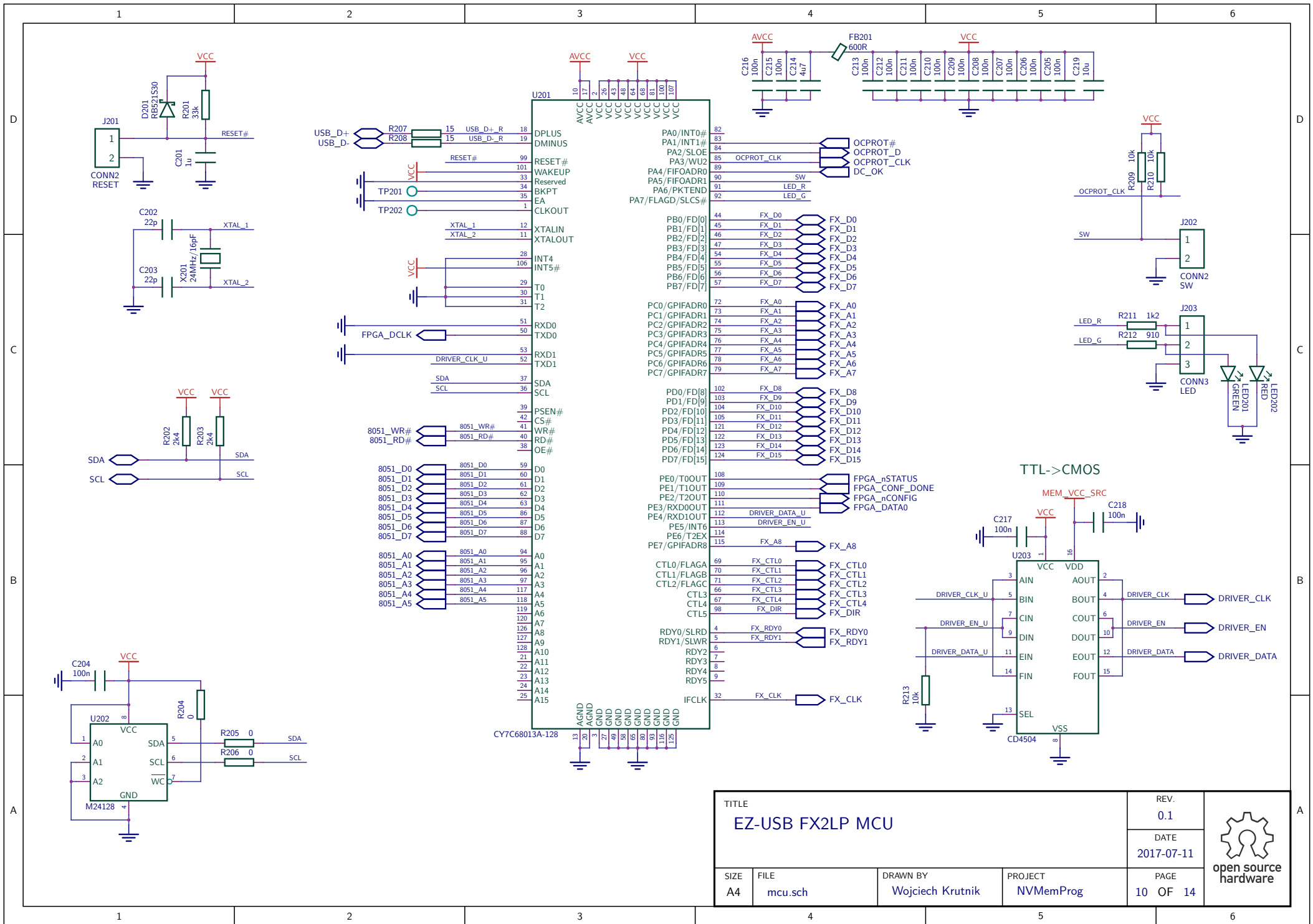
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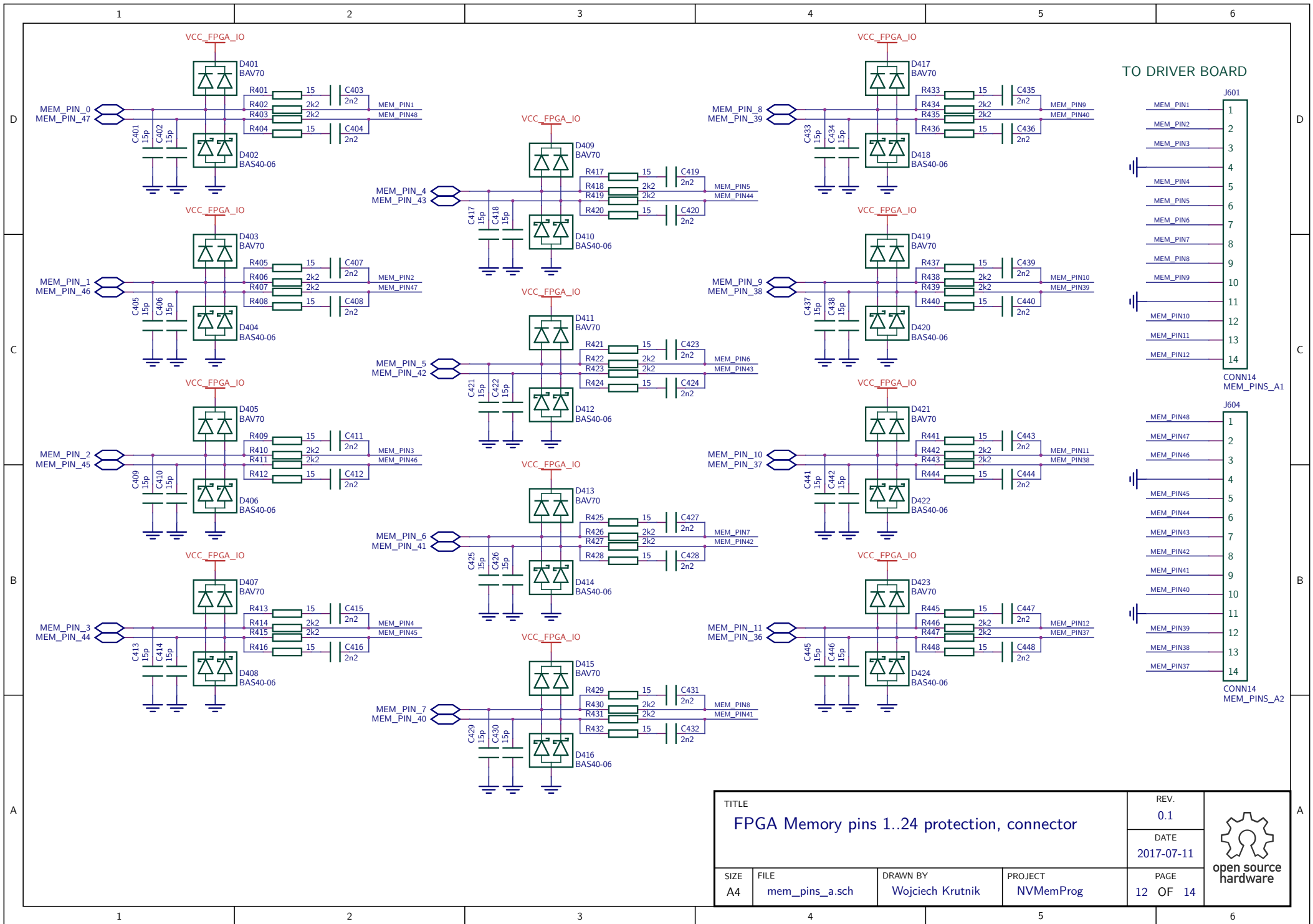


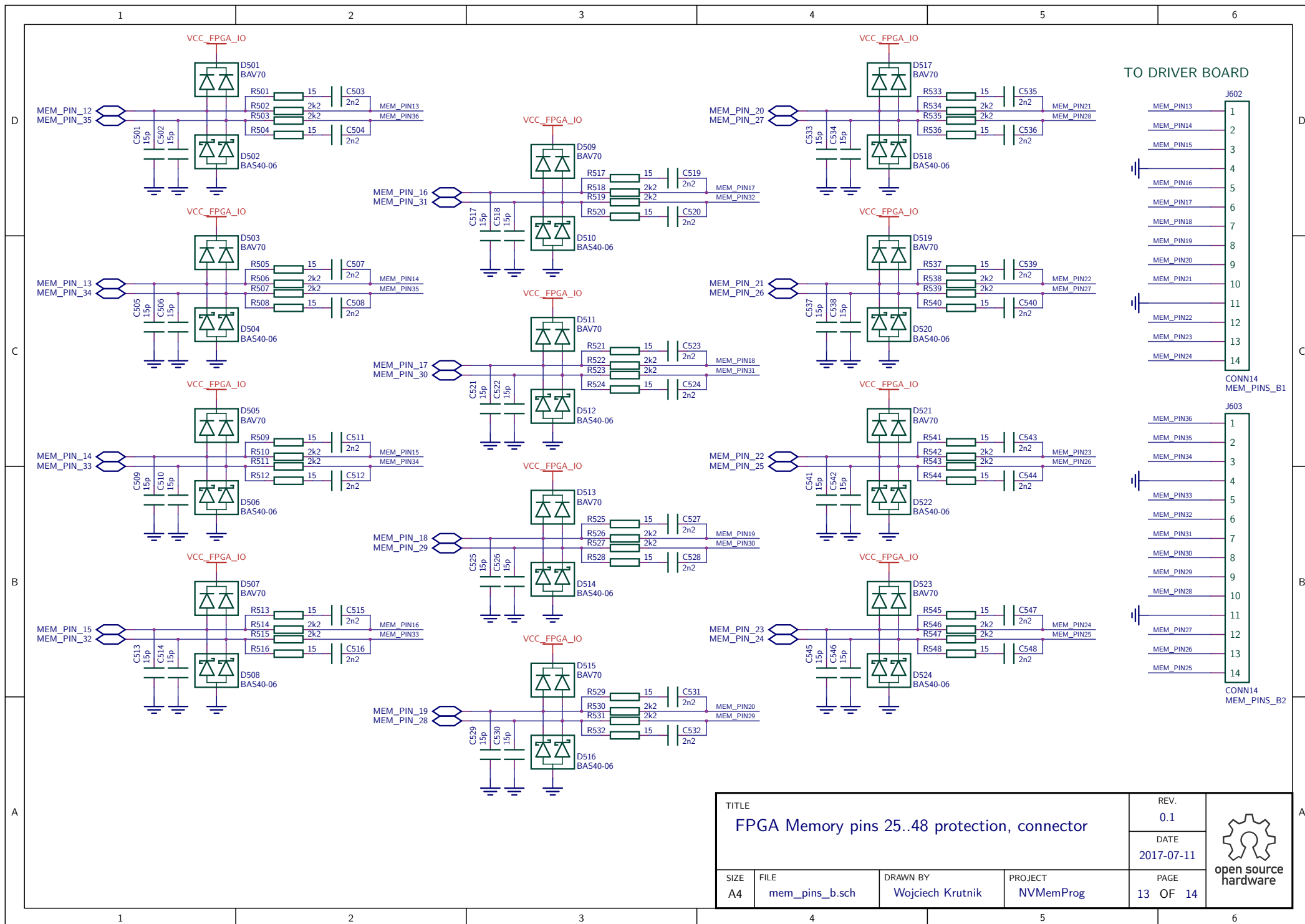
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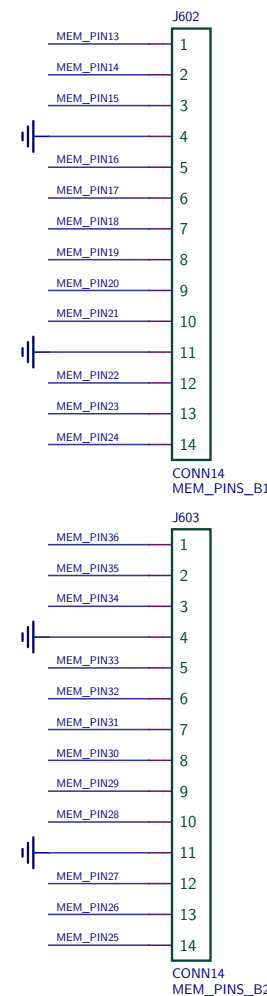
TITLE EZ-USB FX2LP MCU			REV. 0.1	 open source hardware
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			PROJECT NVMemProg	
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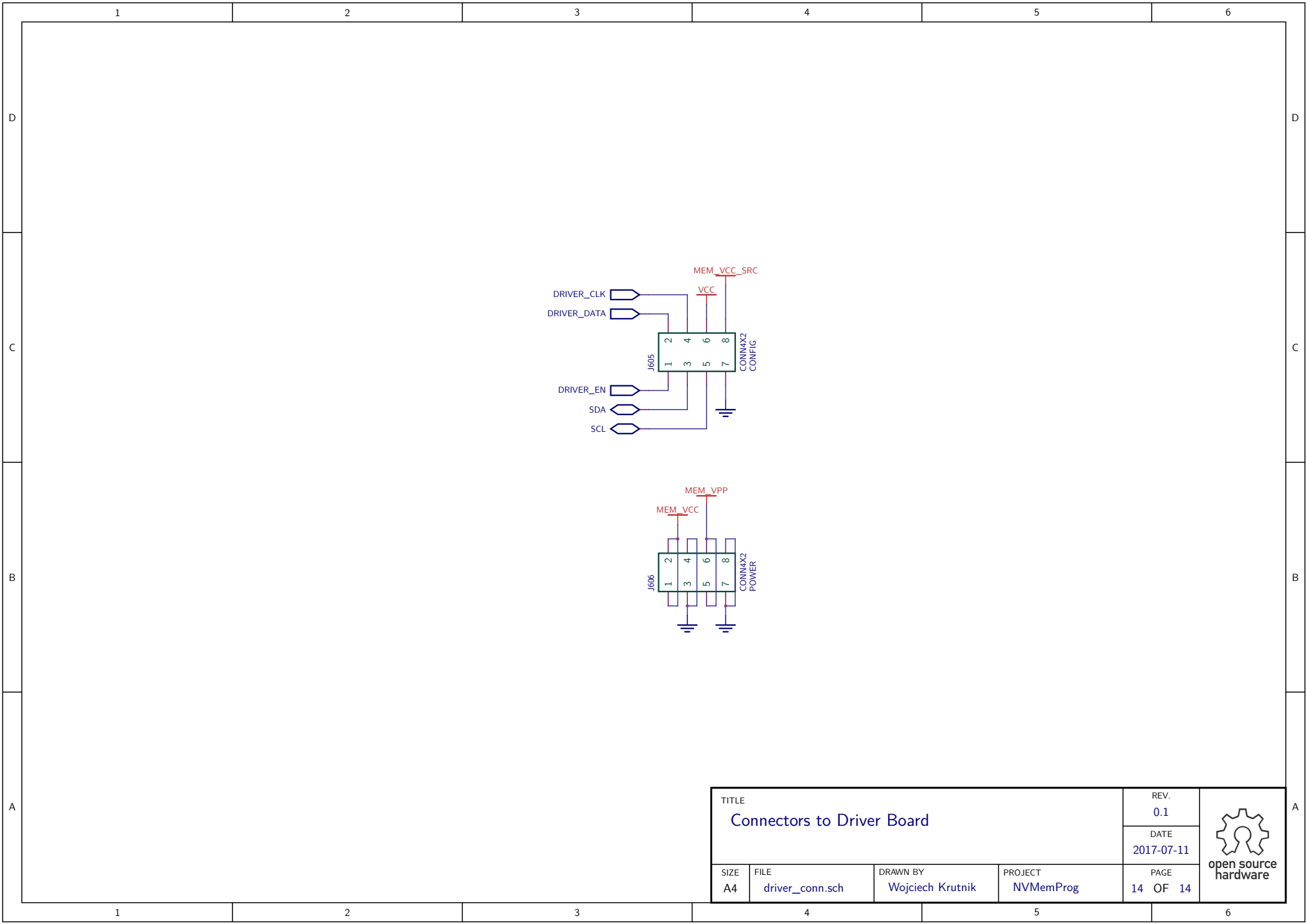





# TO DRIVER BOARD



TITLE FPGA Memory pins 25..48 protection, connector				REV. 0.1	
				DATE 2017-07-11	
SIZE A4	FILE mem_pins_b.sch	DRAWN BY Wojciech Krutnik	PROJECT NVMemProg	PAGE 13 OF 14	



TITLE Connectors to Driver Board				REV. 0.1	 open source hardware
				DATE 2017-07-11	
SIZE A4	FILE driver_conn.sch	DRAWN BY Wojciech Krutnik	PROJECT NVMemProg	PAGE 14 OF 14	