

NVMemProg

Non-Volatile Memory Programmer

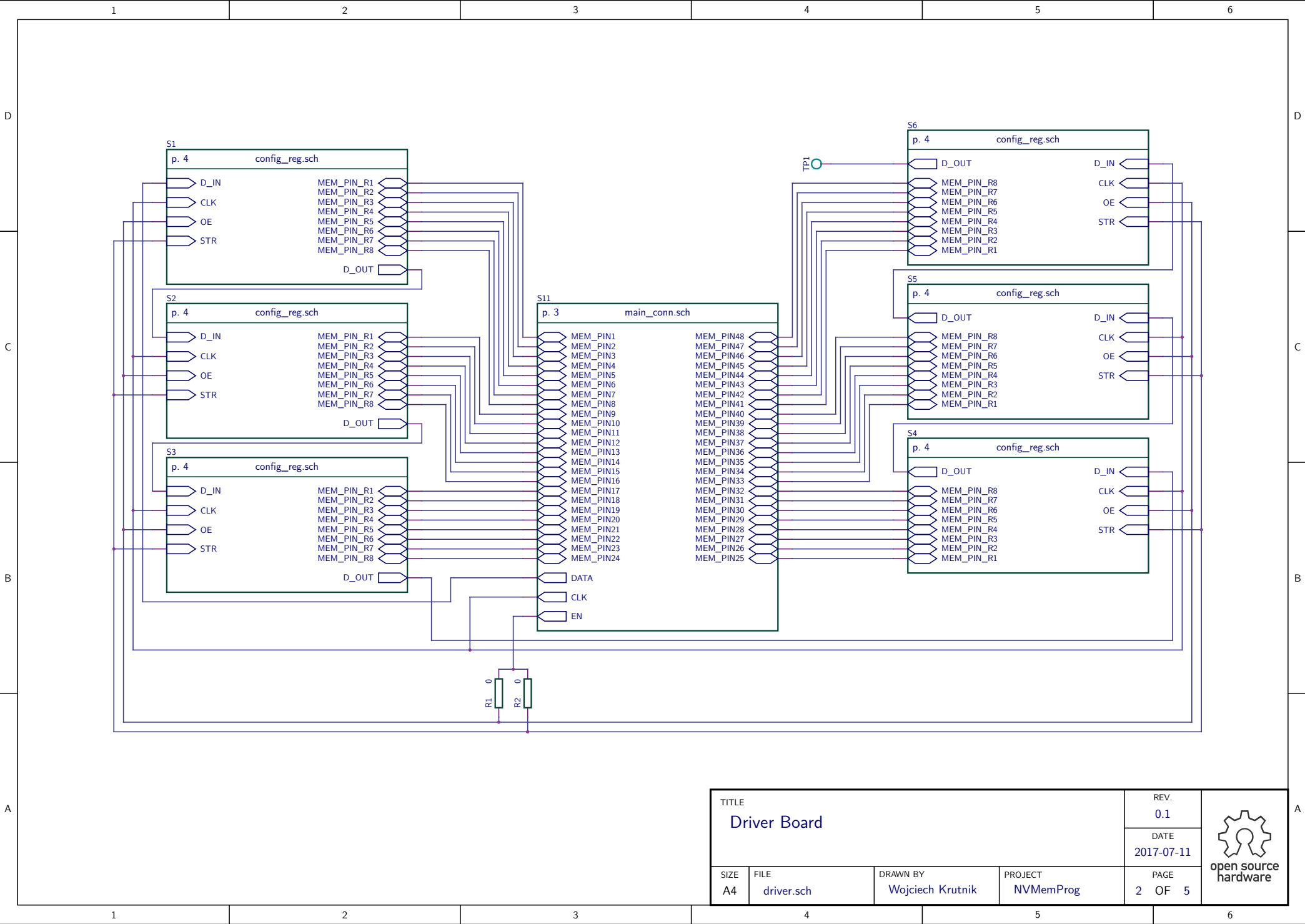
Driver Board Schematics


2017-07-11
rev. 0.1

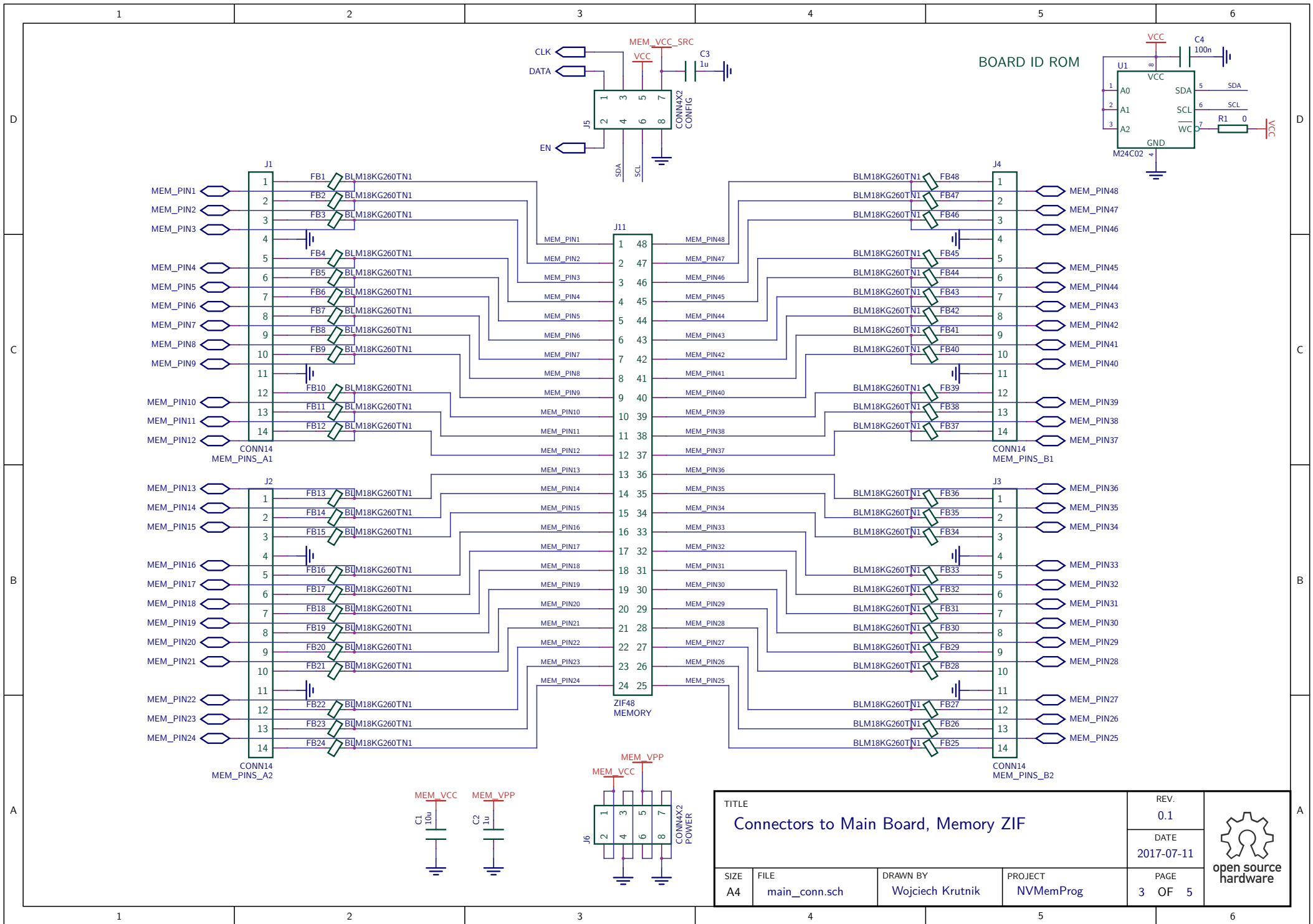
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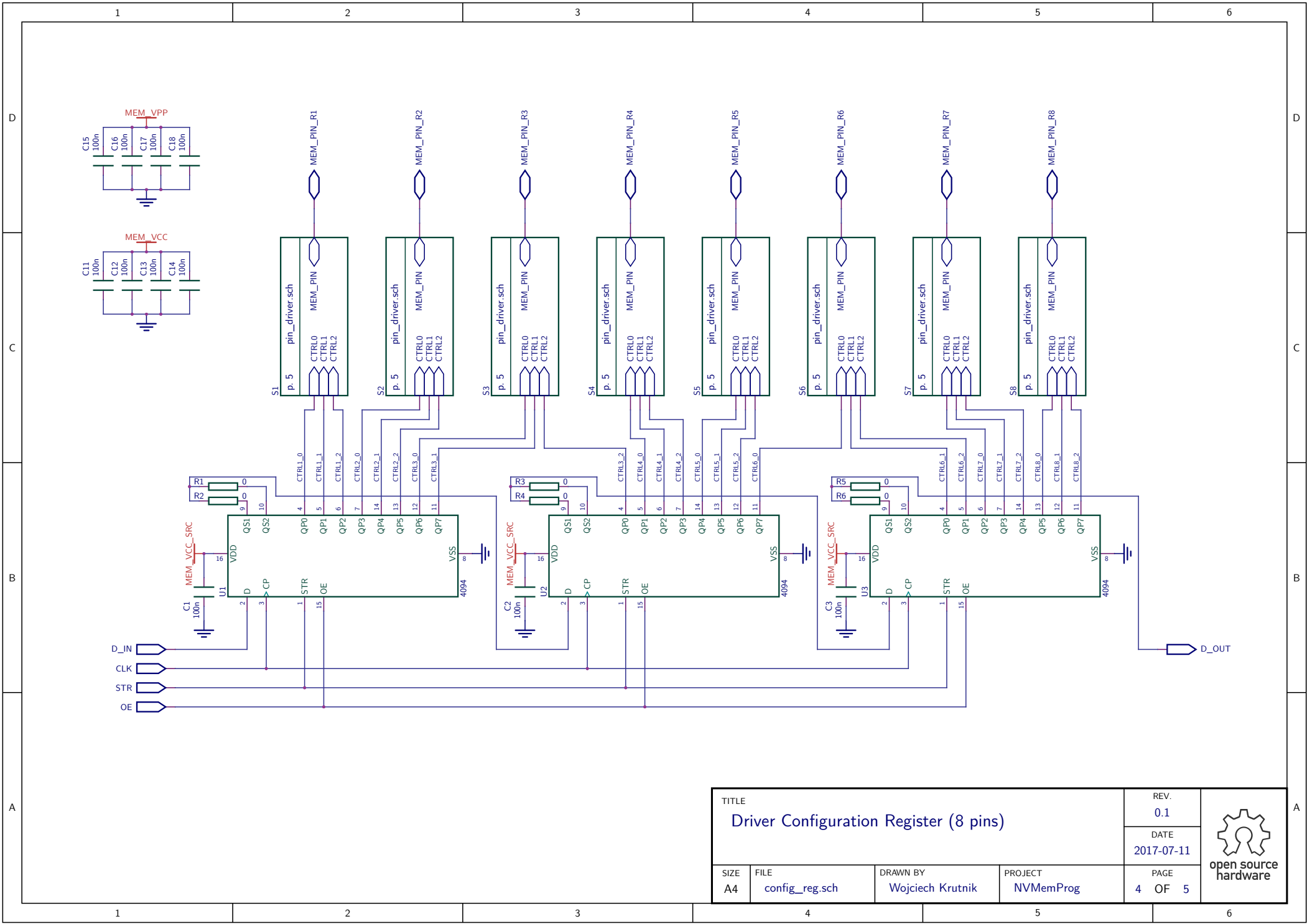
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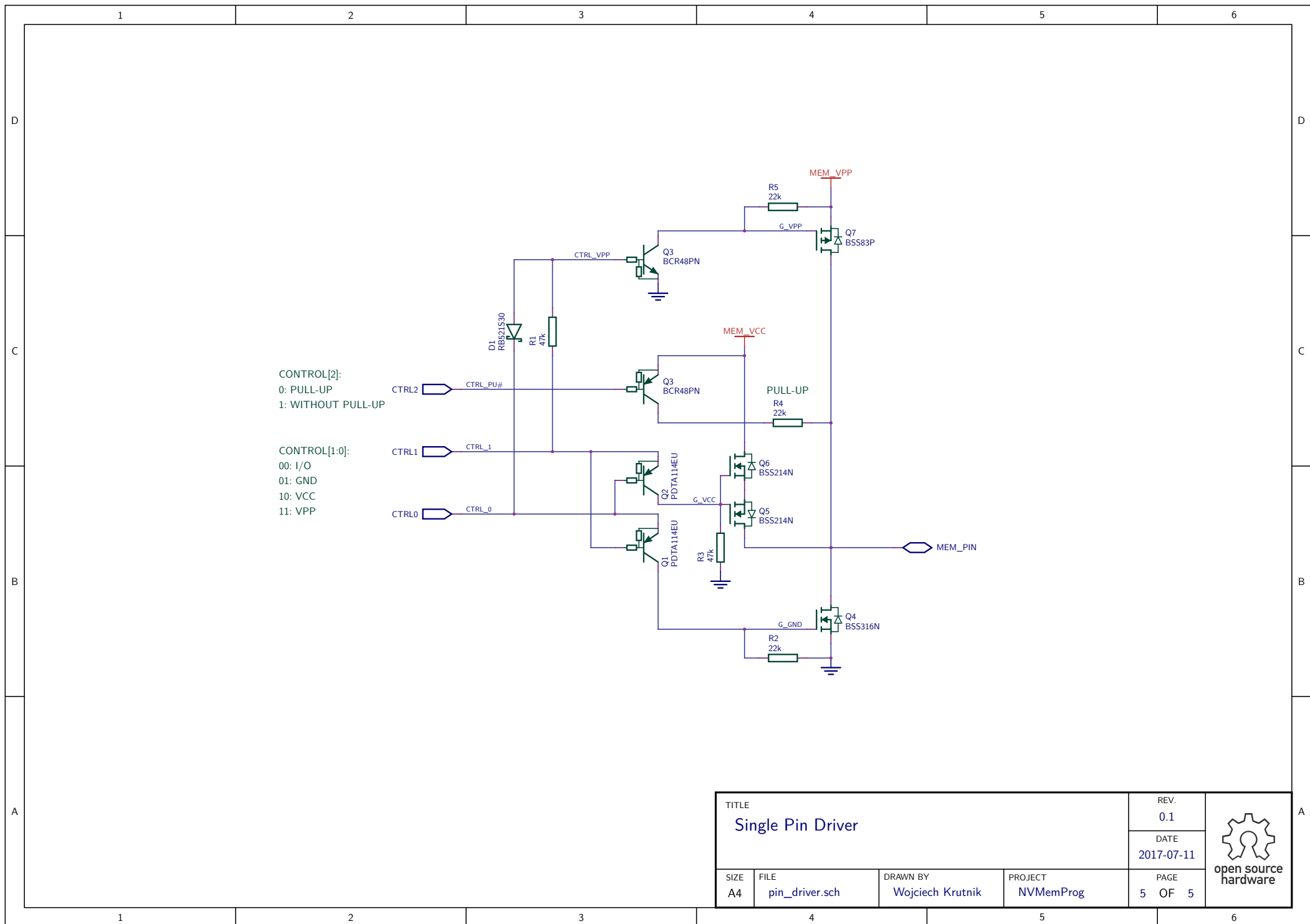
TITLE Driver Board				REV. 0.1	 open source hardware
				DATE 2017-07-11	
SIZE A4	FILE driver.sch	DRAWN BY Wojciech Krutnik	PROJECT NVMemProg	PAGE 2 OF 5	



TITLE				REV.	 open source hardware
Connectors to Main Board, Memory ZIF				0.1	
				DATE	
				2017-07-11	
SIZE	FILE	DRAWN BY	PROJECT	PAGE	
A4	main_conn.sch	Wojciech Krutnik	NVMemProg	3 OF 5	



TITLE Driver Configuration Register (8 pins)			REV. 0.1	 open source hardware
			DATE 2017-07-11	
SIZE A4	FILE config_reg.sch	DRAWN BY Wojciech Krutnik	PROJECT NVMemProg	PAGE 4 OF 5



<div>TITLE</div> <div>Single Pin Driver</div>				REV.	<div>0.1</div> <div>DATE</div> <div>2017-07-11</div>
				SIZE	
A4	FILE	pin_driver.sch	DRAWN BY	Wojciech Krutnik	PROJECT
					NVMemProg
				PAGE	<div>5 OF 5</div> <div>open source hardware</div>