a) random\_test instantiates env\_h

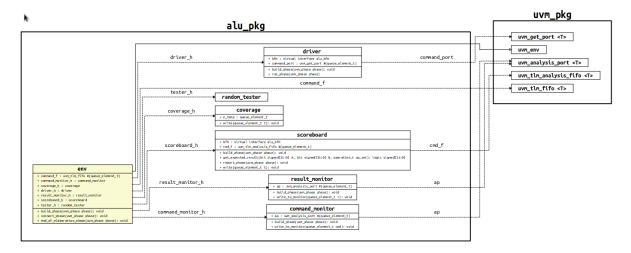
env\_h instantiates: randome\_tester\_h, uvm\_tlm\_fifo, driver\_h, coverage\_h, scoreboard\_h, command\_monitor\_h, result\_monitor\_h

scoreboard hinstatiate uvm tlm analysis fifo

- b) uvm tlm fifo, uvm tlm analysis fifo
- c) random\_tester\_h, driver\_h, coverage\_h, scoreboard\_h, command\_monitor\_h, result\_monitor\_h
- d) driver, command monitor, result monitor
- e) command monitor, result monitor
- f) driver
- g) random\_tester
- h) uvm\_blocking\_put\_port, uvm\_nonblocking\_put\_port, uvm\_put\_port, uvm\_blocking\_get\_port, uvm\_nonblocking\_get\_port, uvm\_blocking\_peek\_port, uvm\_nonblocking\_peek\_port, uvm\_peek\_port, uvm\_blocking\_get\_peek\_port, uvm\_nonblocking\_get\_peek\_port, uvm\_get\_peek\_port, uvm\_port\_base
- i) command\_monitor, coverage, result\_monitor, scoreboard

## **UVM** structure:

## **UML** diagram:



## **UVM block diagram:**

